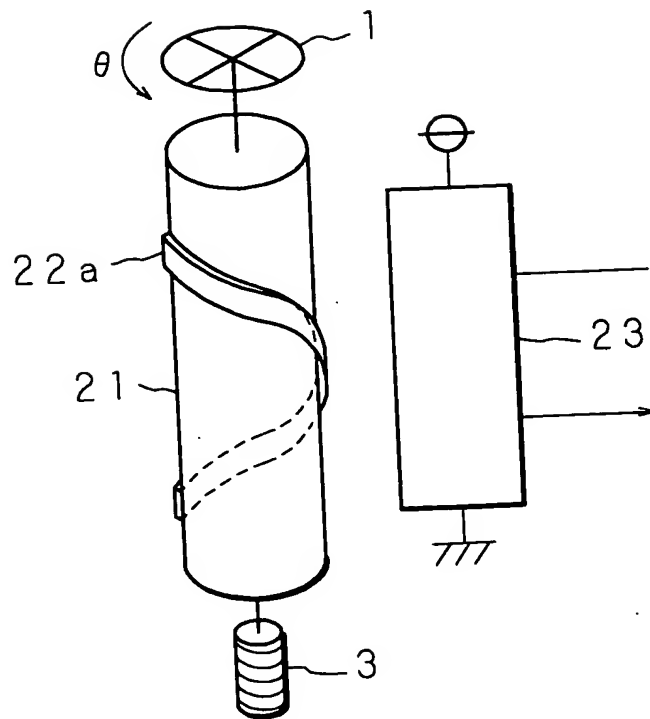


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FIG. 1



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FIG. 2

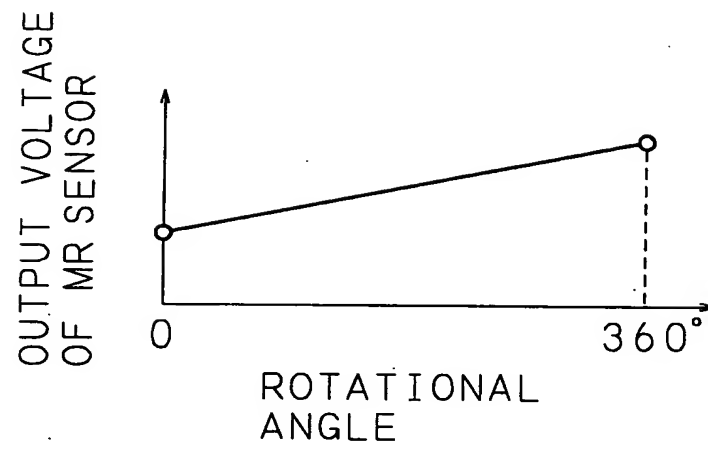
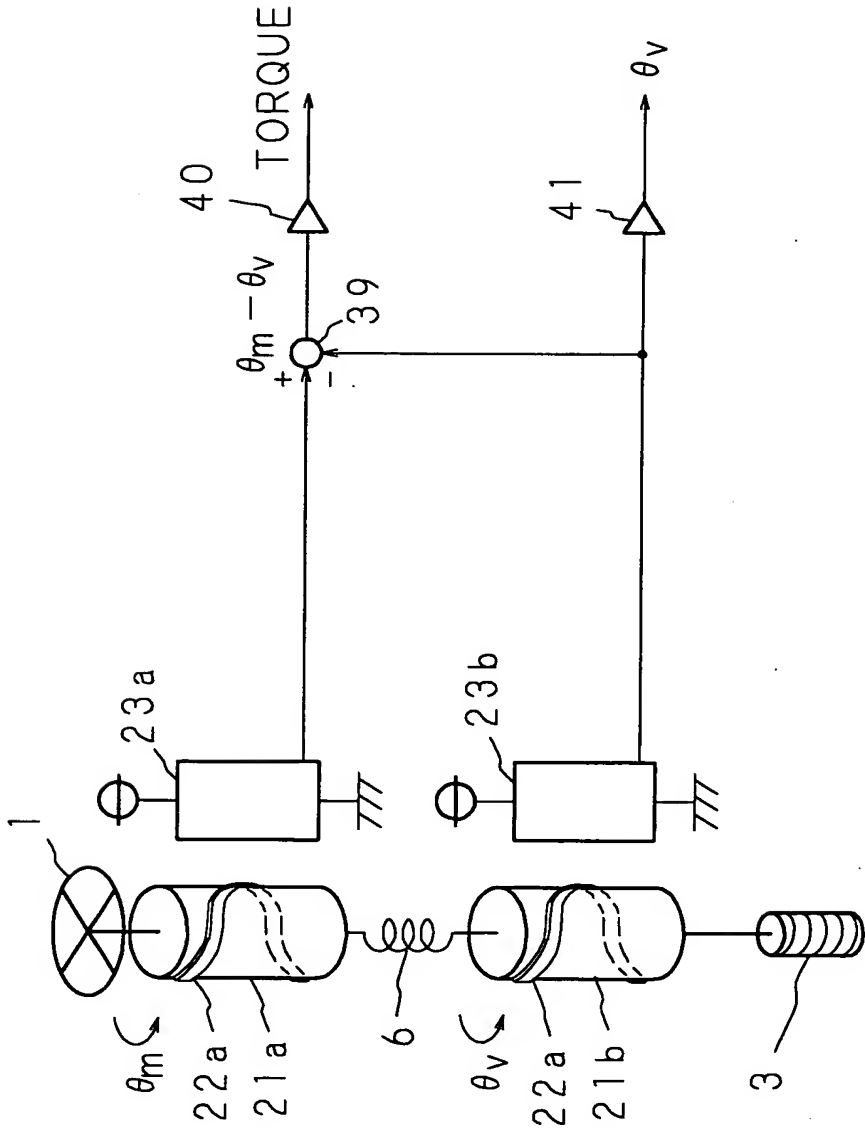


FIG. 3



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FIG. 4A

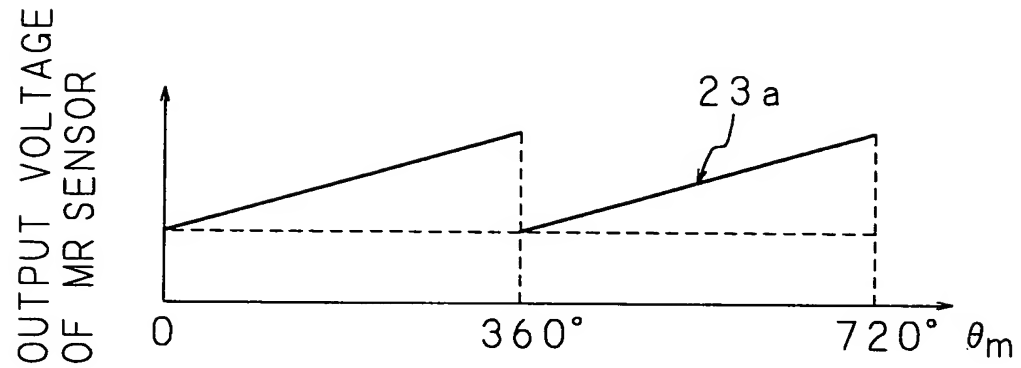


FIG. 4B

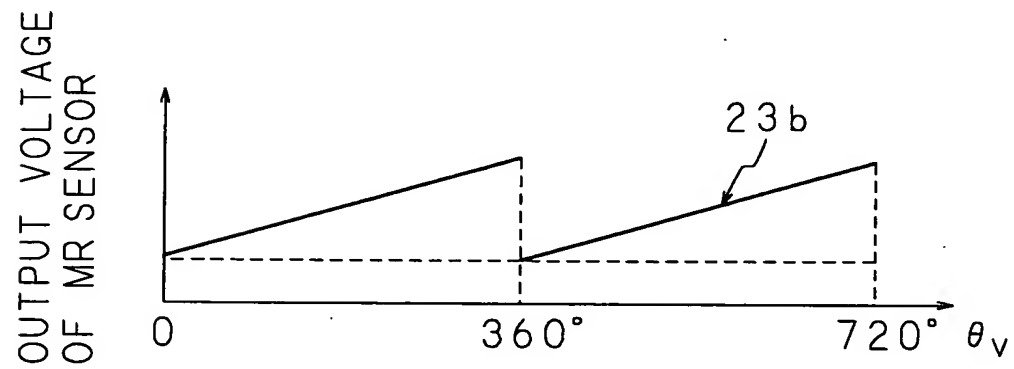
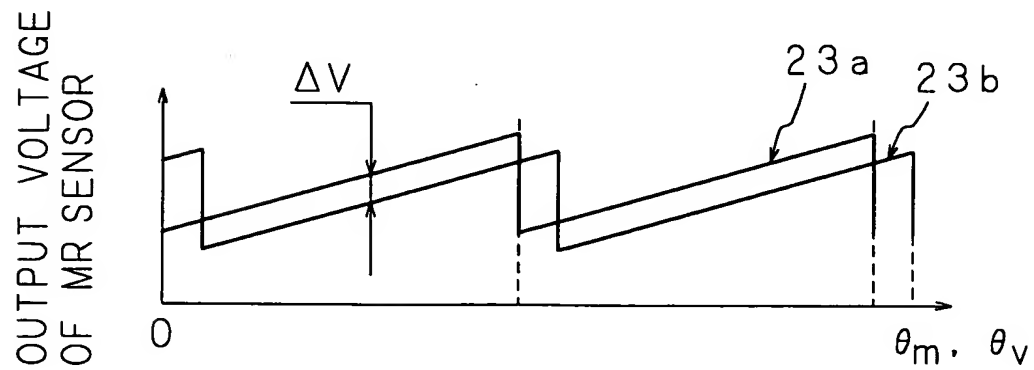


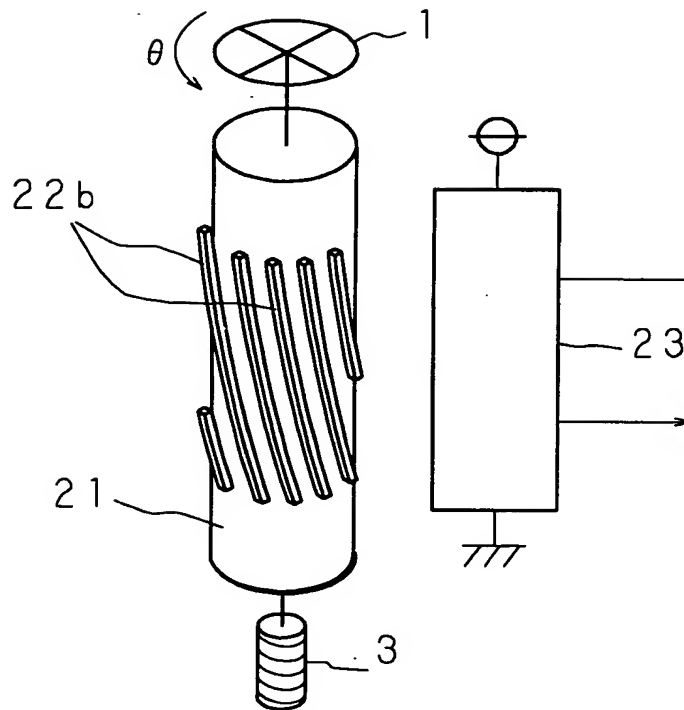
FIG. 4C





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FIG. 5



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FIG. 6A

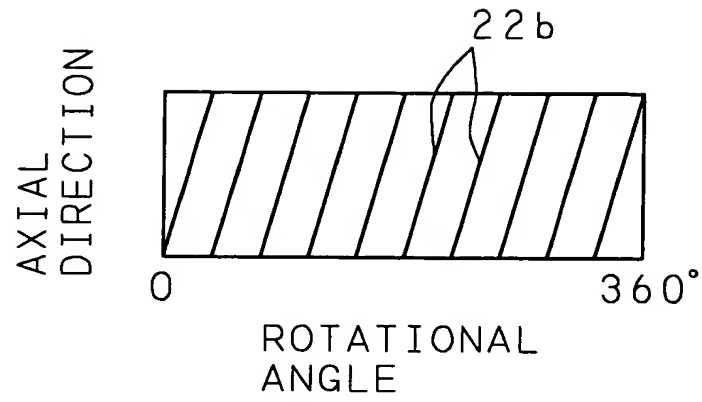
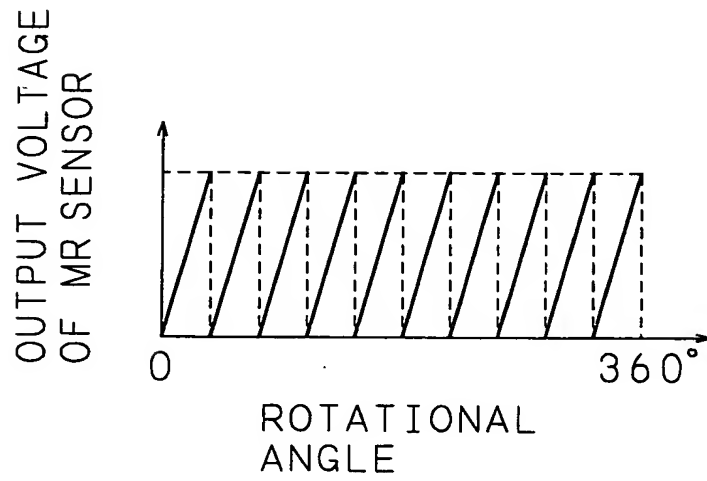


FIG. 6B





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FIG. 8A

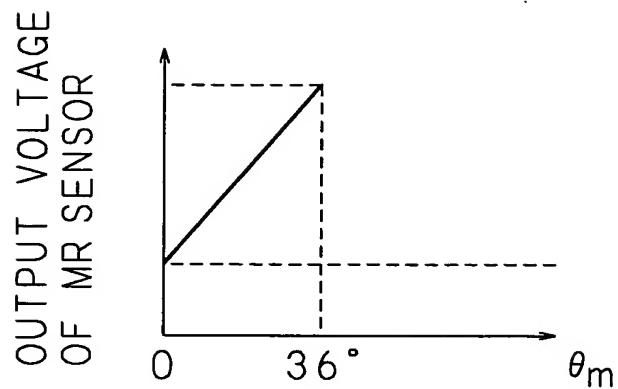


FIG. 8B

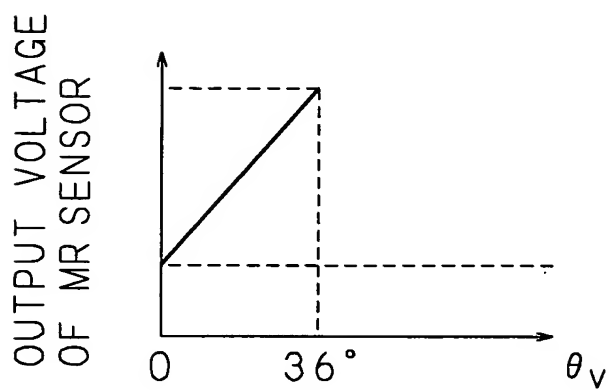
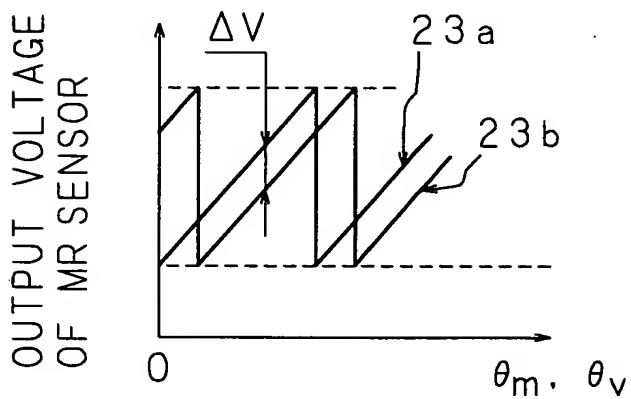
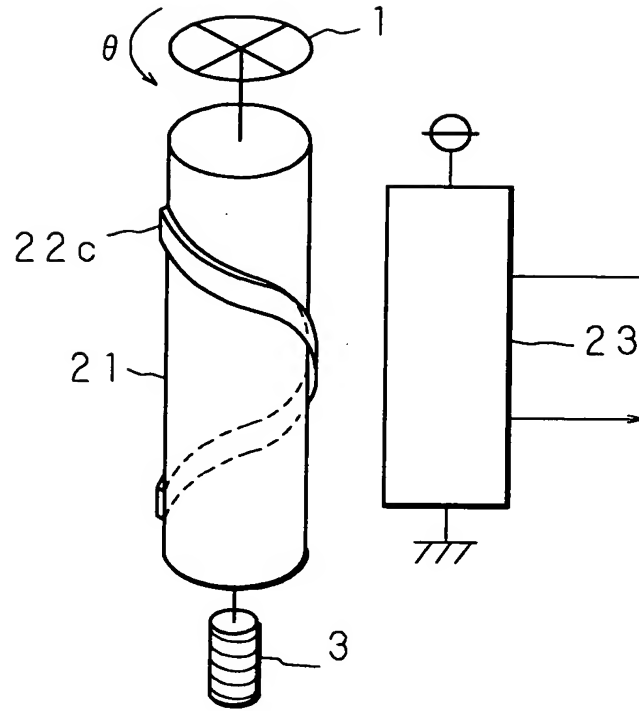


FIG. 8C



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FIG. 9



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FIG. 10A

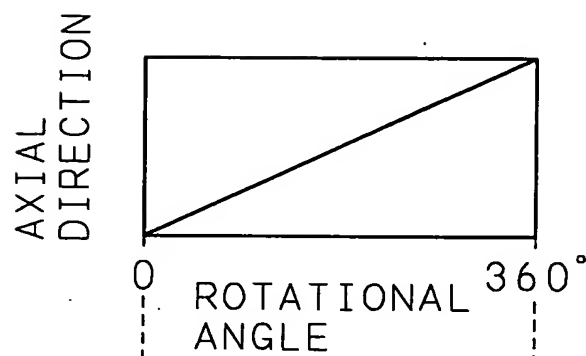
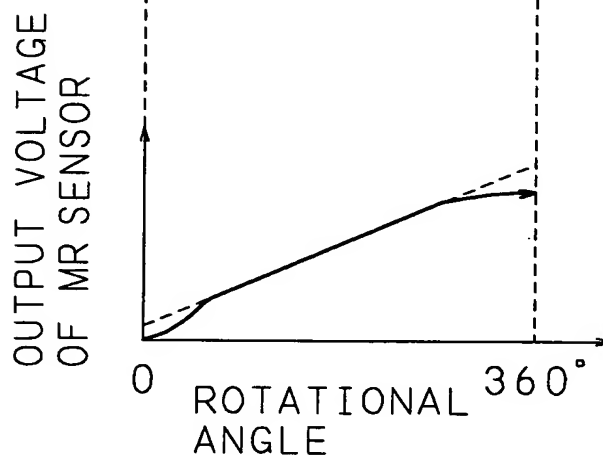


FIG. 10B



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FIG. 11A

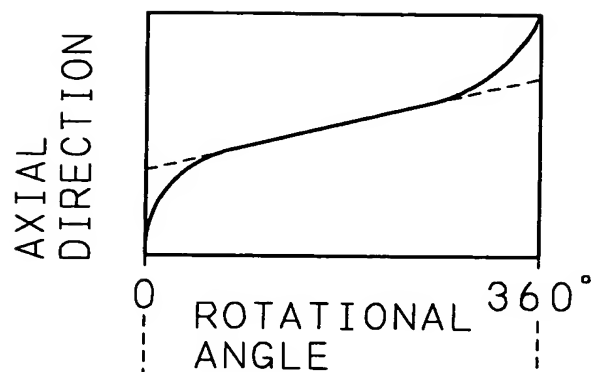


FIG. 11B

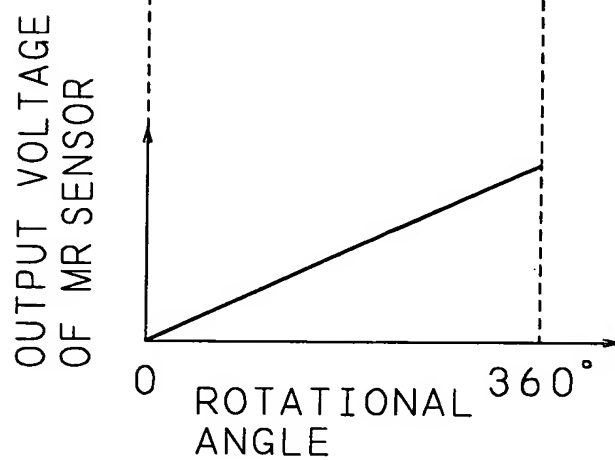
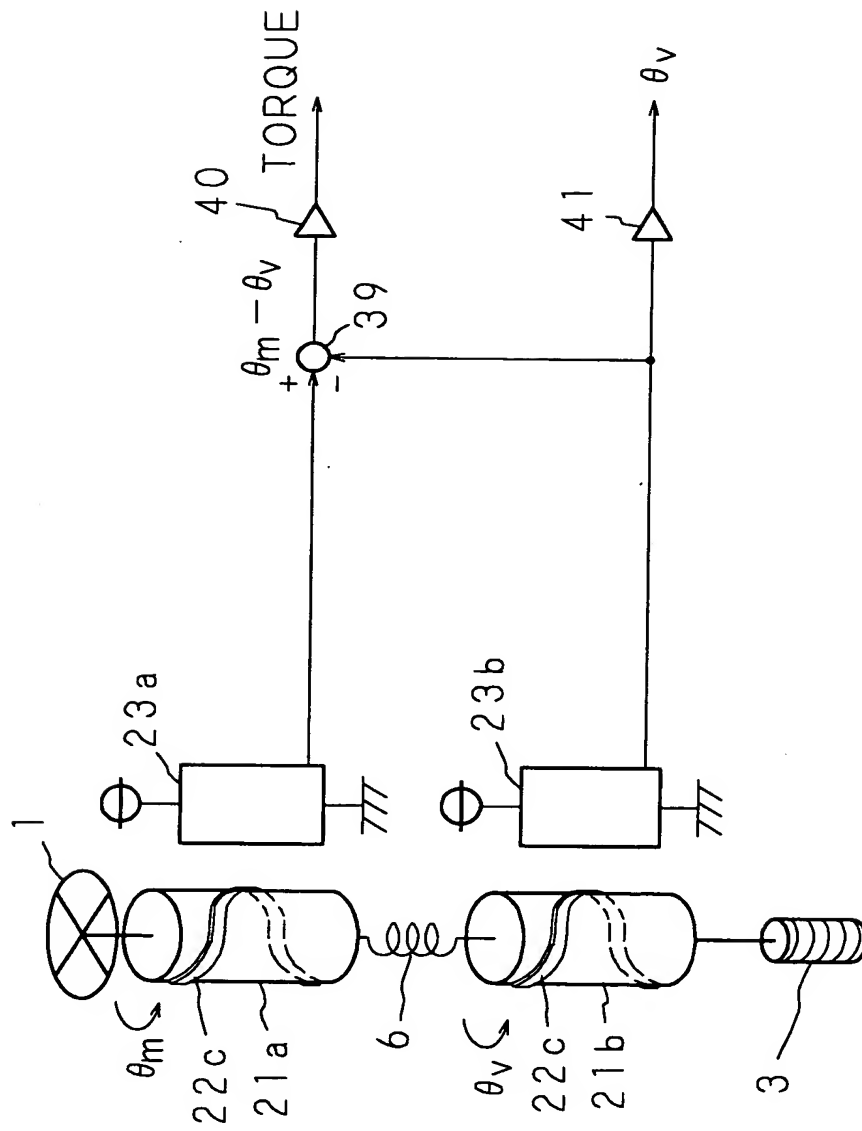


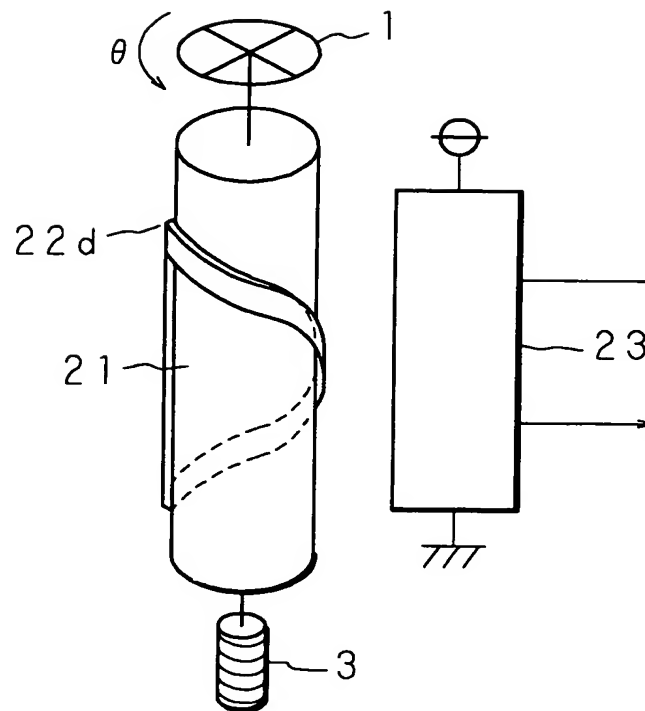
FIG. 12





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FIG. 13



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FIG. 14A

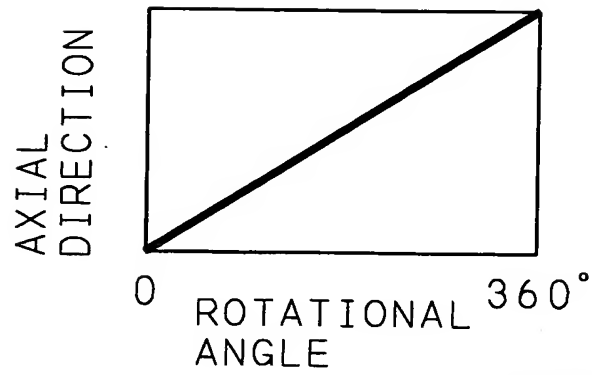
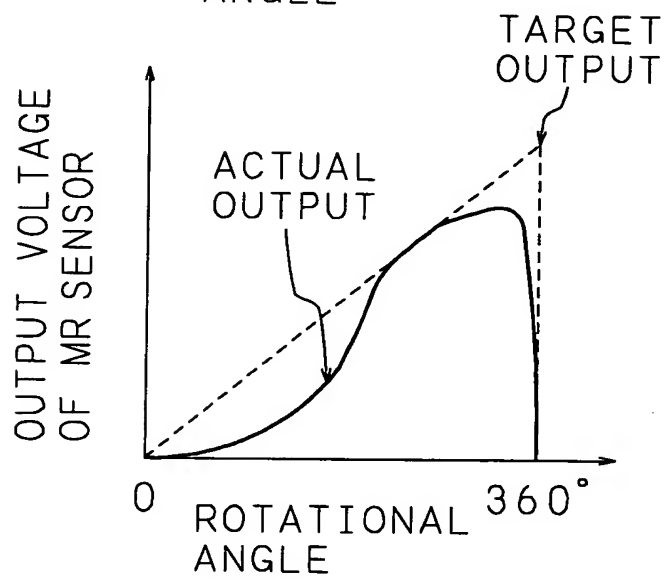


FIG. 14B



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FIG. 15A

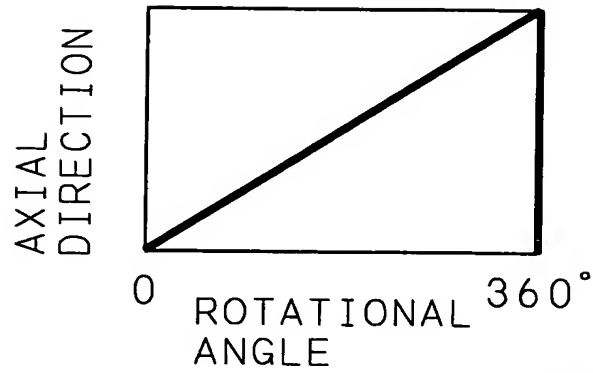
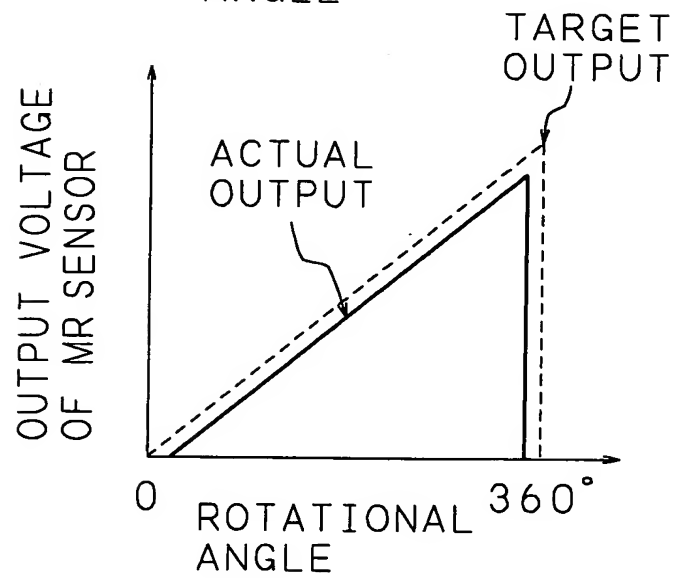
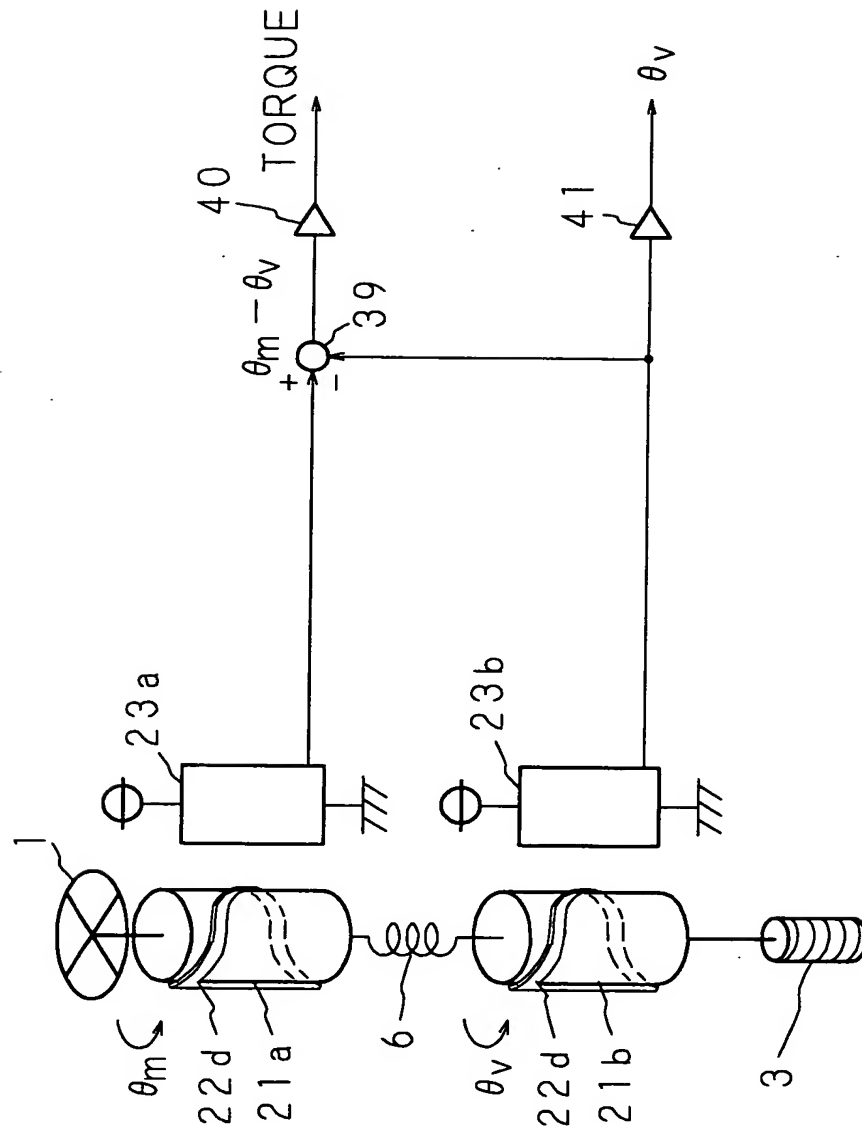


FIG. 15B



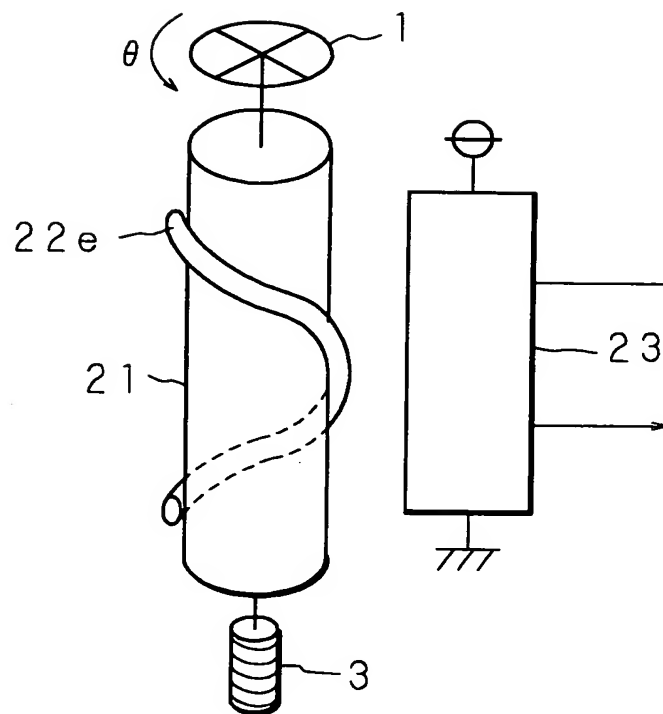
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FIG. 16



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FIG. 17



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FIG. 18A

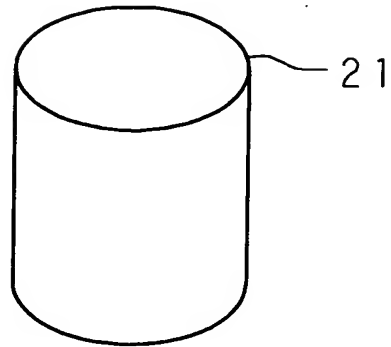


FIG. 18B

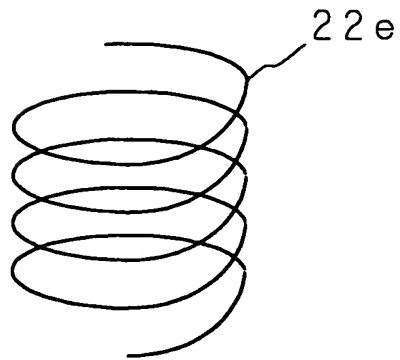
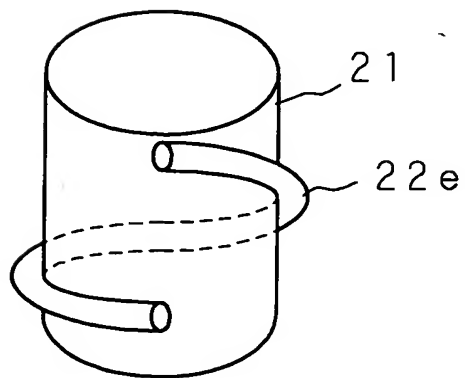


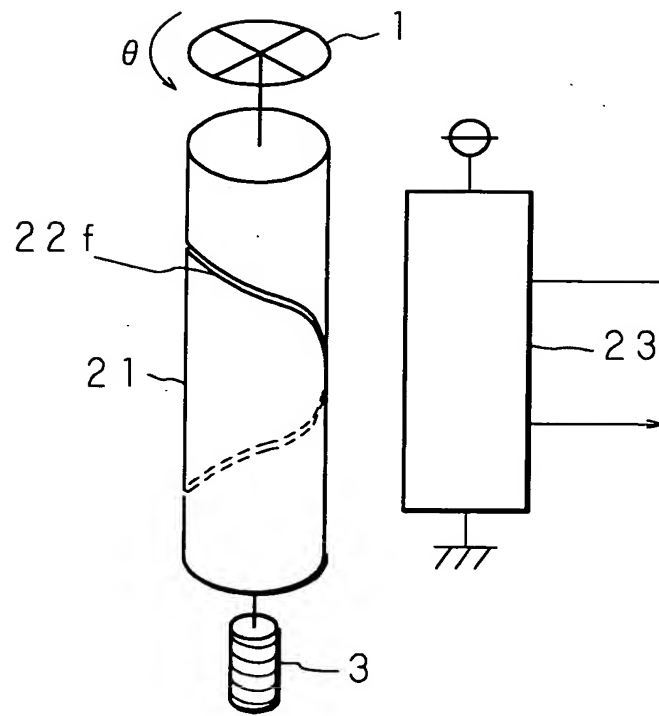
FIG. 18C





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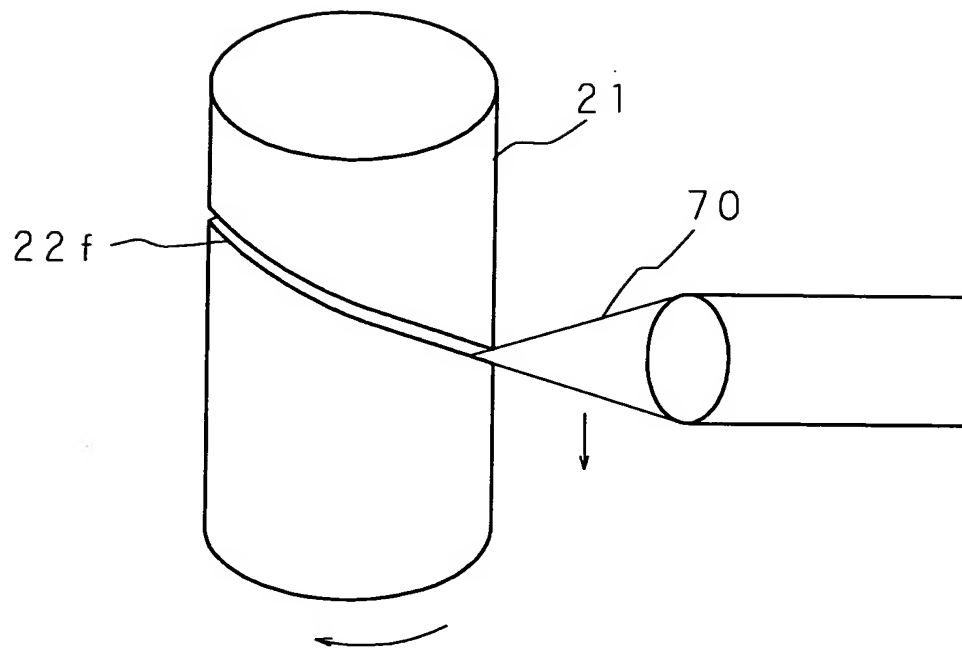
FIG. 20





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FIG. 21



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FIG. 22

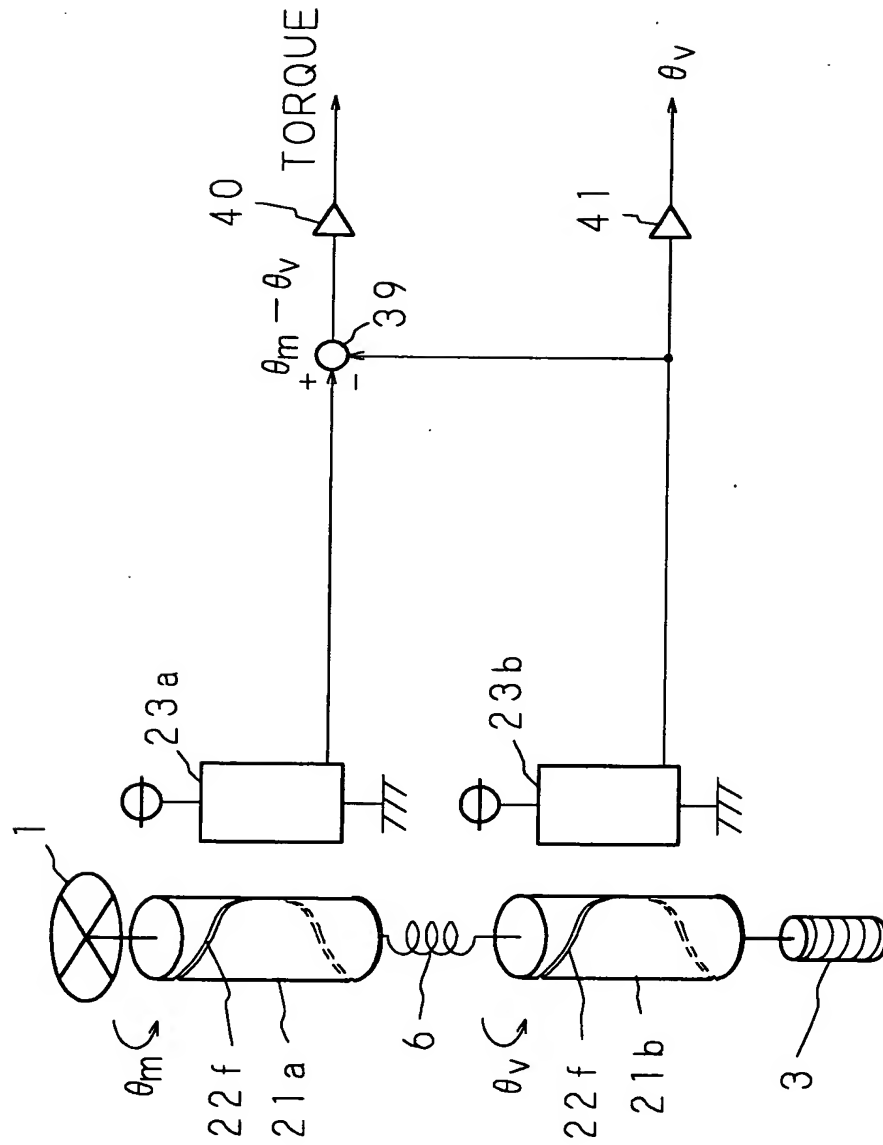
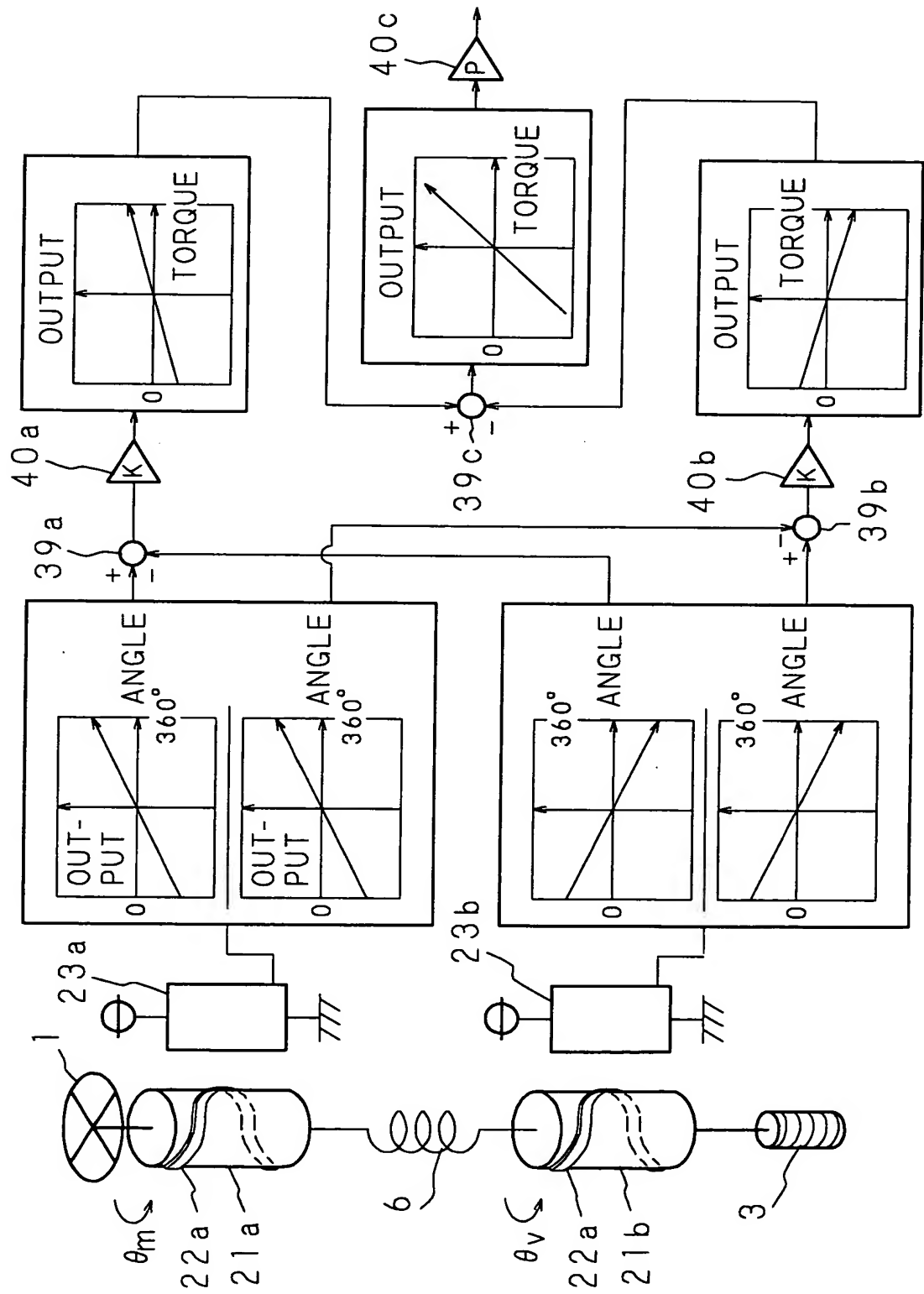
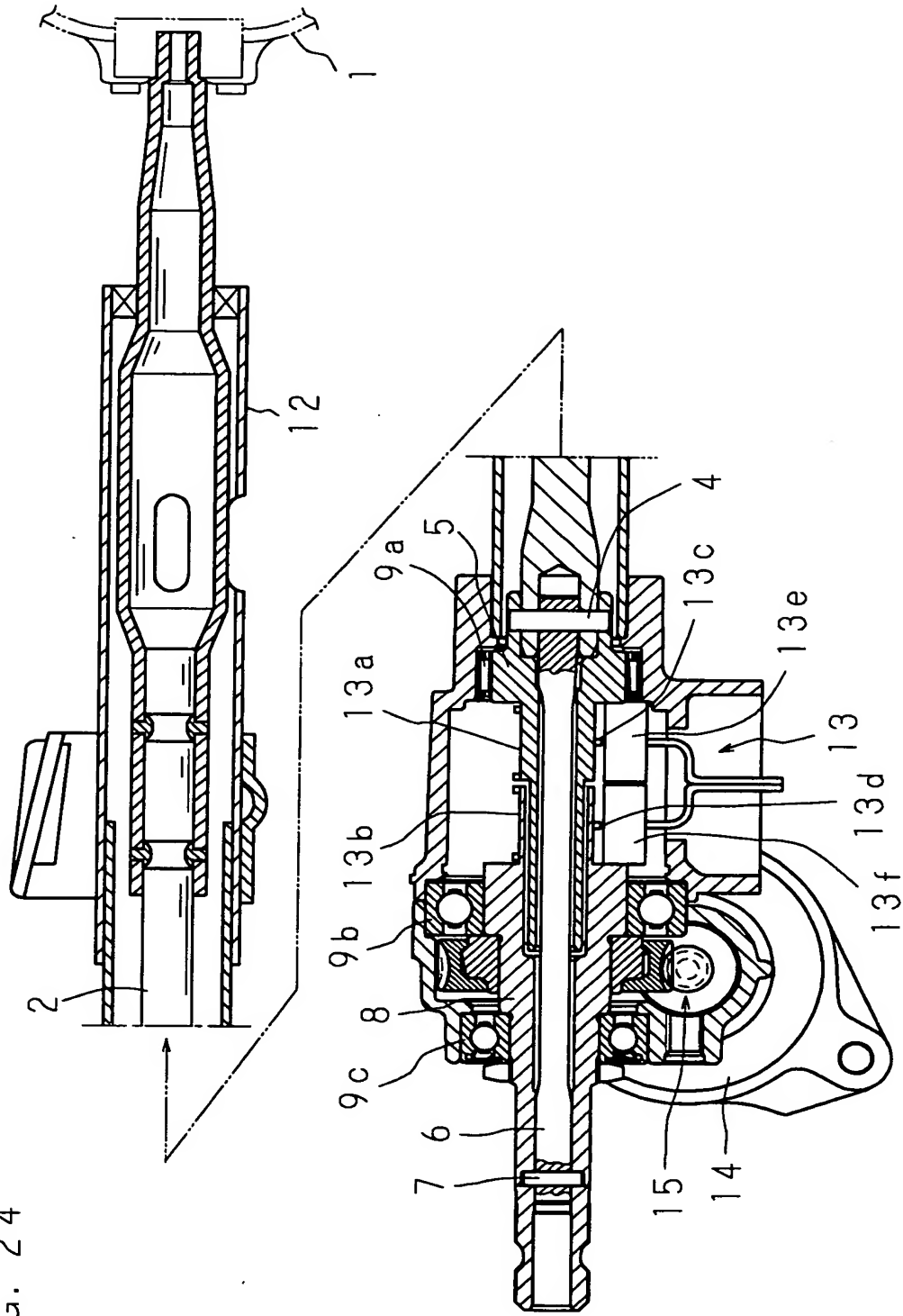


FIG. 23



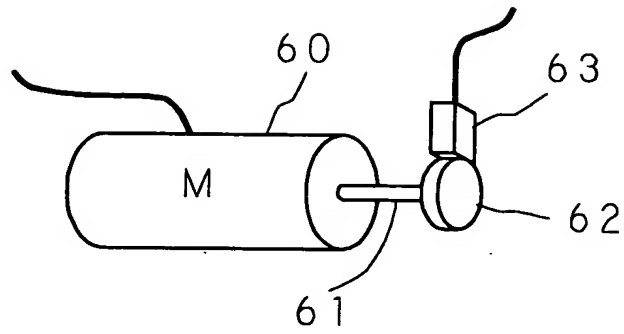
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FIG. 24



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FIG. 25



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FIG. 26A

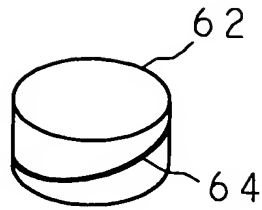


FIG. 26B

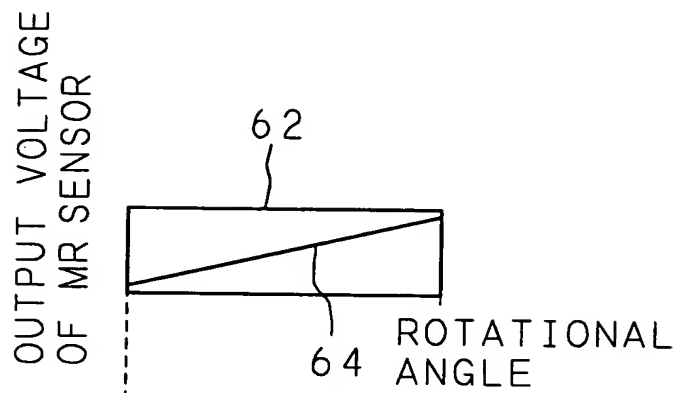
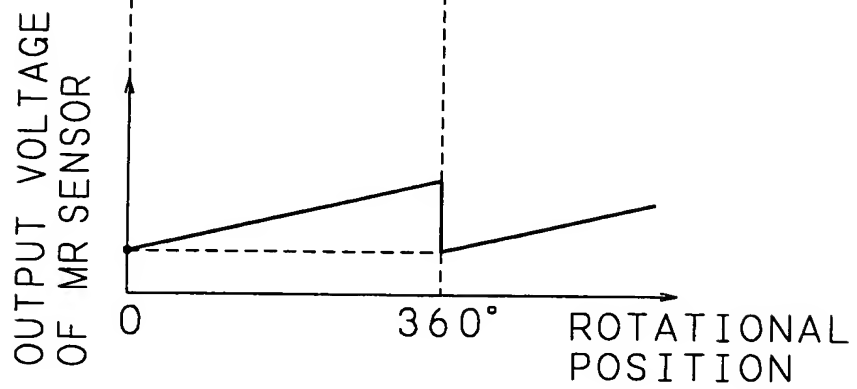


FIG. 26C



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FIG. 27A

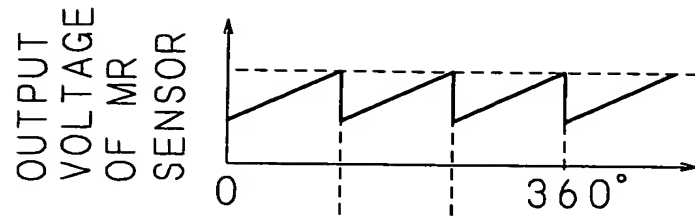


FIG. 27B

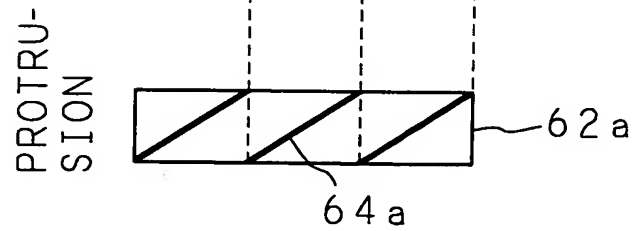


FIG. 27C

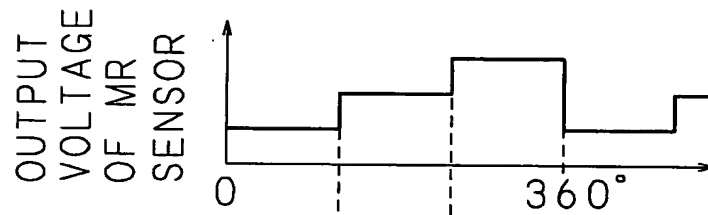


FIG. 27D

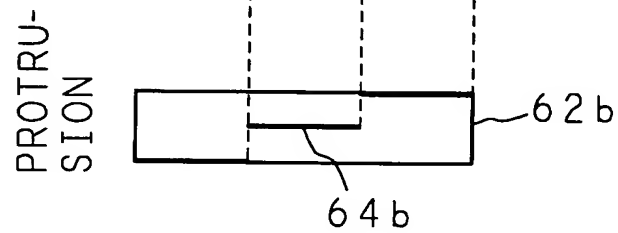


FIG. 27E

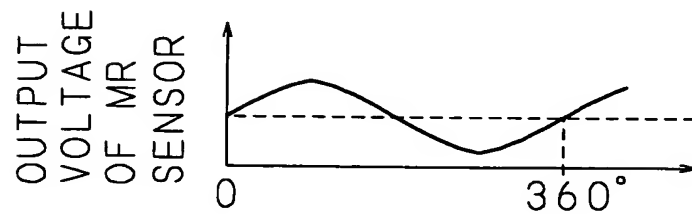


FIG. 27F

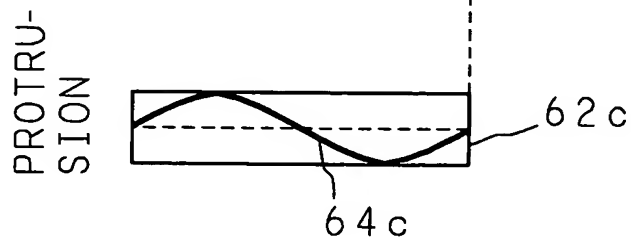


FIG. 28A

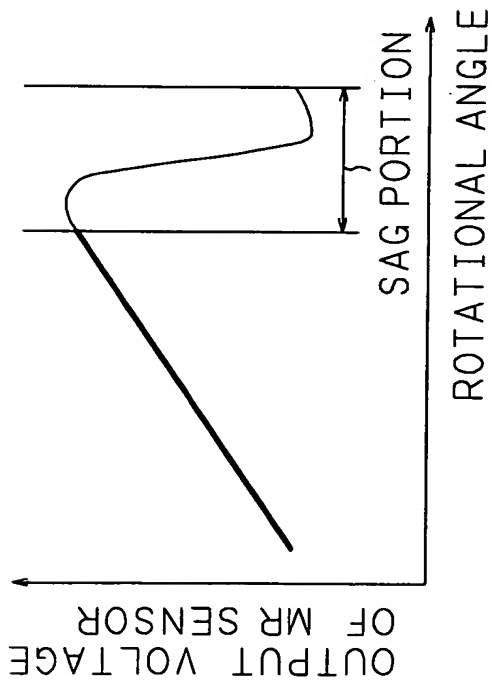


FIG. 28C

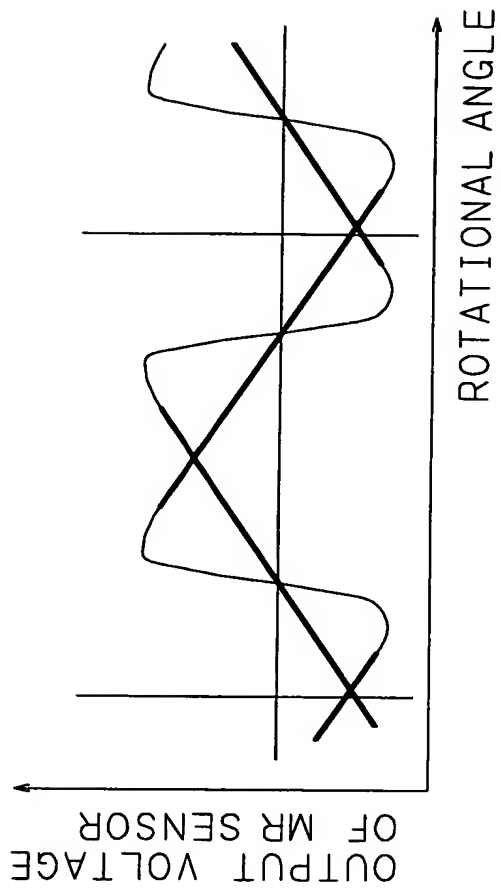
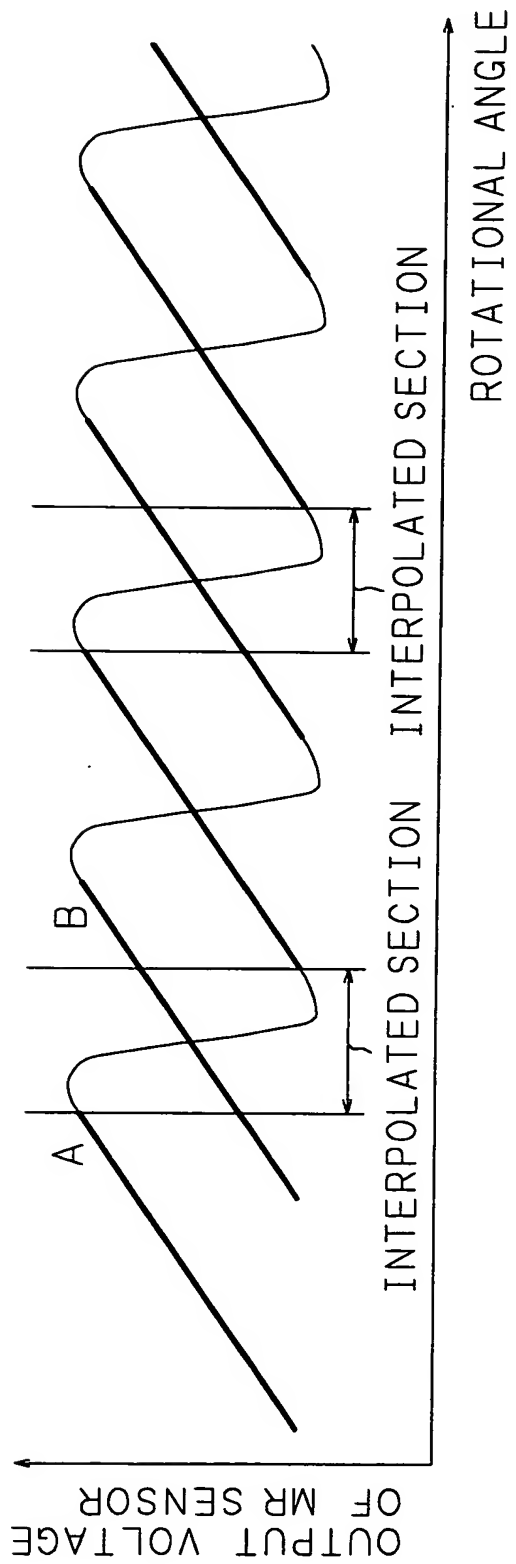


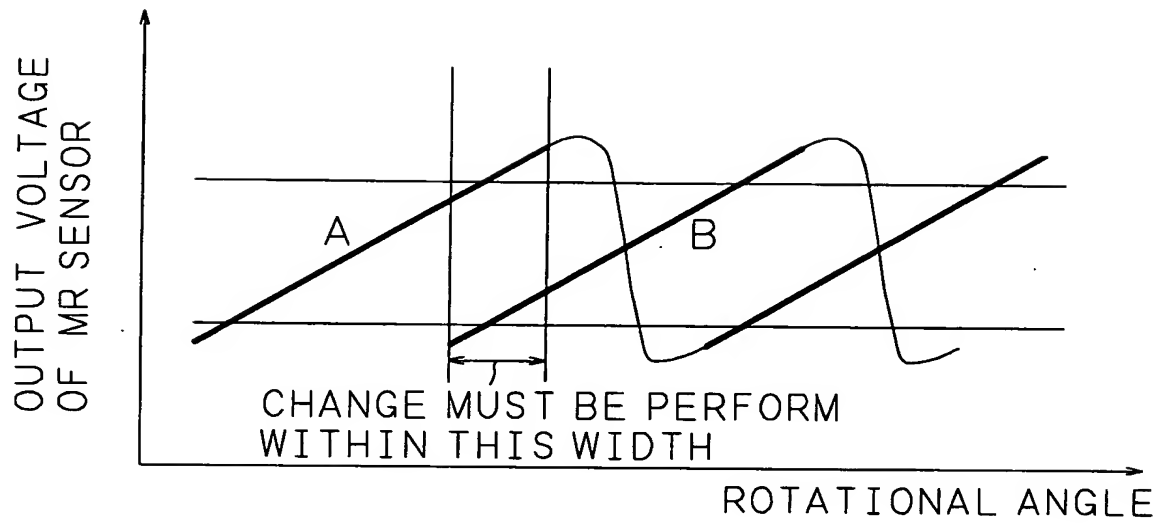
FIG. 28B





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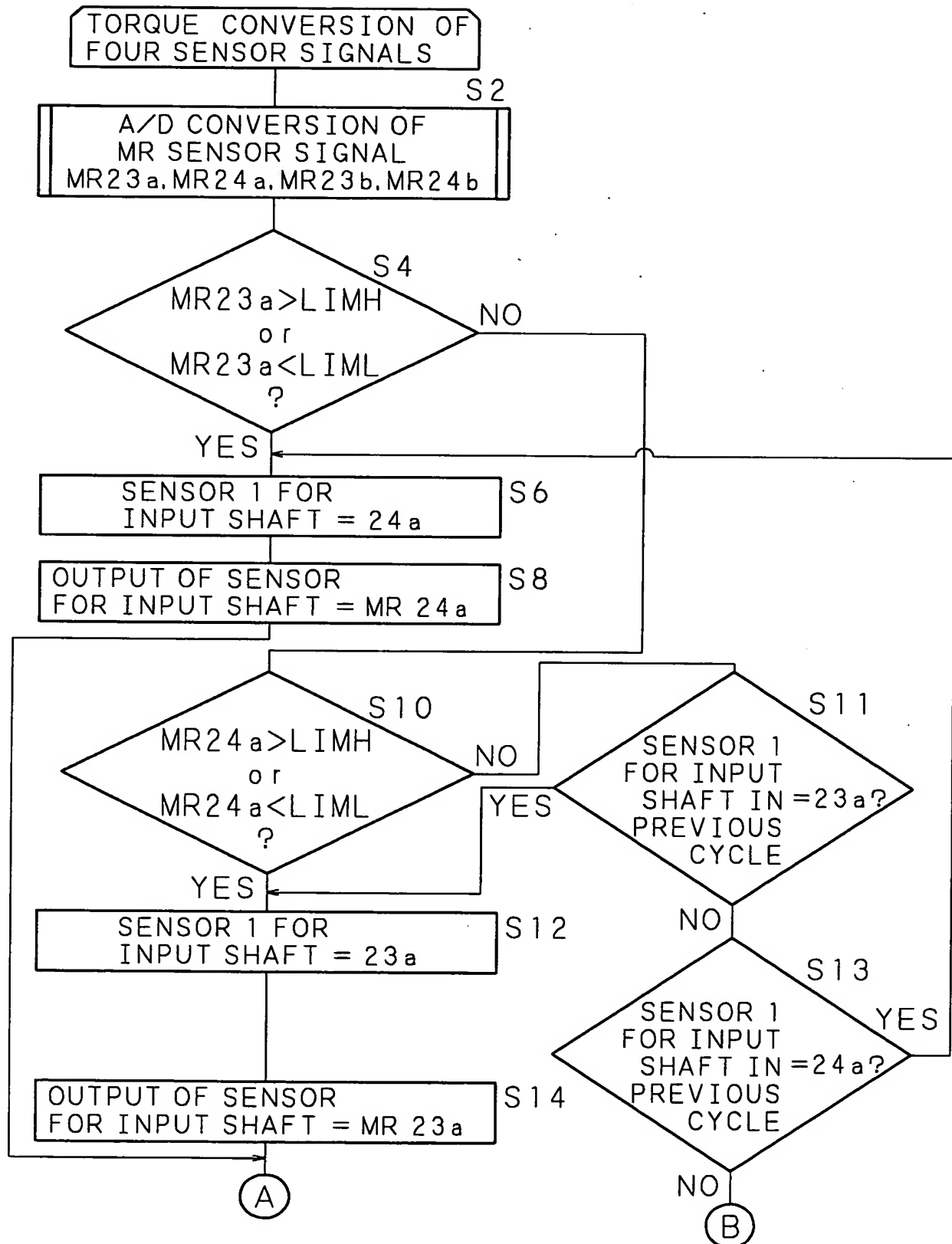
FIG. 29



SIGNAL  
PROCESSING  
UNIT

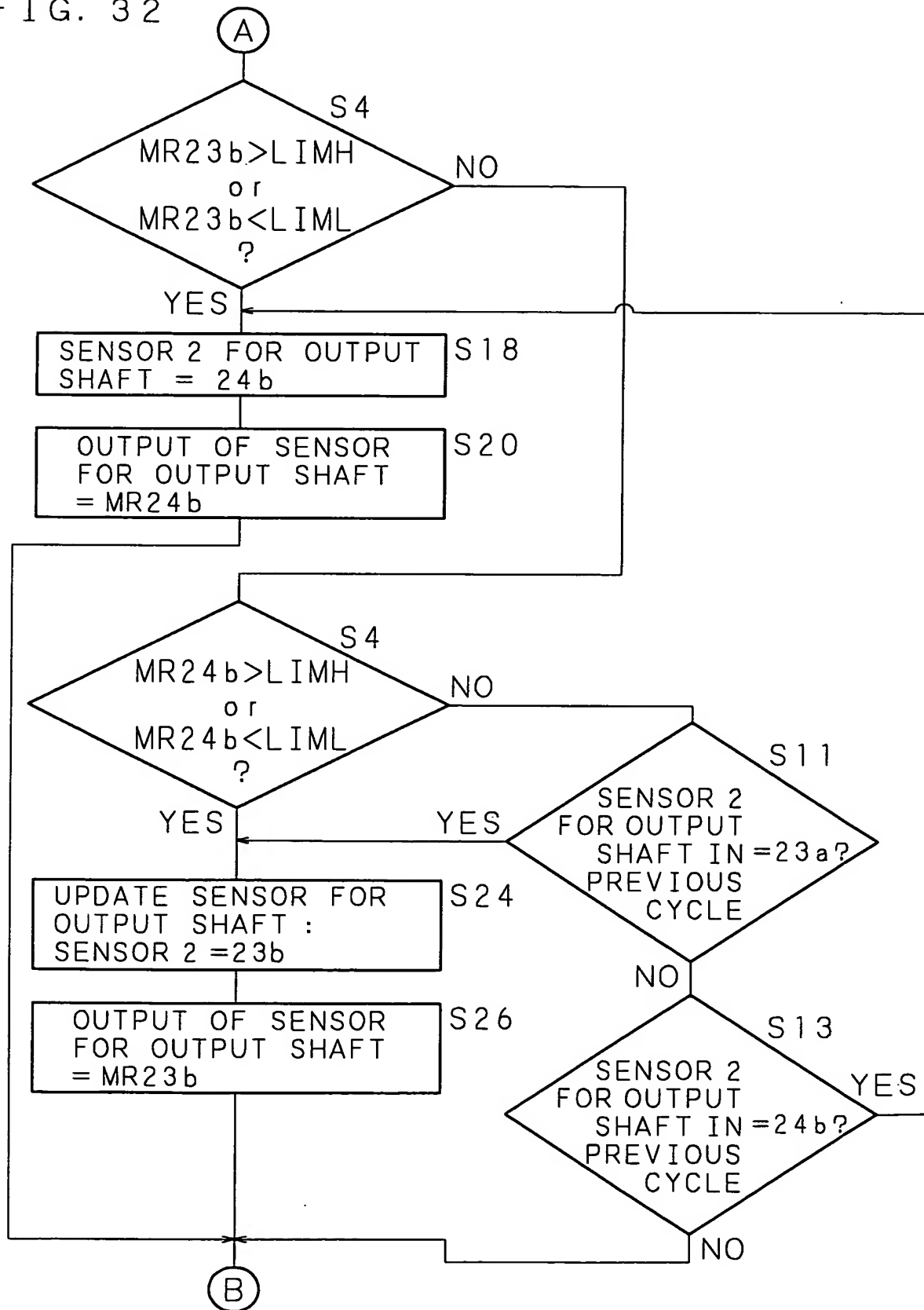
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FIG. 31



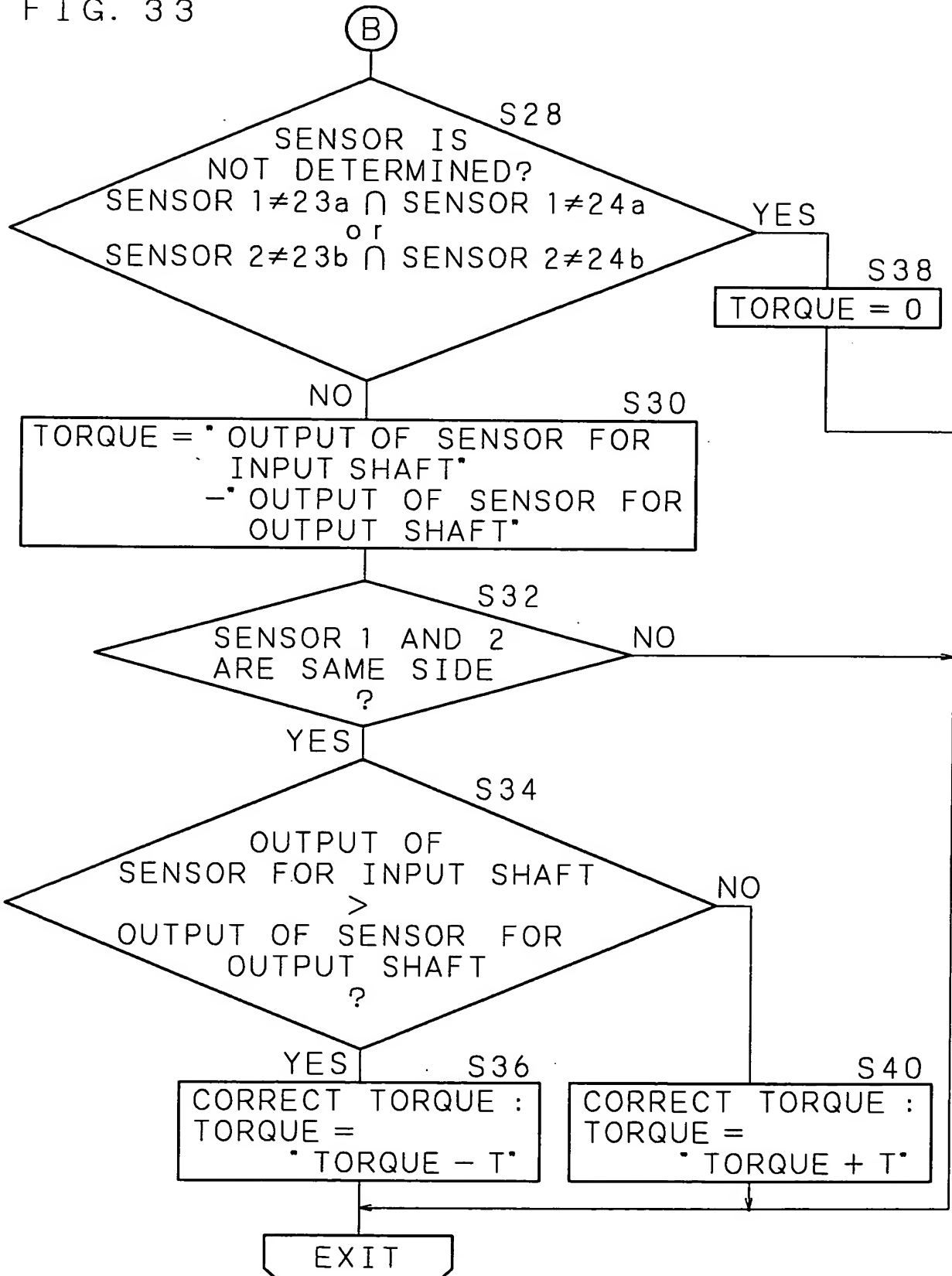
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FIG. 32



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FIG. 33



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FIG. 34A

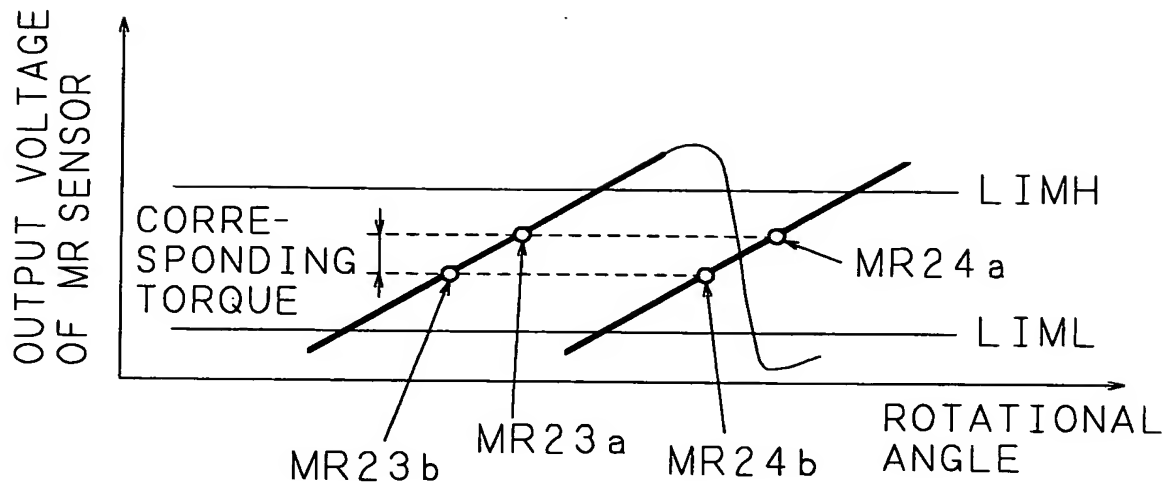
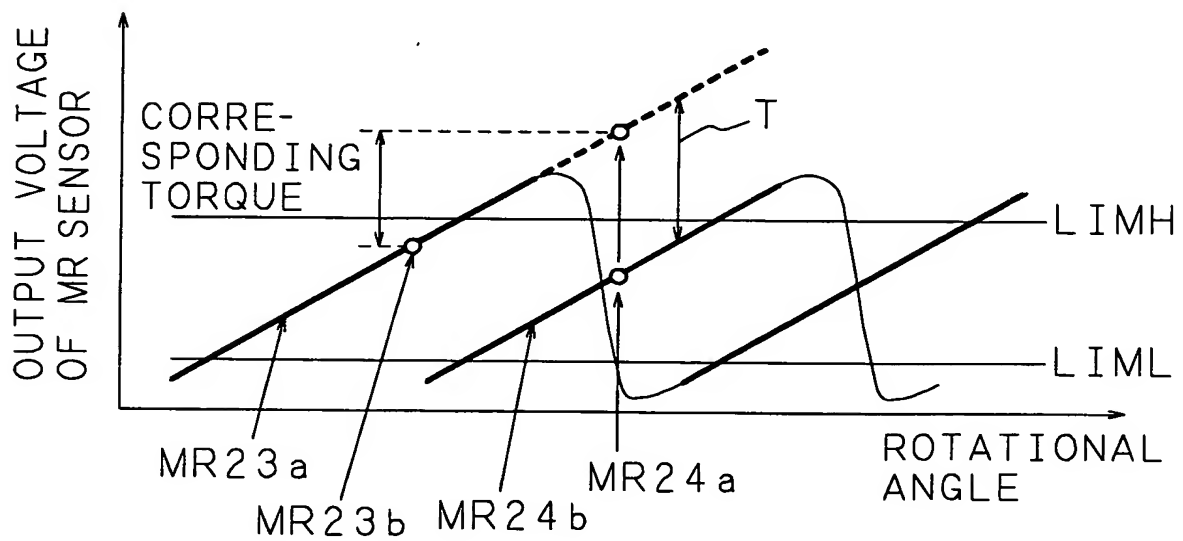
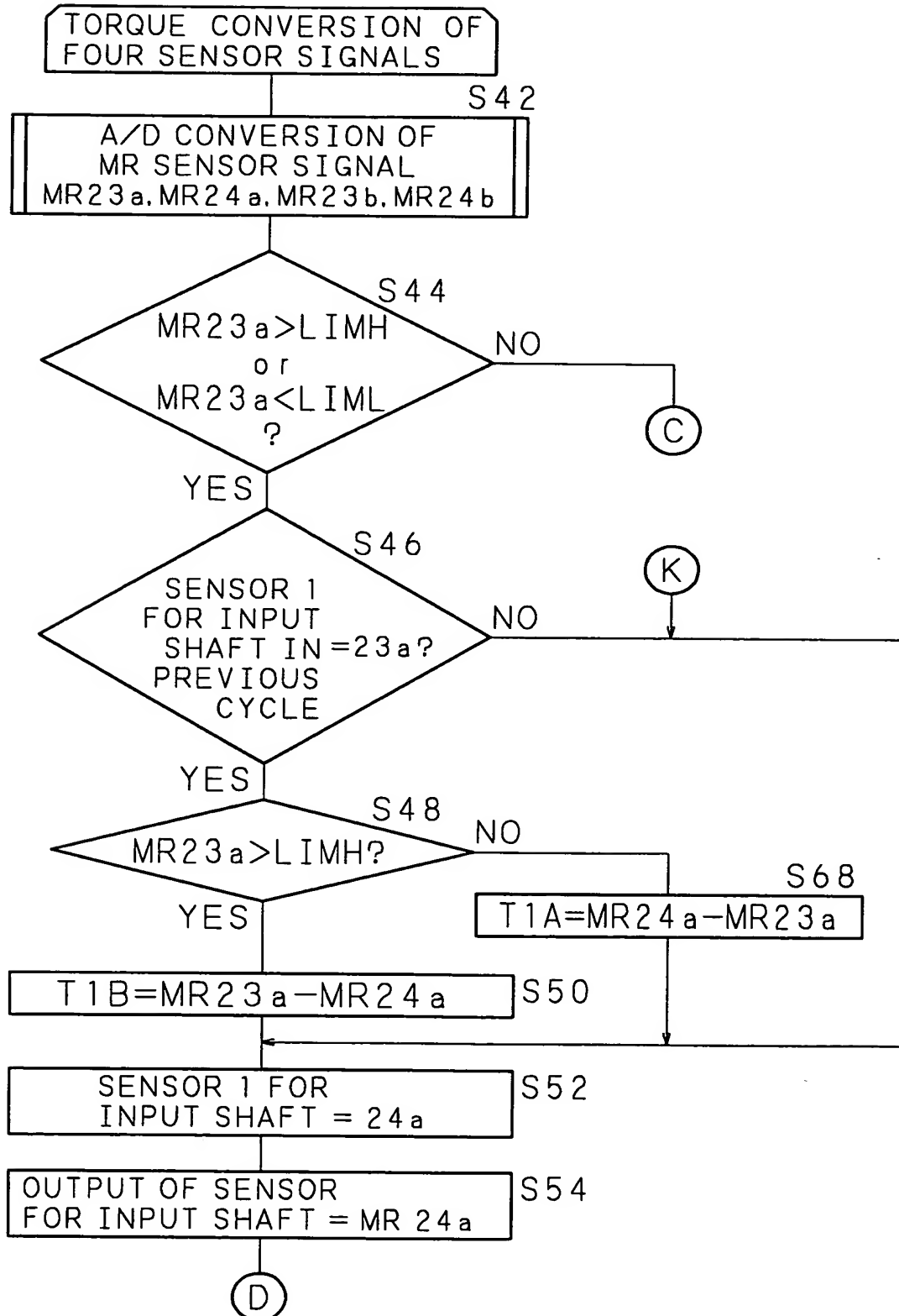


FIG. 34B



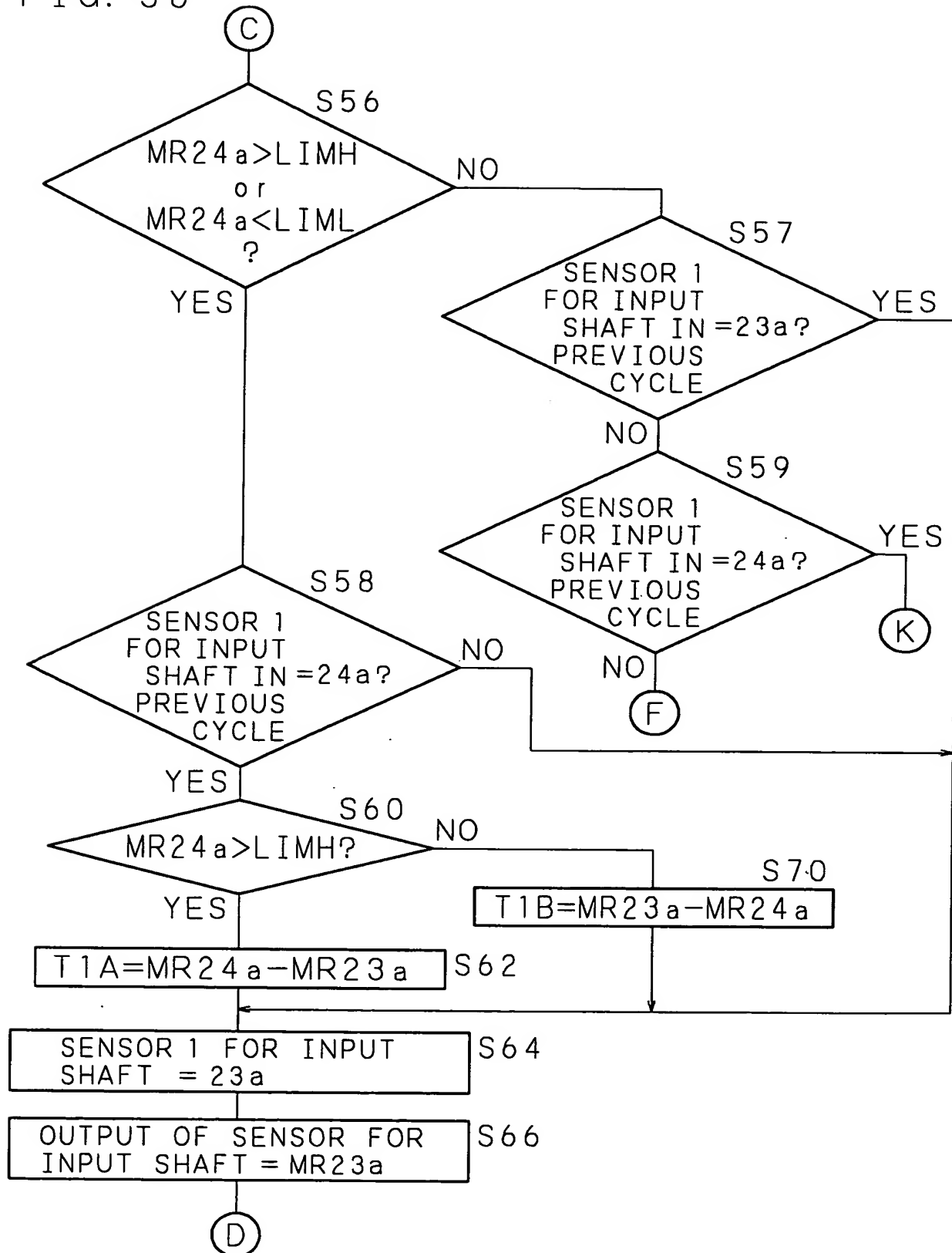
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FIG. 35



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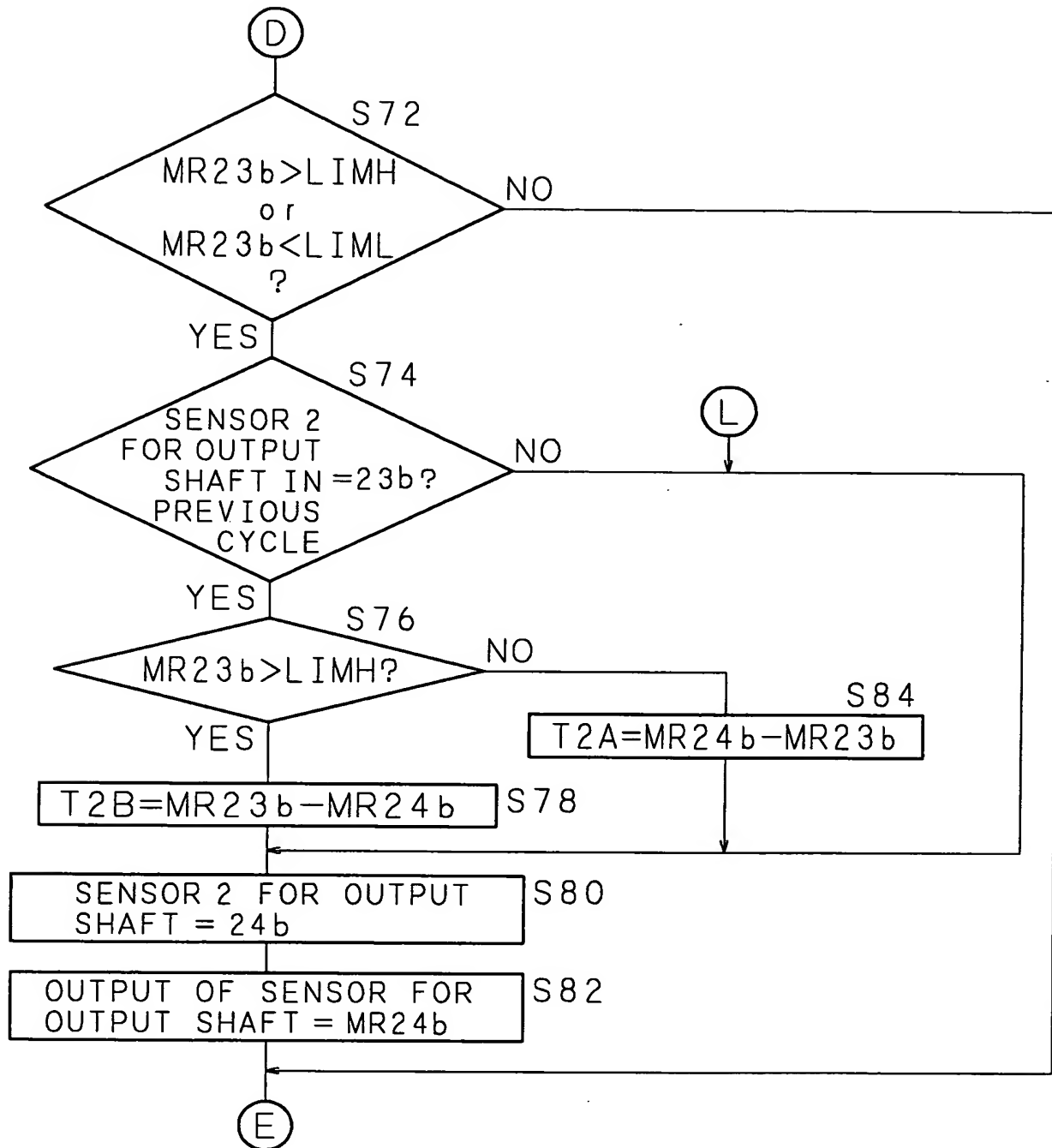
FIG. 36





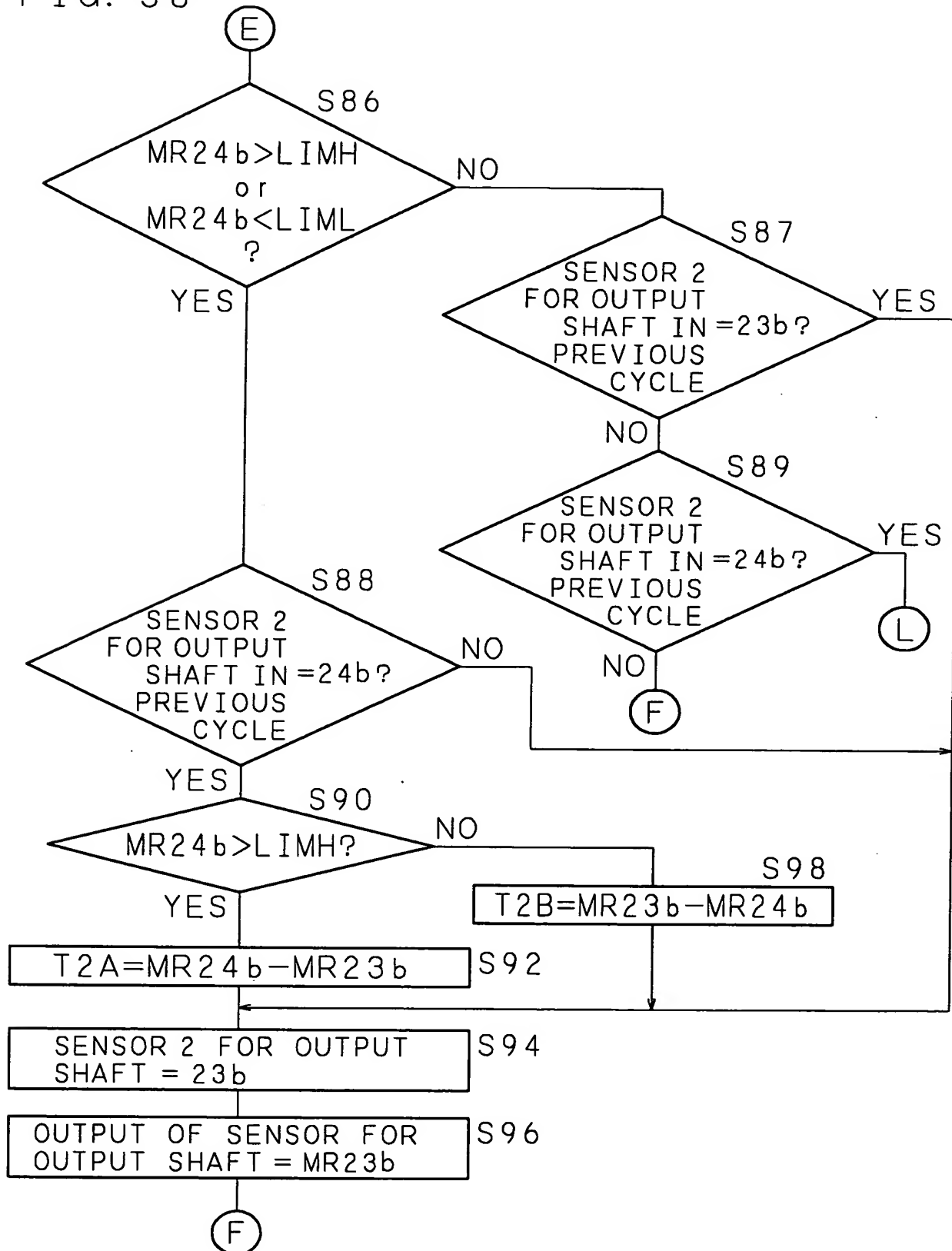
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FIG. 37



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FIG. 38



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FIG. 39

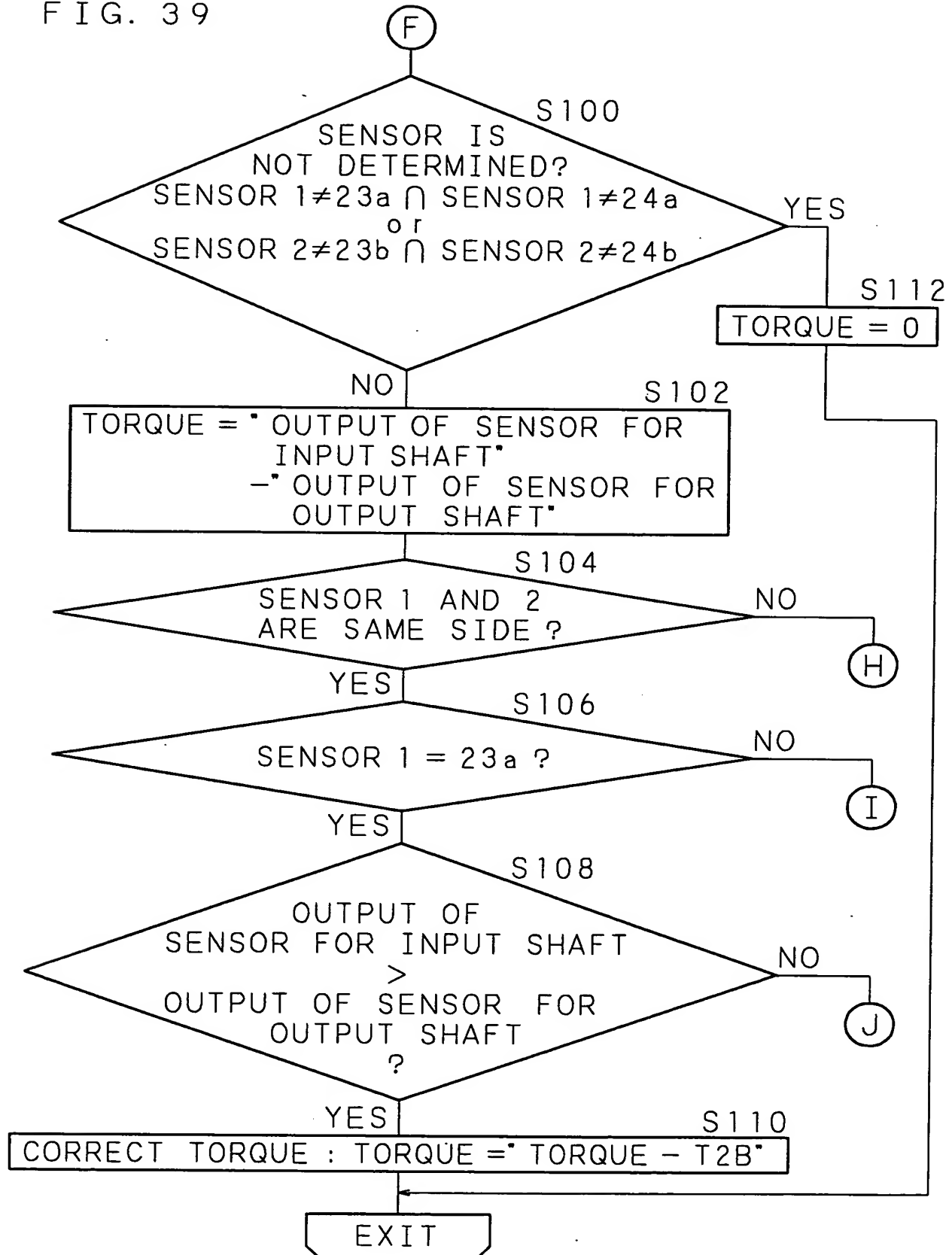
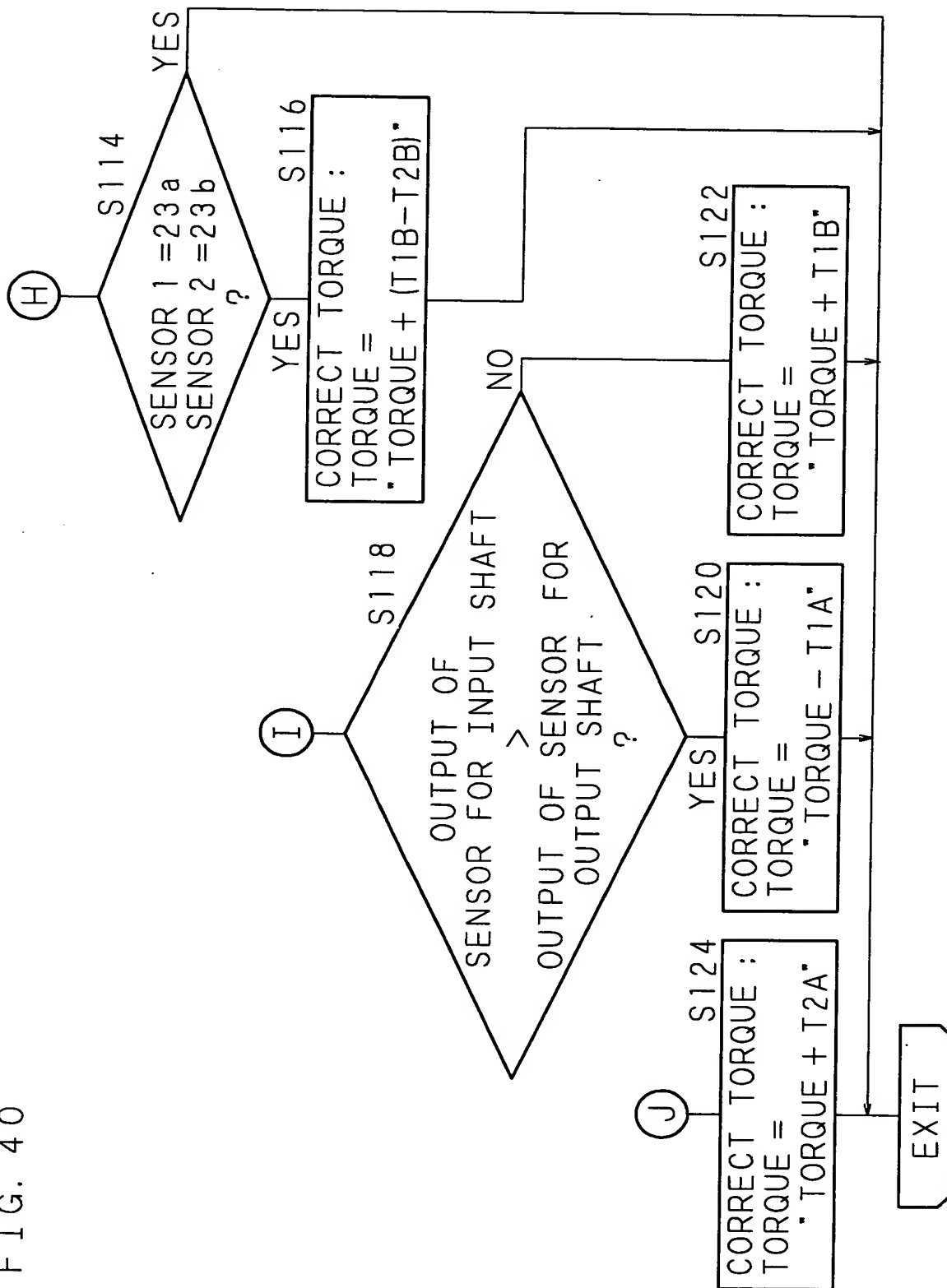


FIG. 40



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FIG. 41A

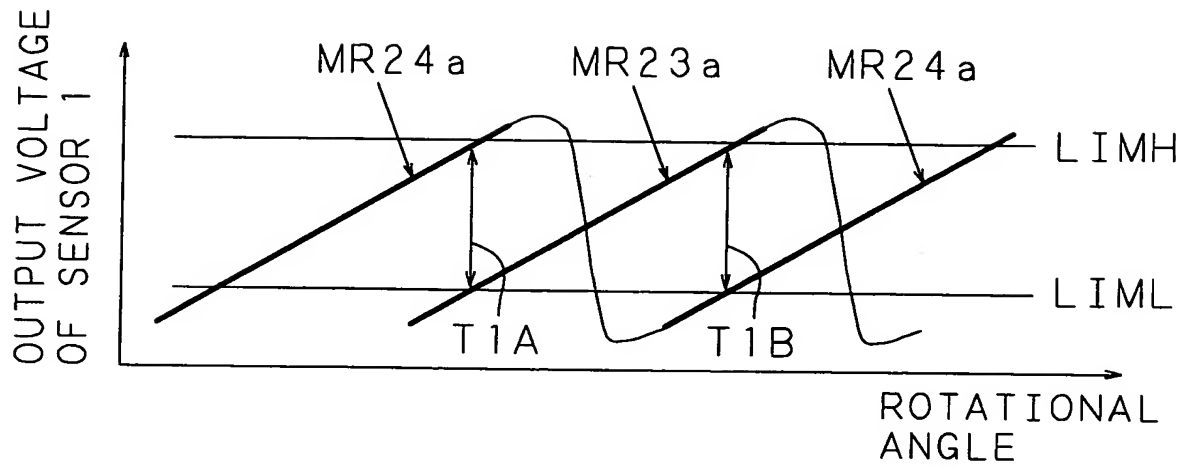
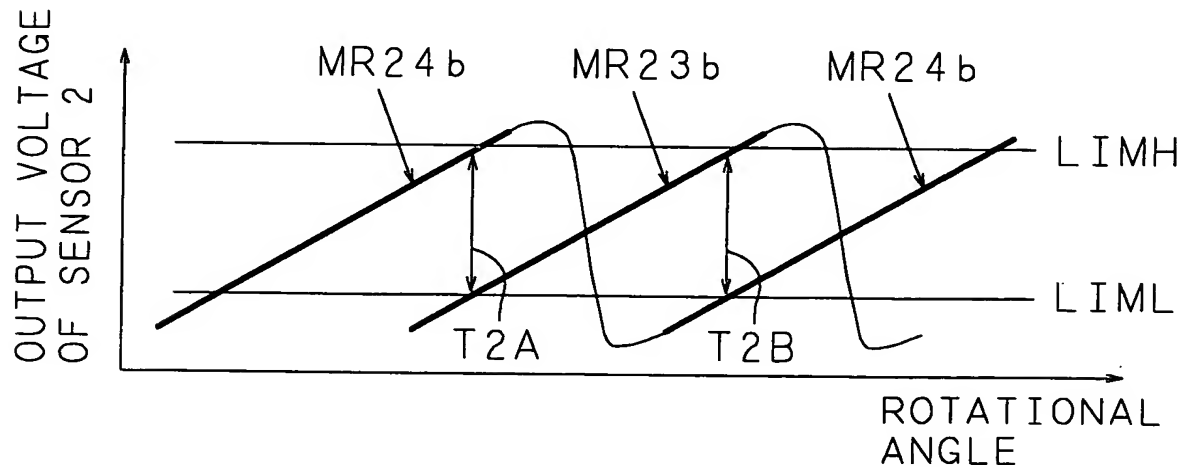
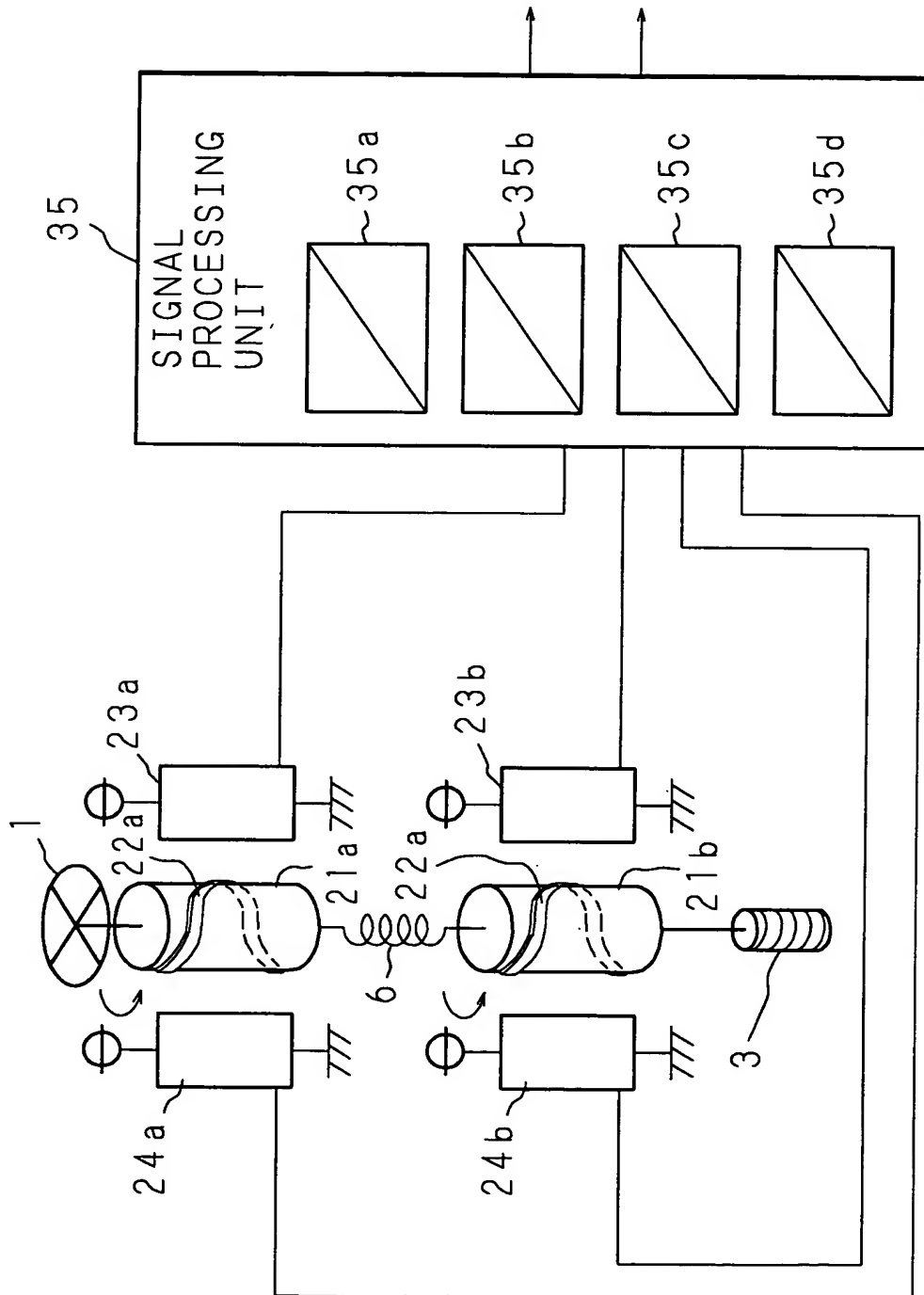


FIG. 41B



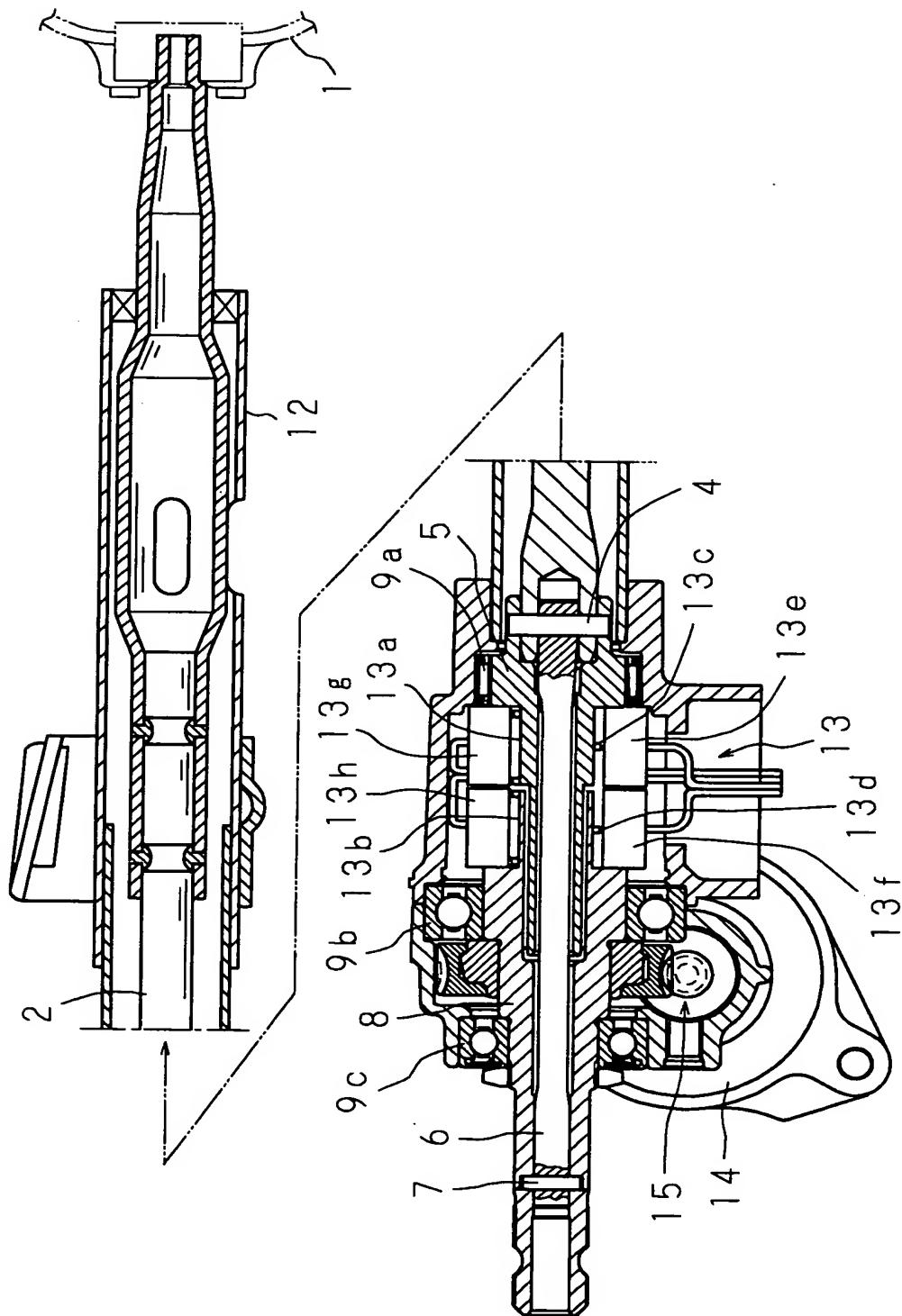
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FIG. 42



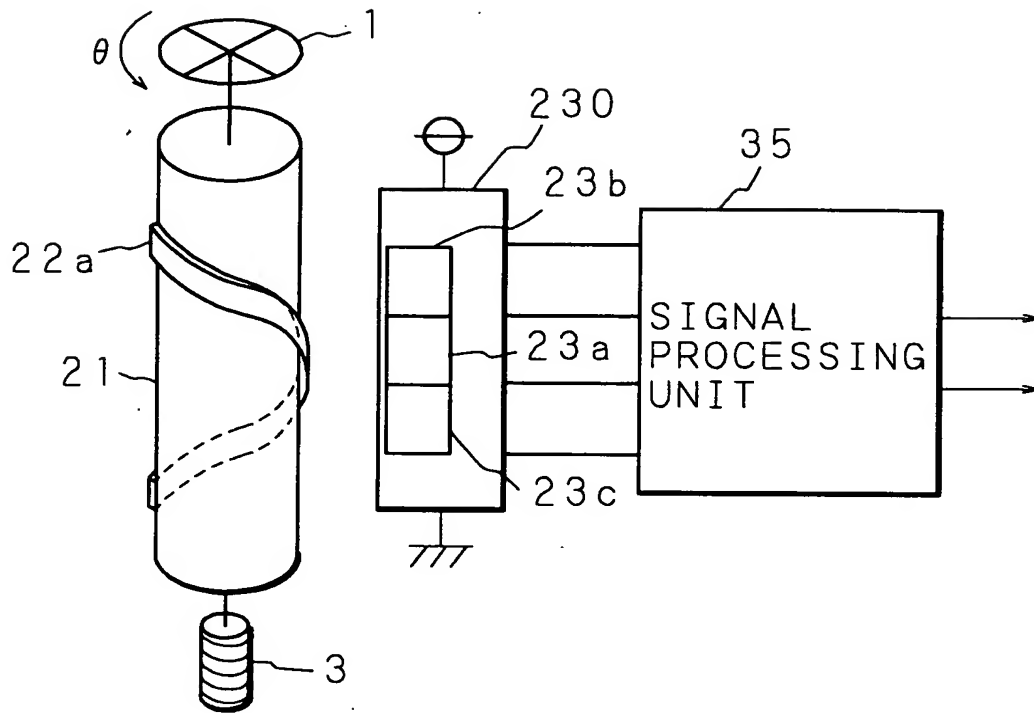
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FIG. 43



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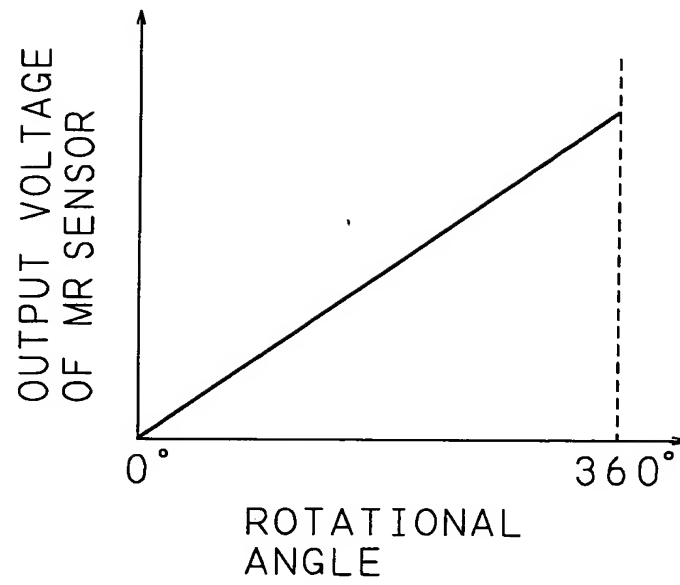
FIG. 44





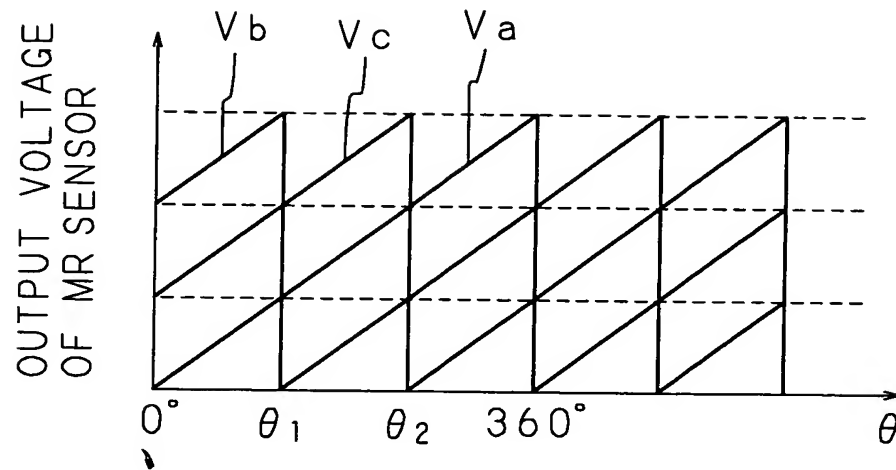
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FIG. 45



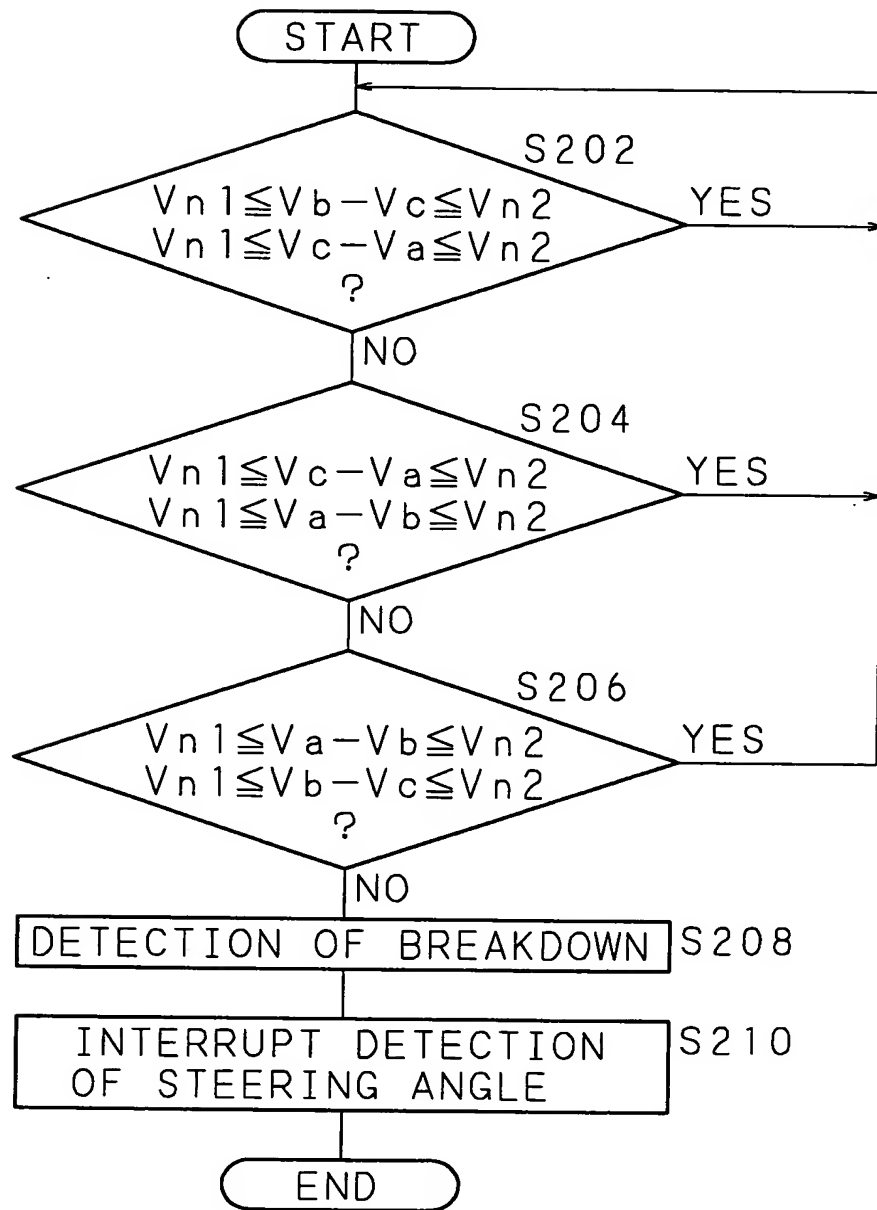
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FIG. 46



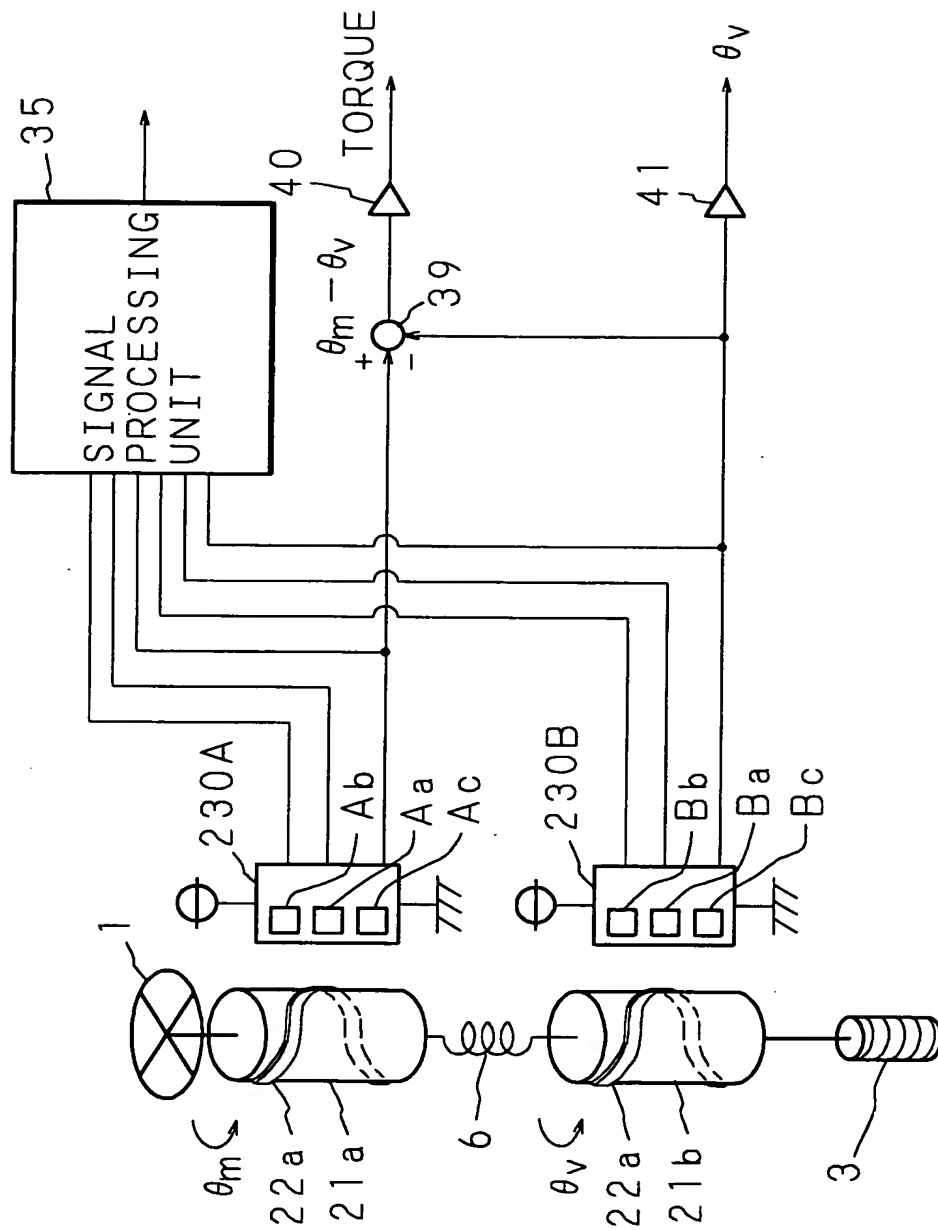
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FIG. 47



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FIG. 48



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FIG. 49A

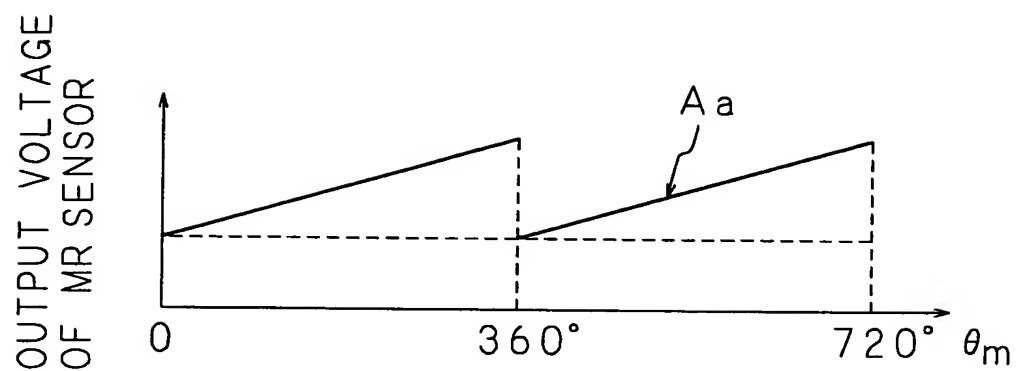


FIG. 49B

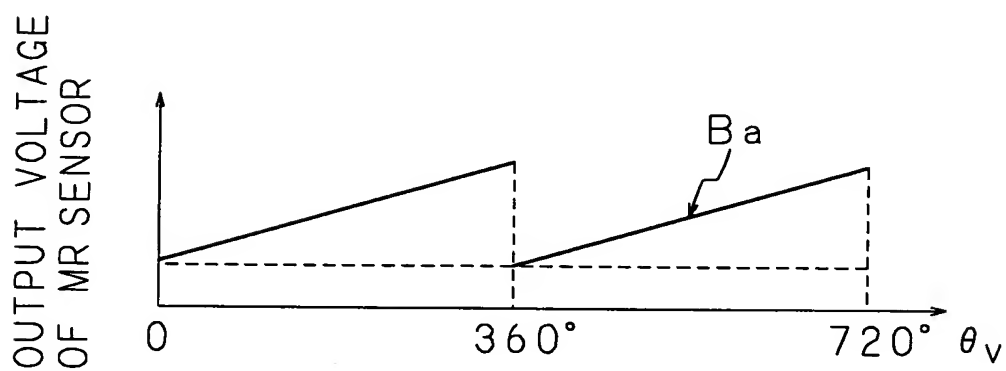


FIG. 49C

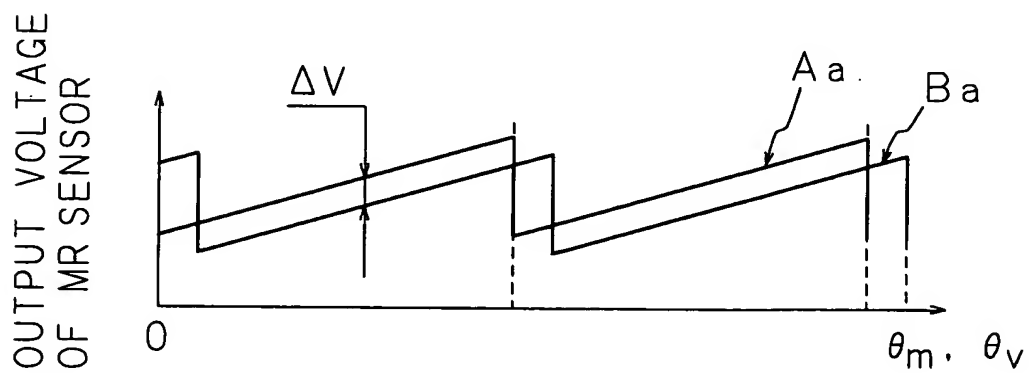




FIG. 51

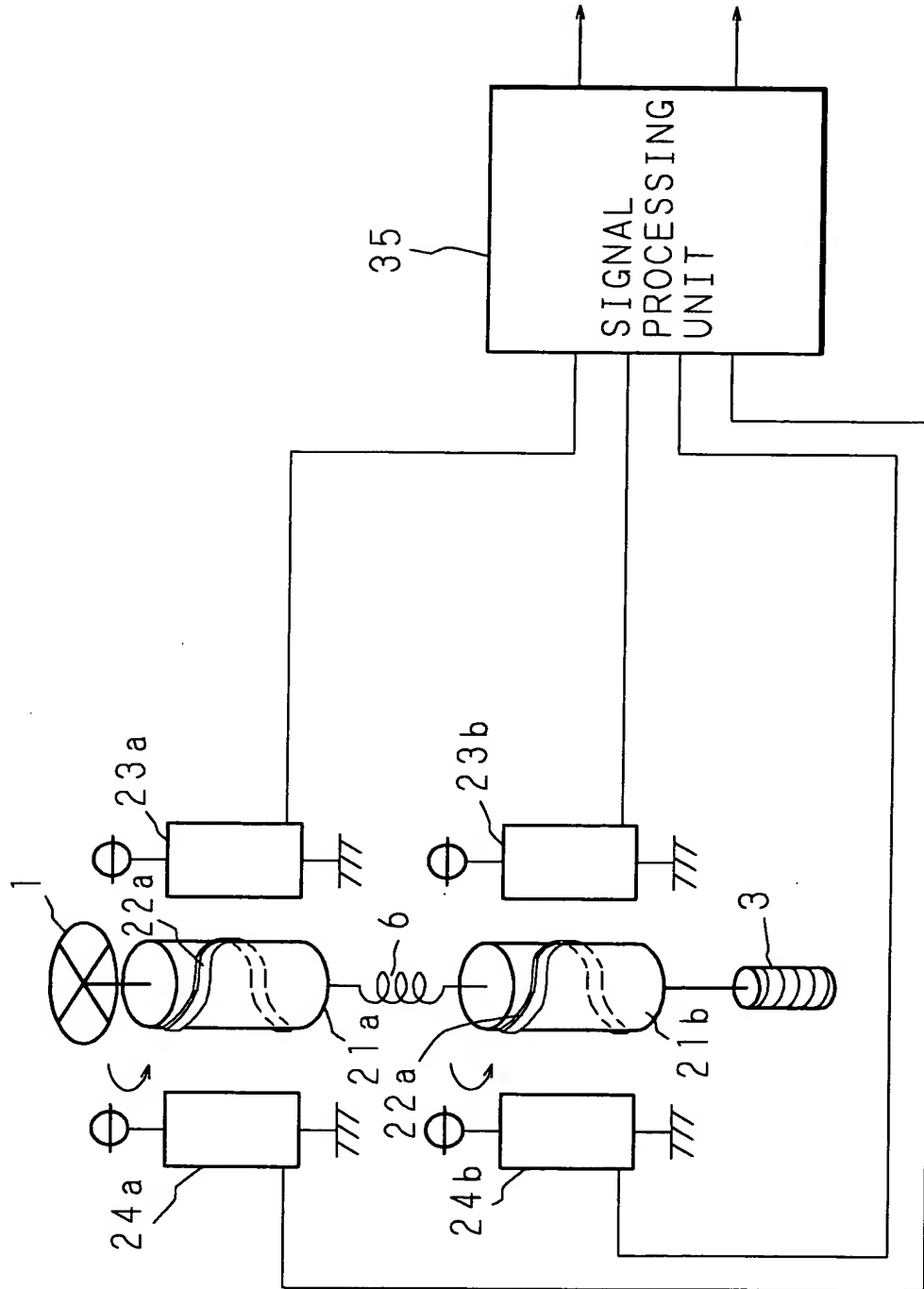


FIG. 52

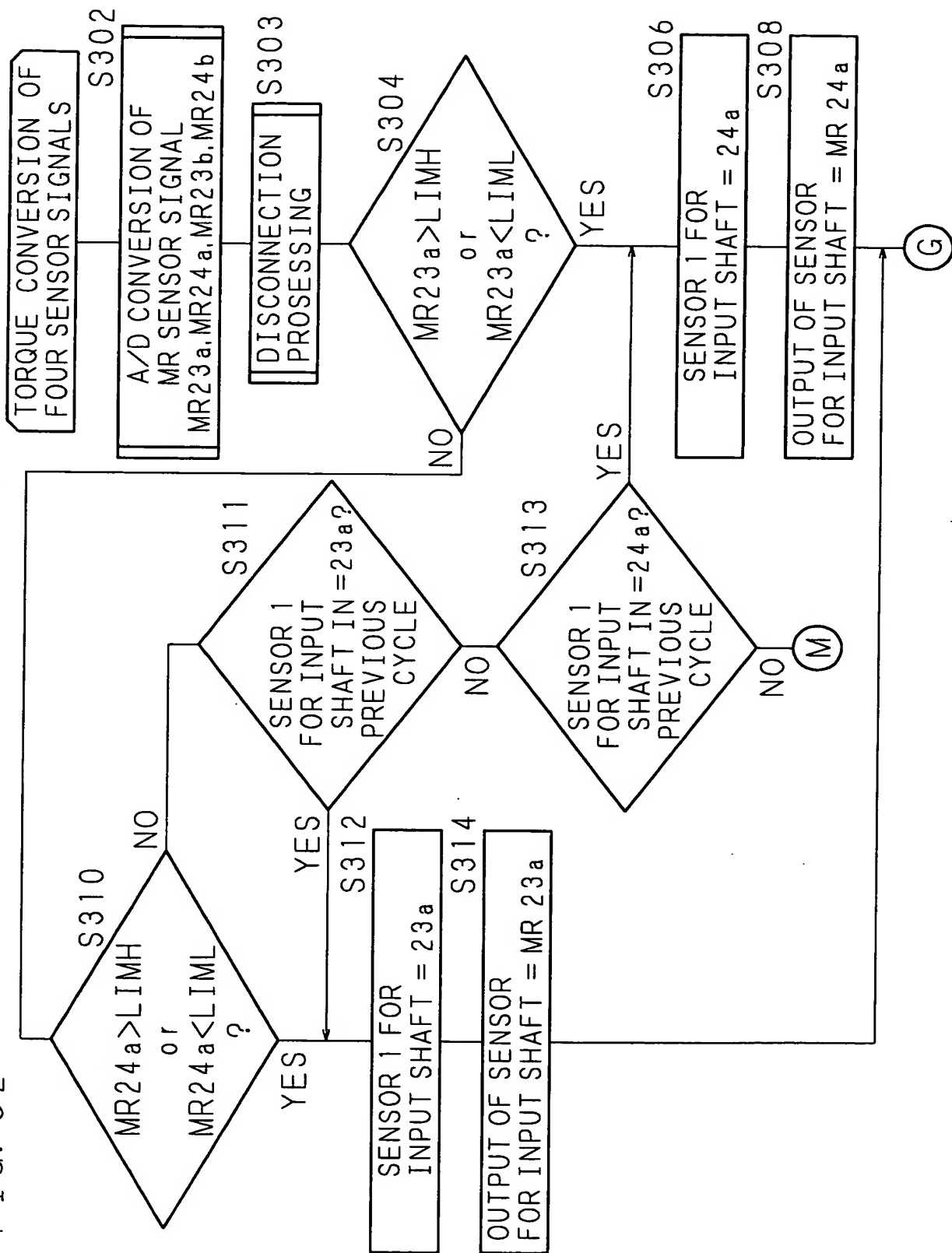
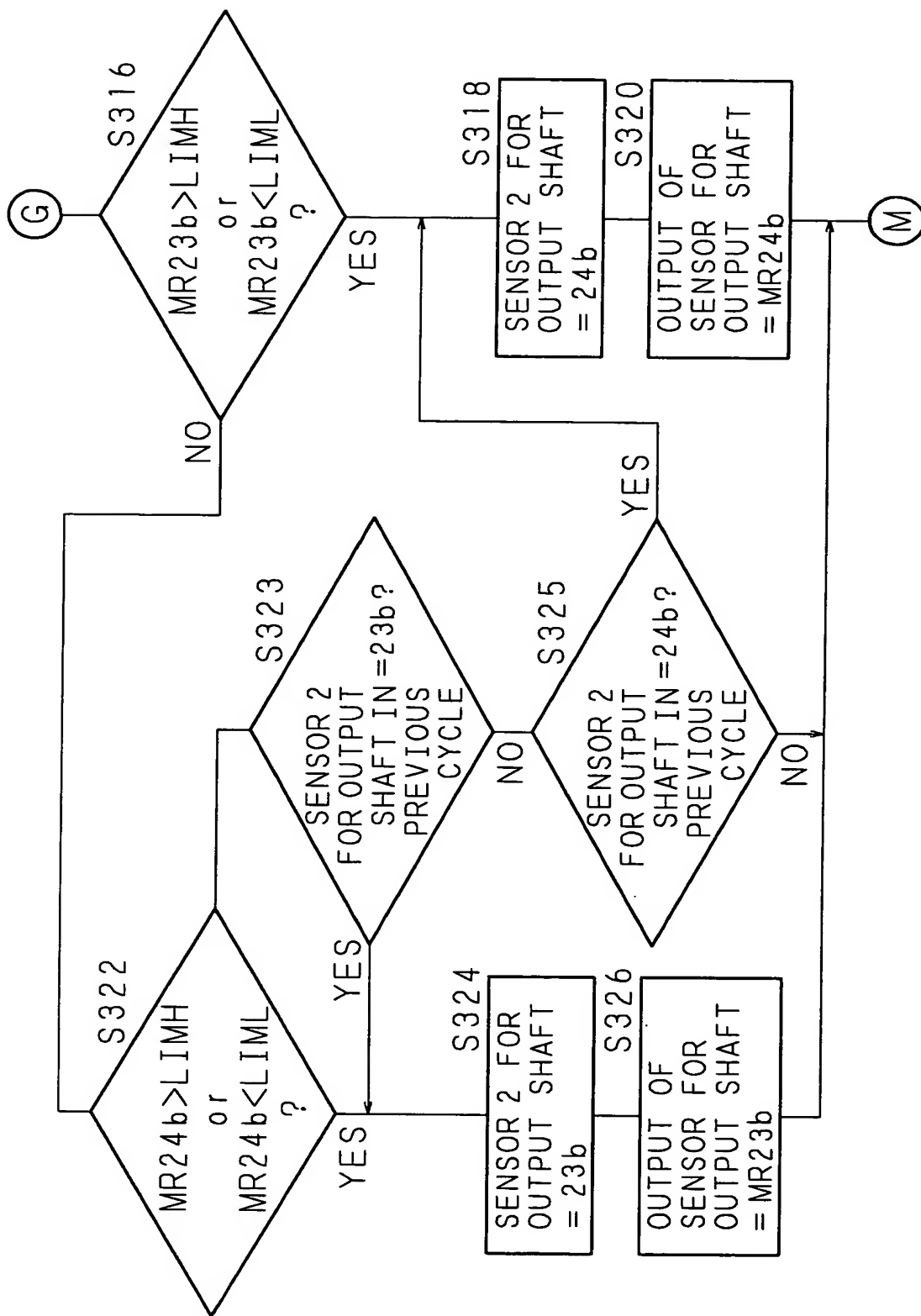


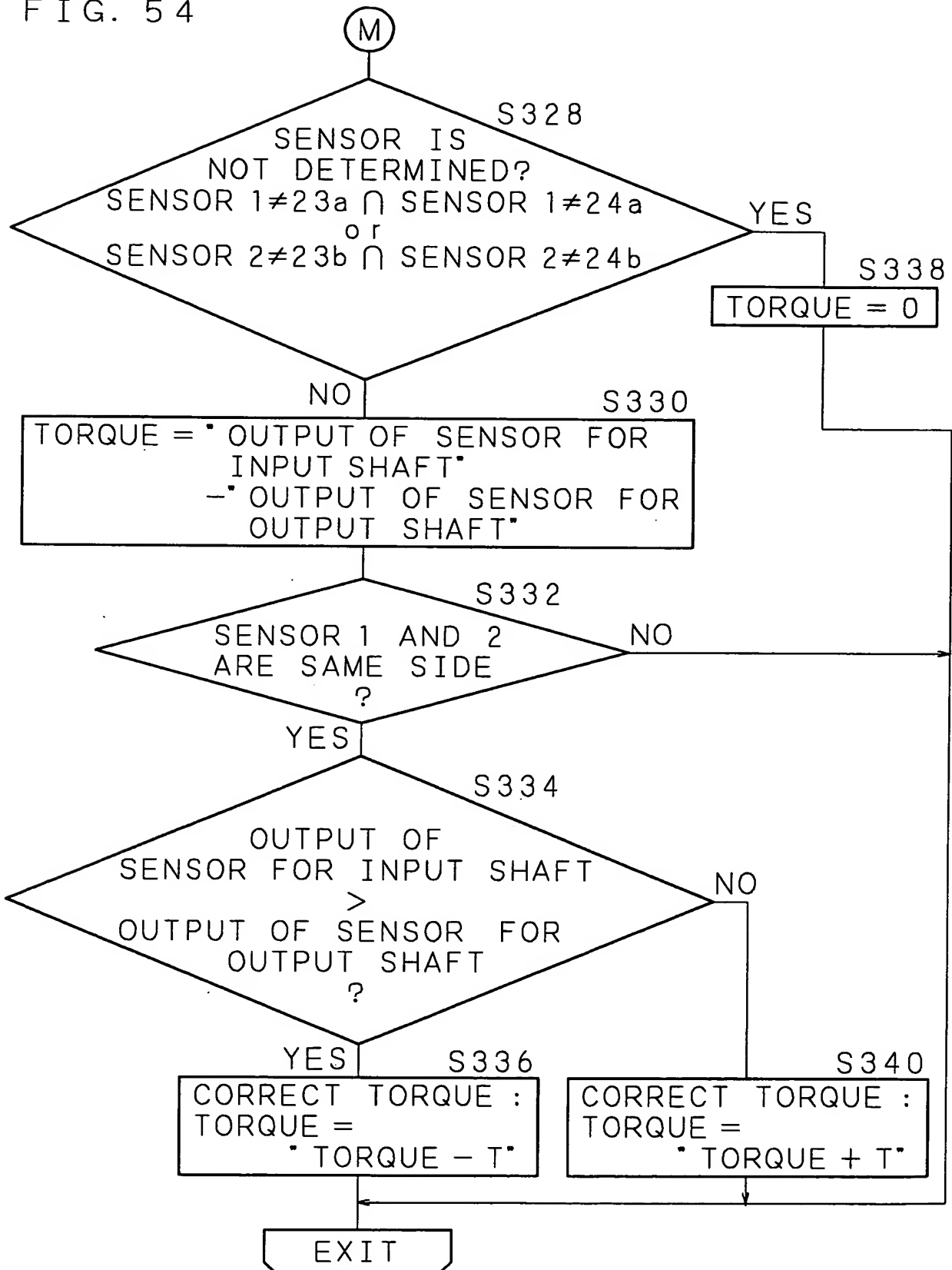


FIG. 53



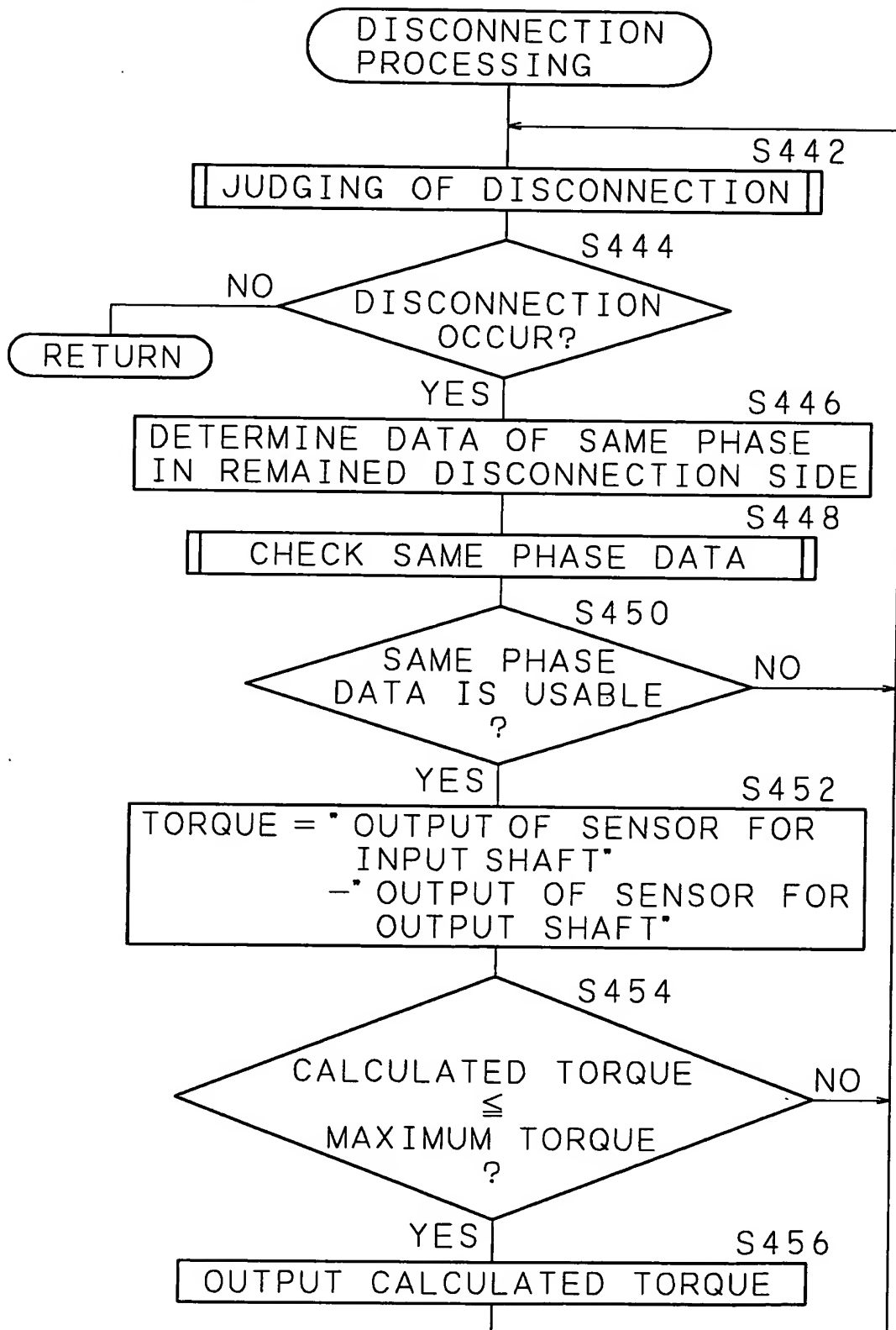
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FIG. 54



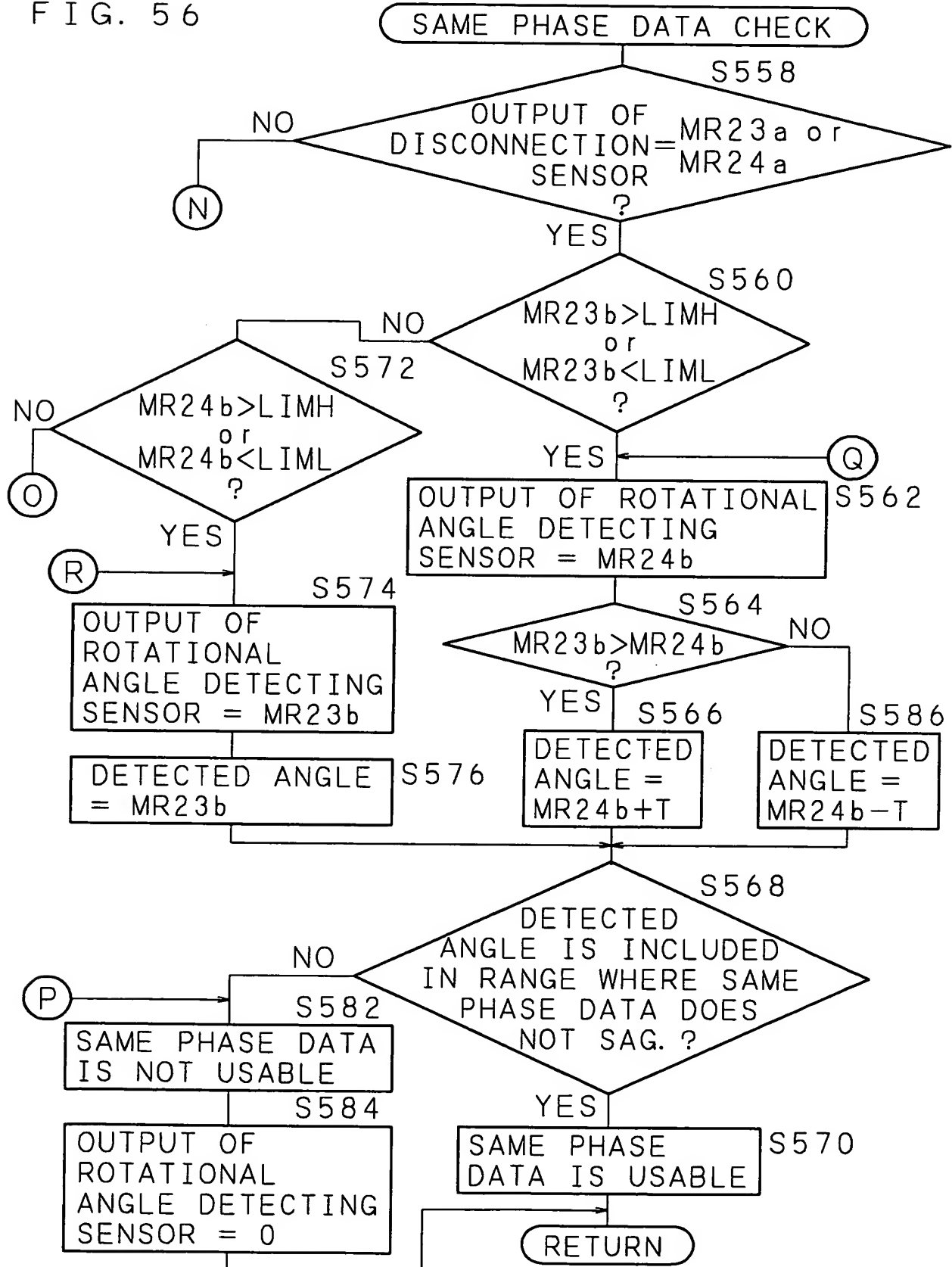
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FIG. 55



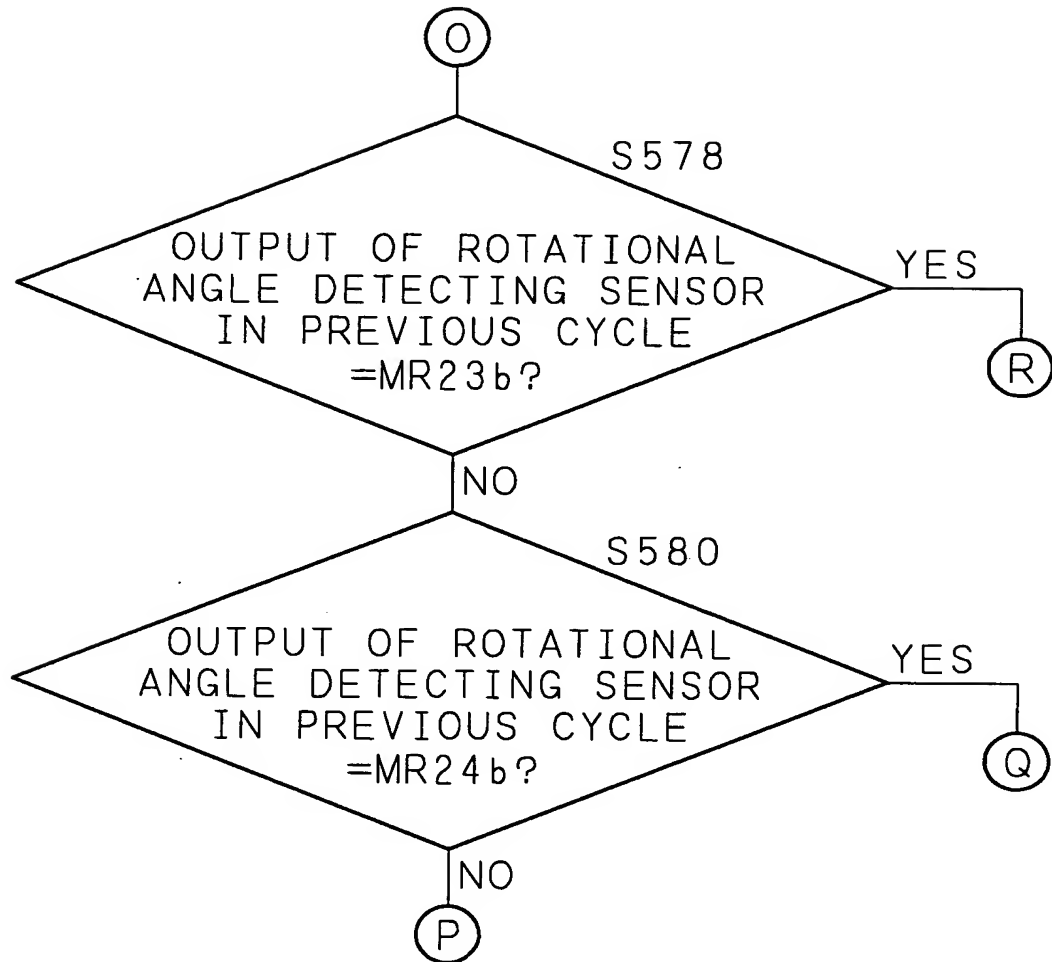
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FIG. 56



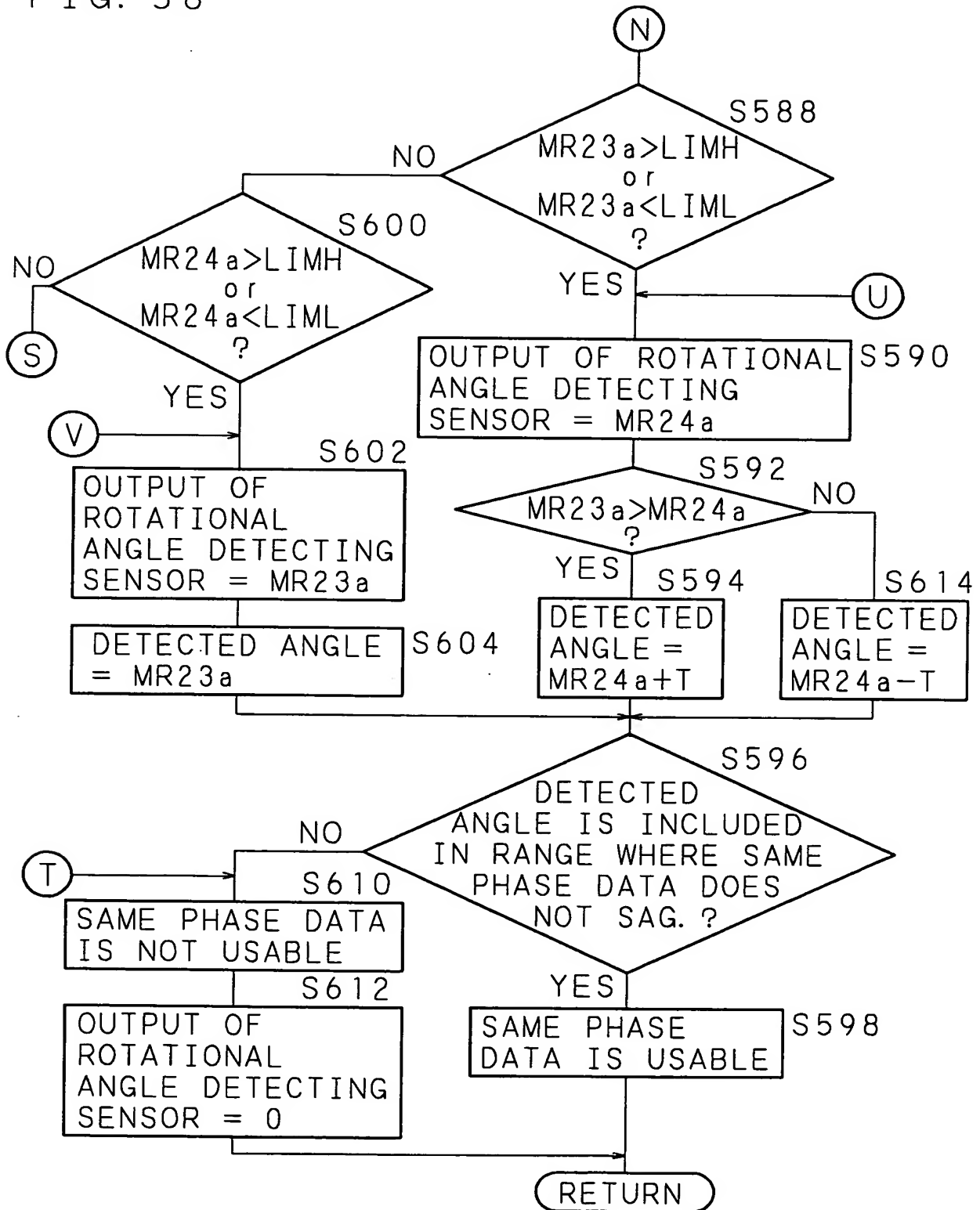
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FIG. 57



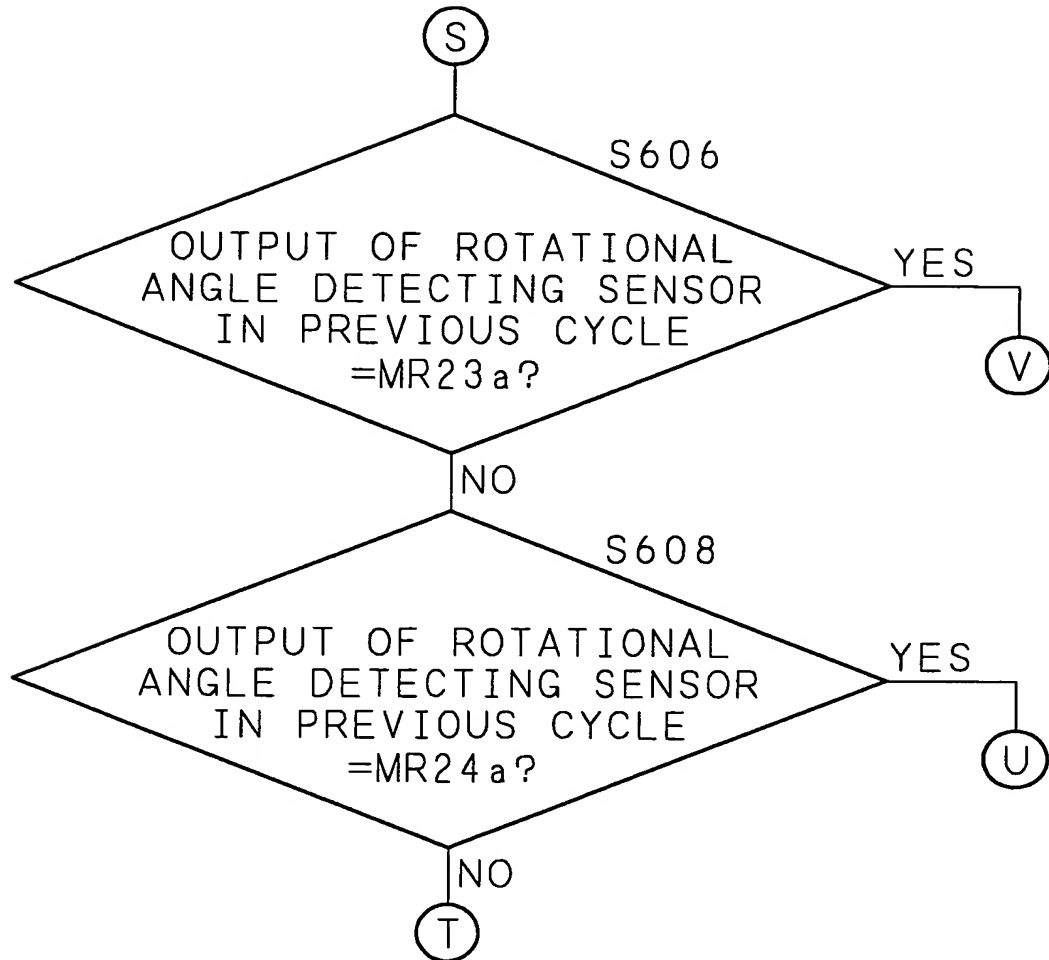
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FIG. 58



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FIG. 59



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FIG. 60A

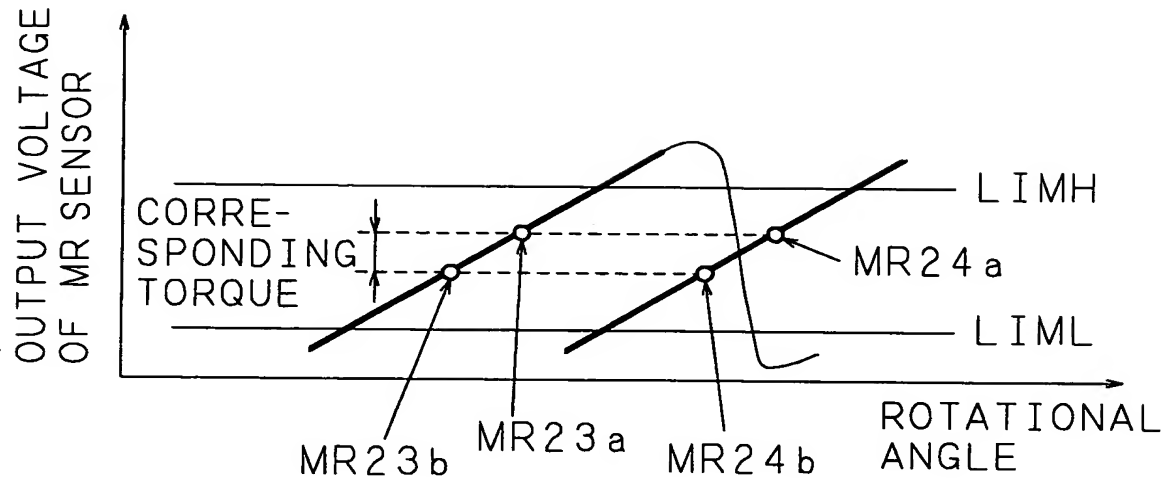
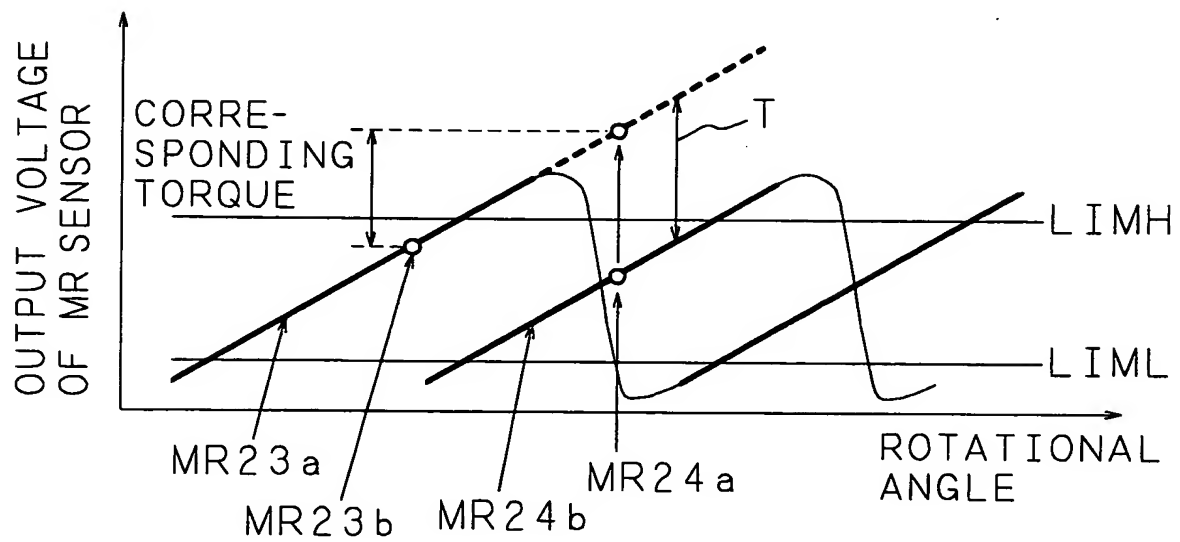


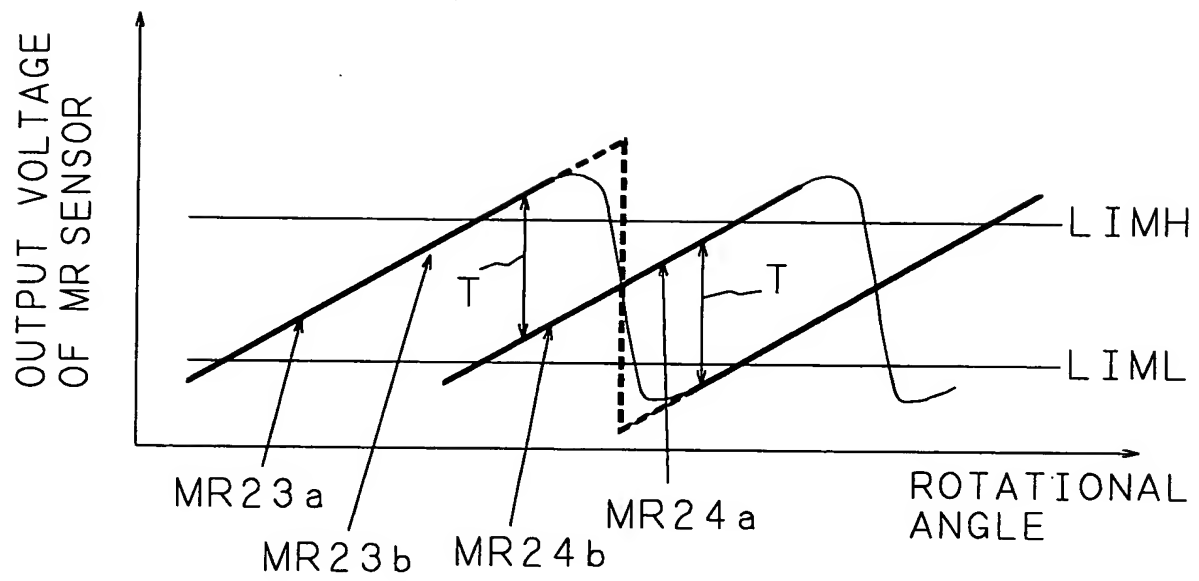
FIG. 60B





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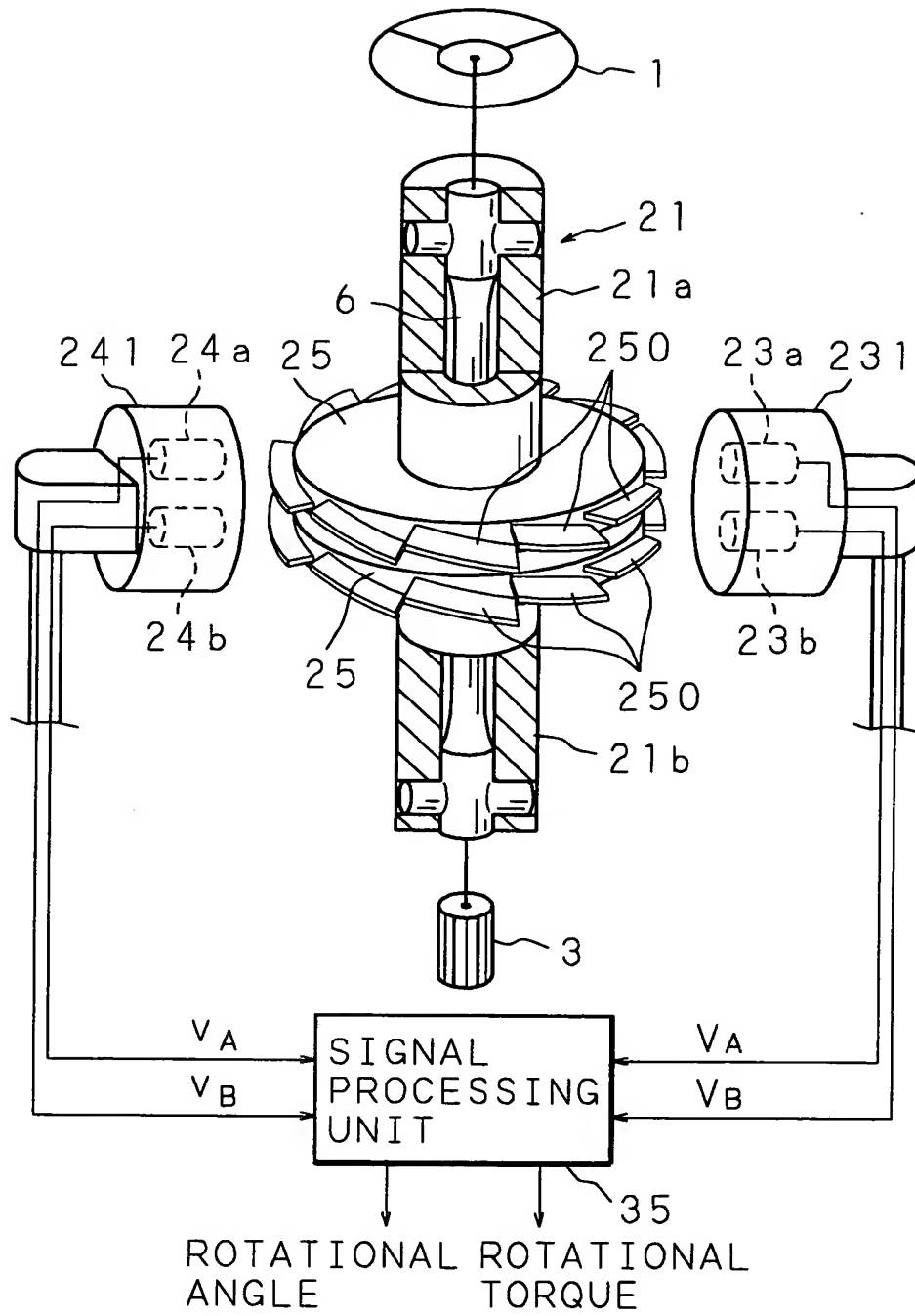
FIG. 61



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FIG. 63



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FIG. 64

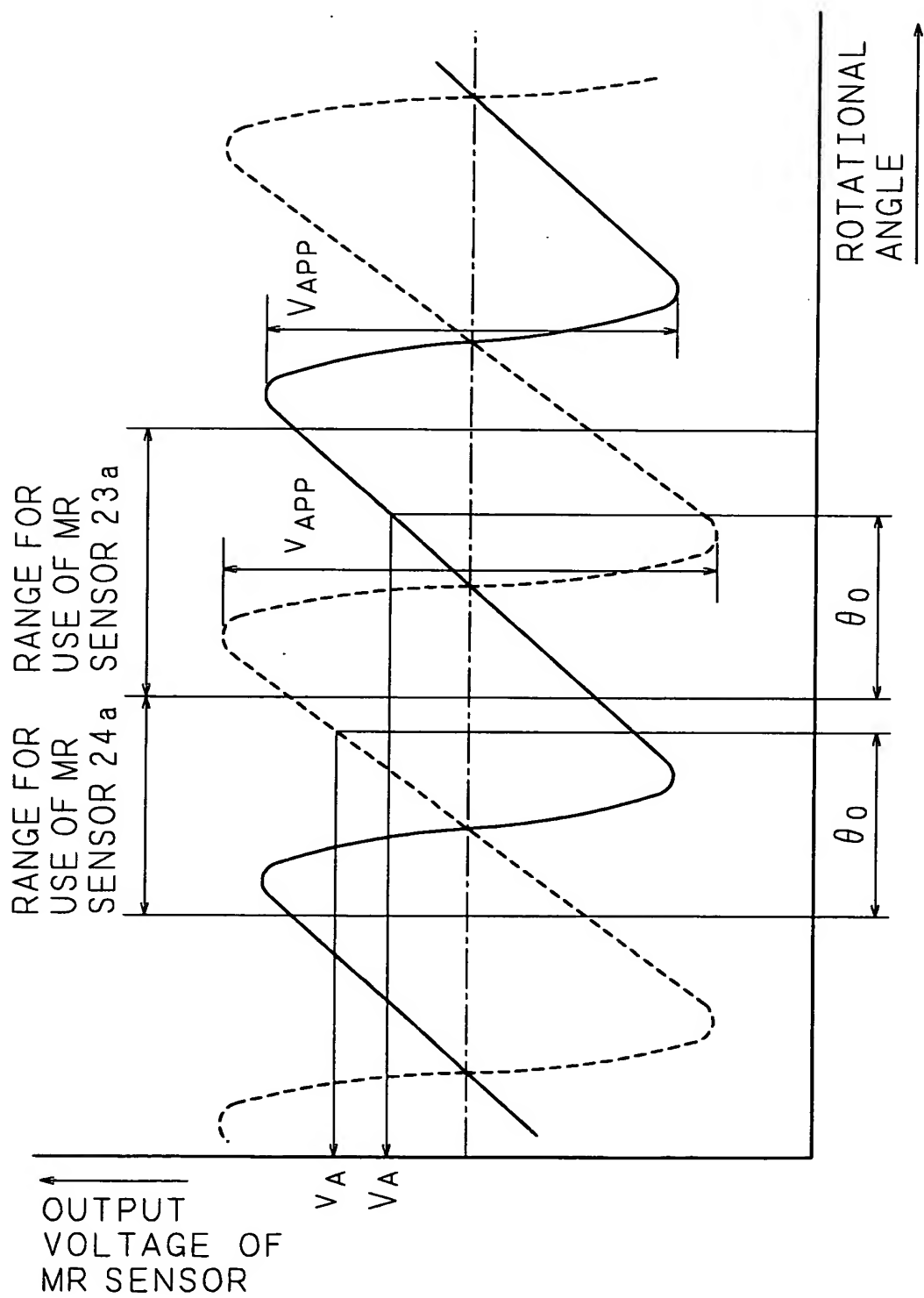
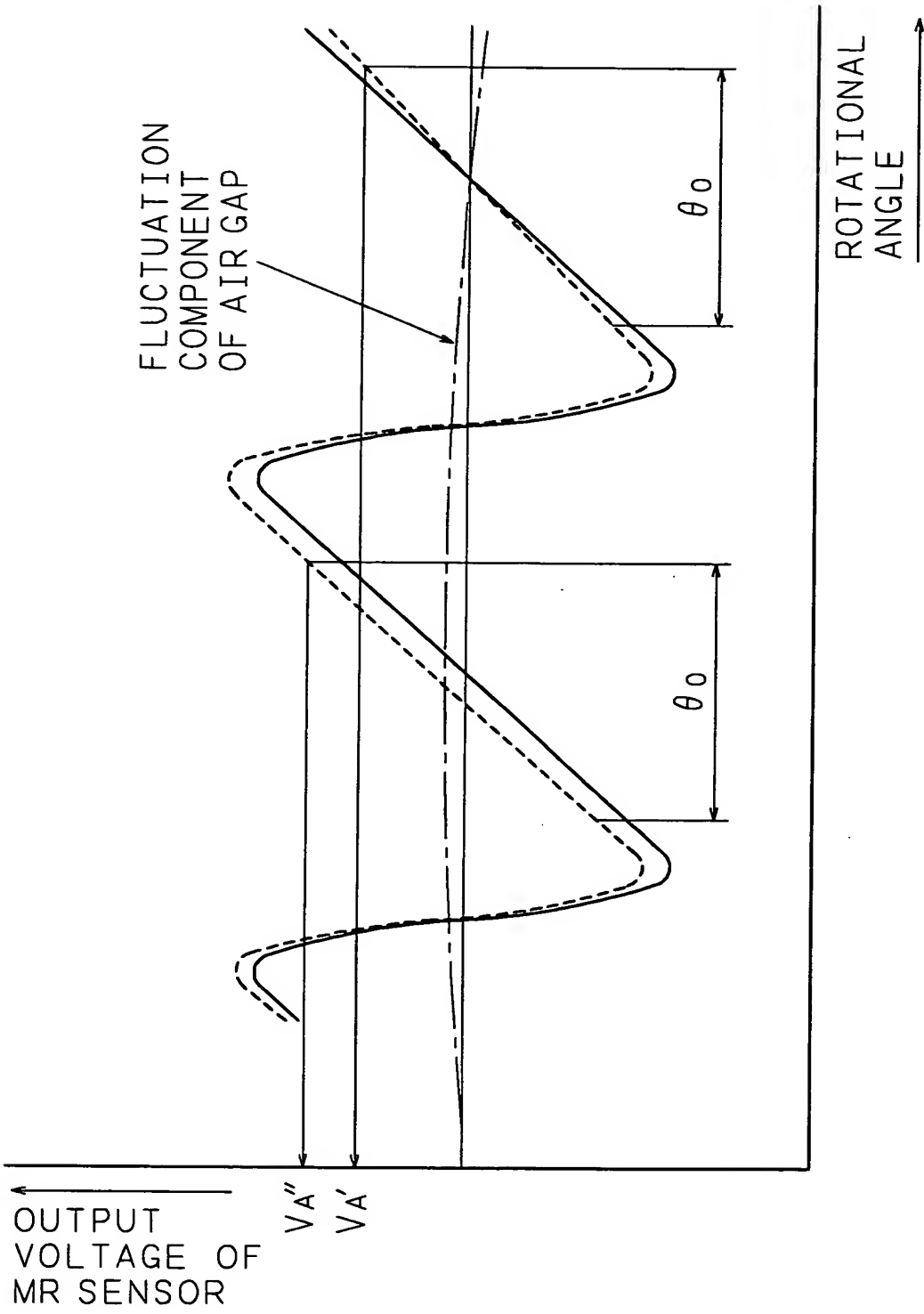
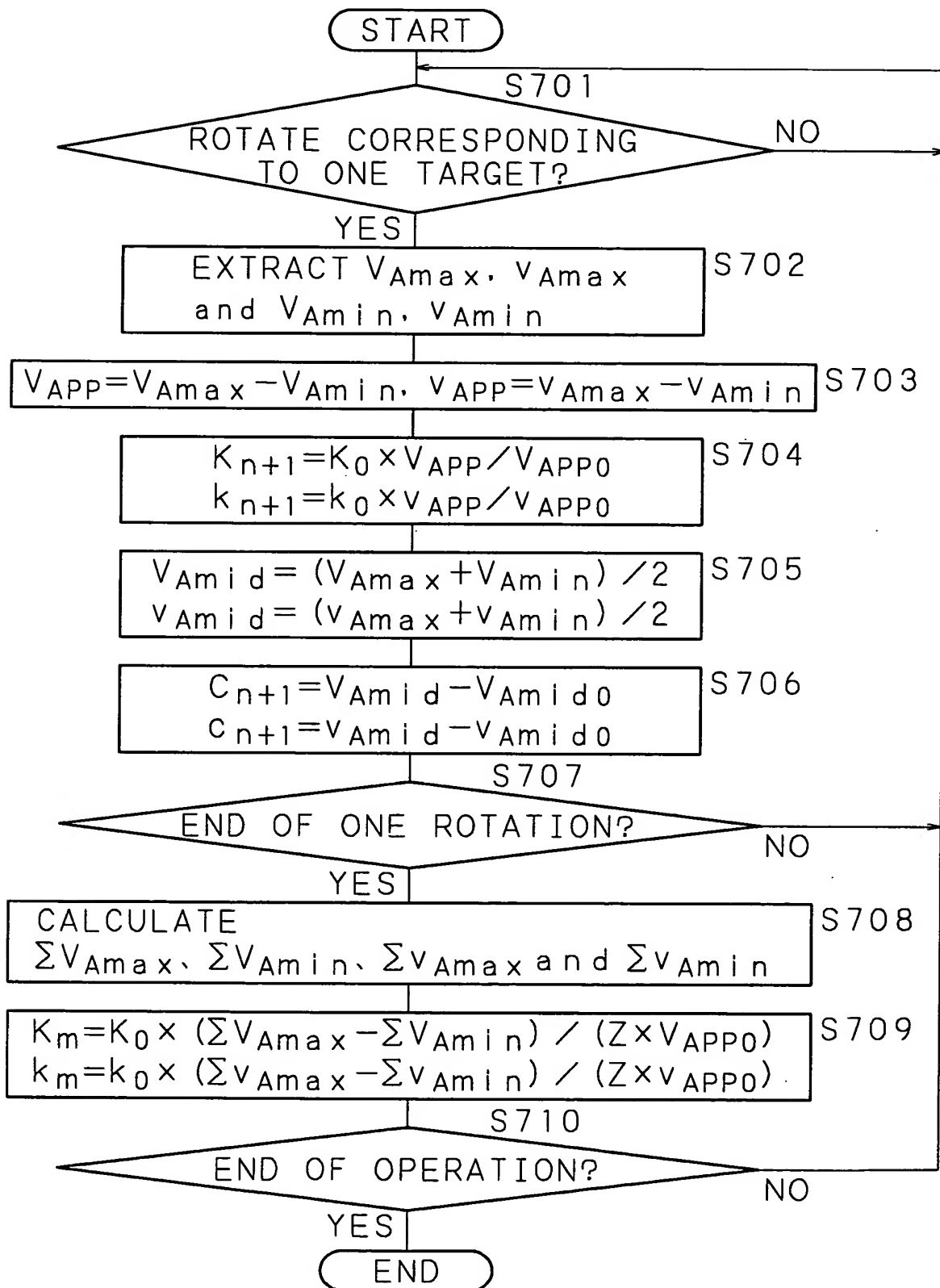


FIG. 65



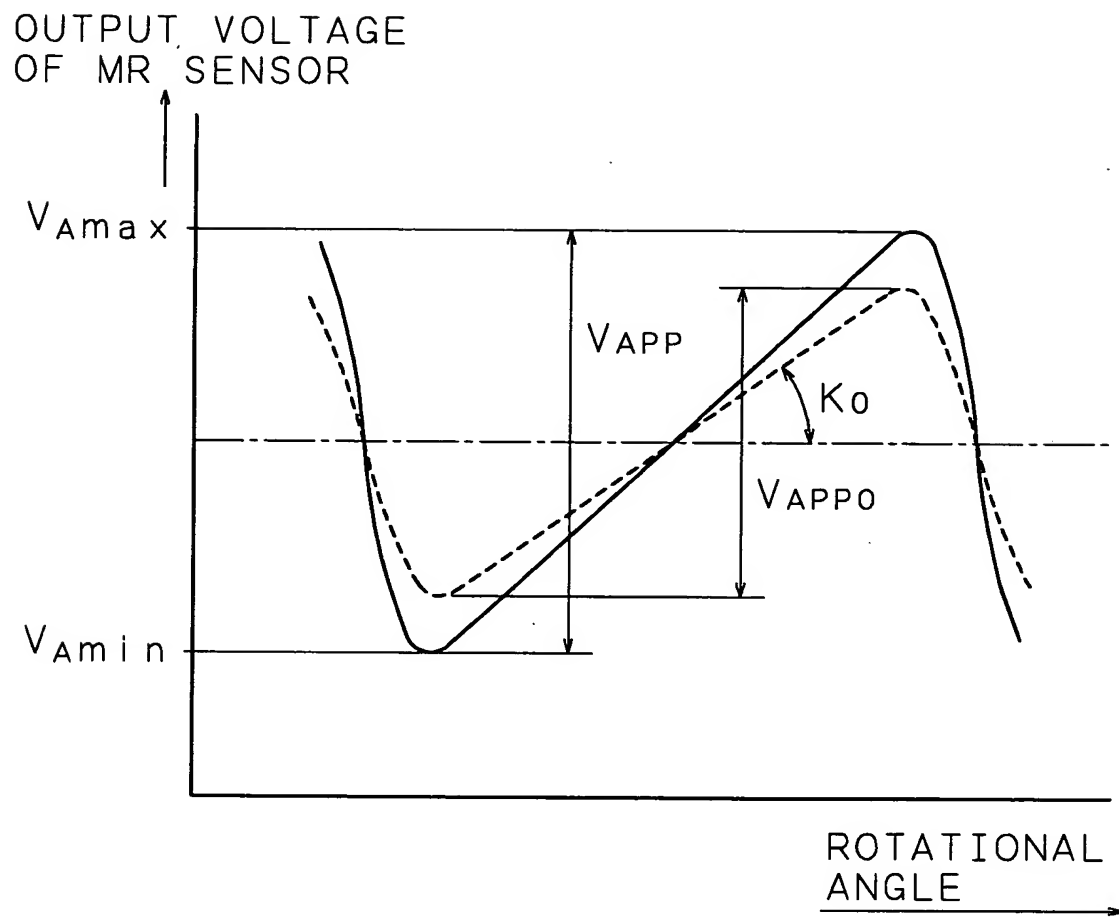
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FIG. 66



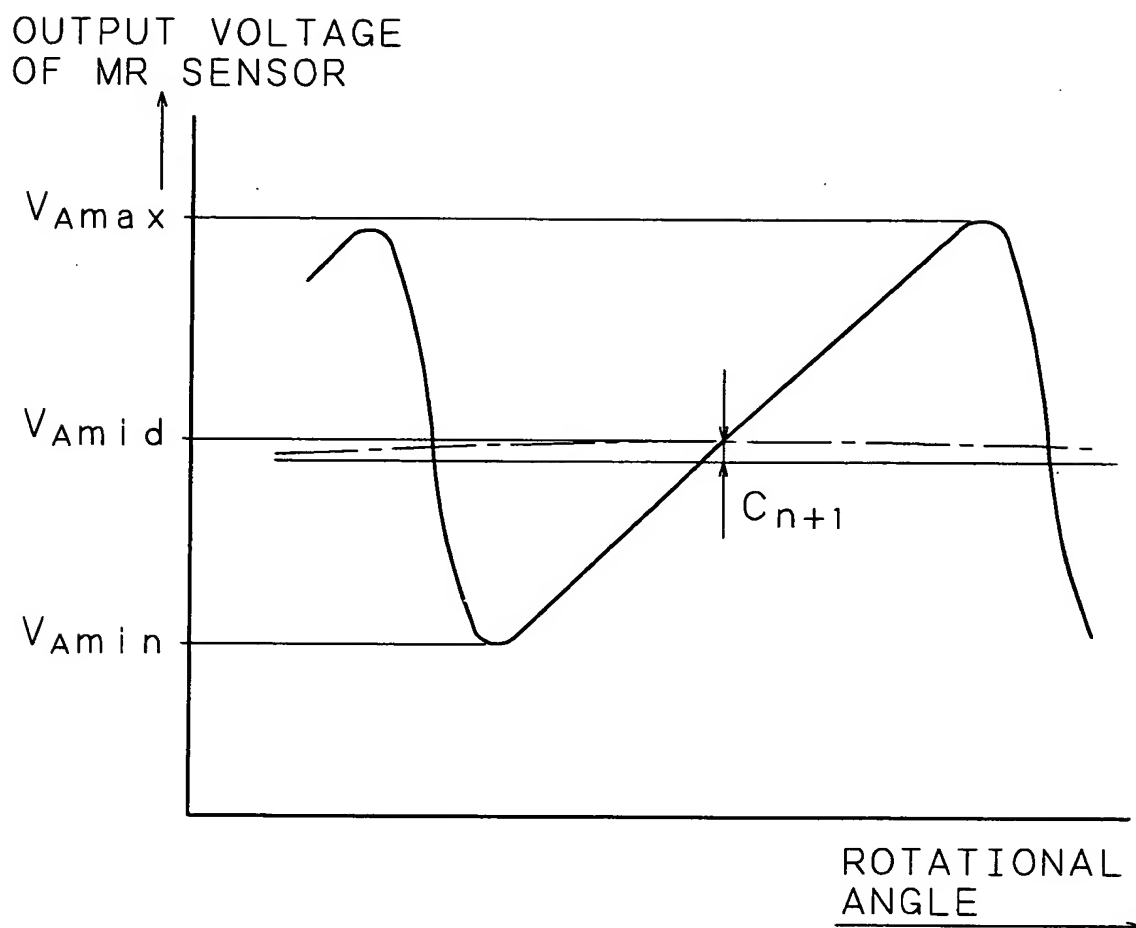
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FIG. 67



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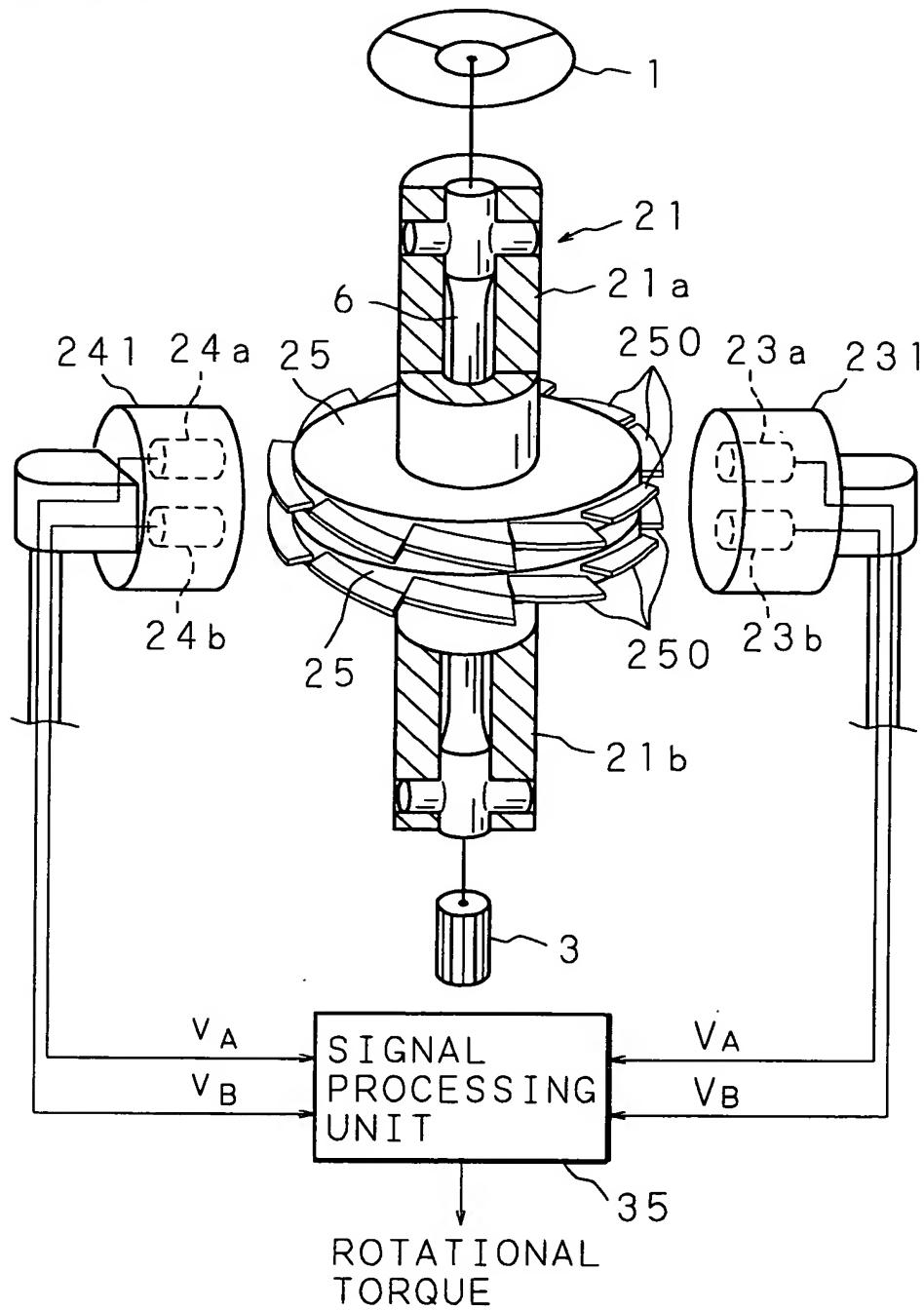
FIG. 68





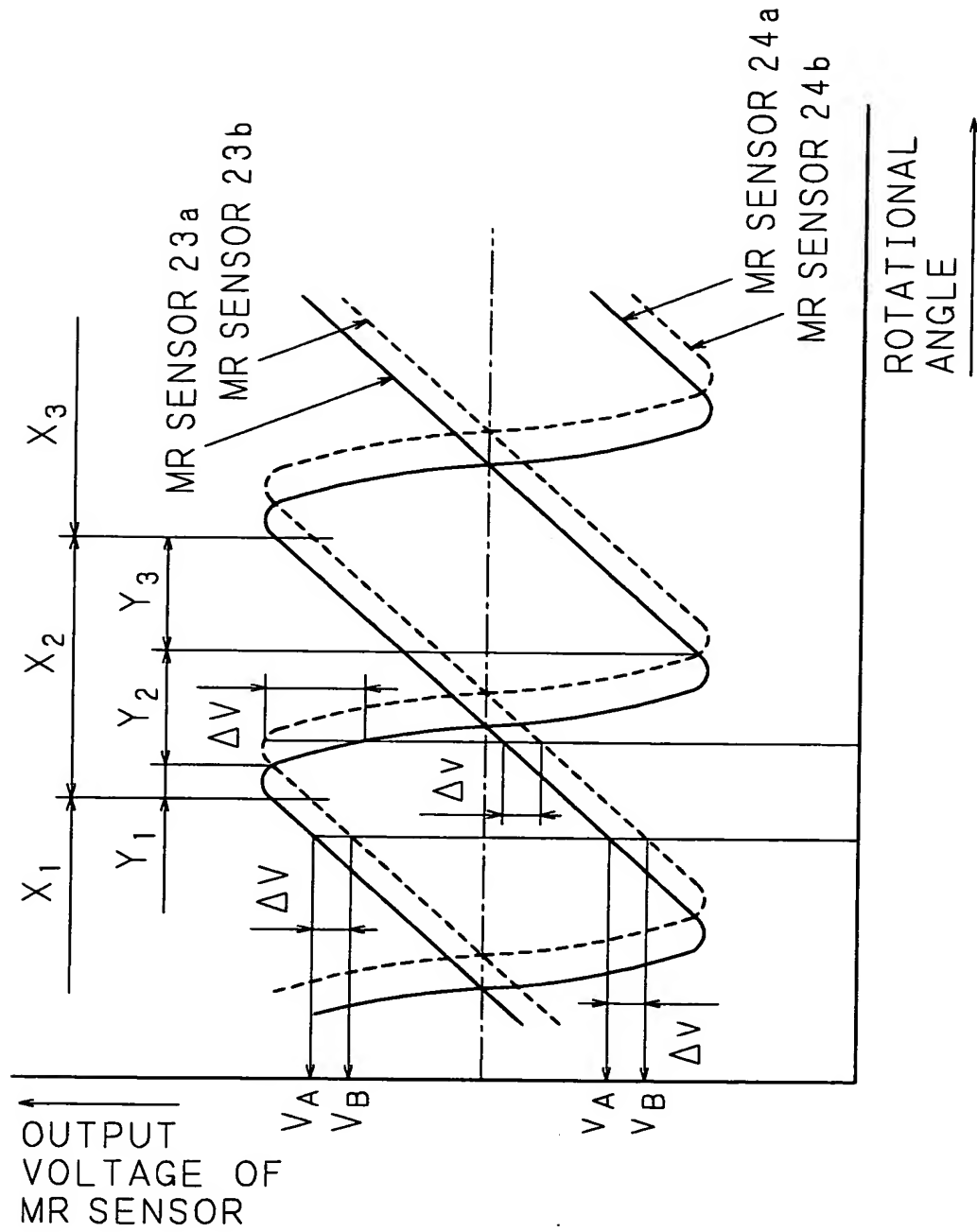
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FIG. 69



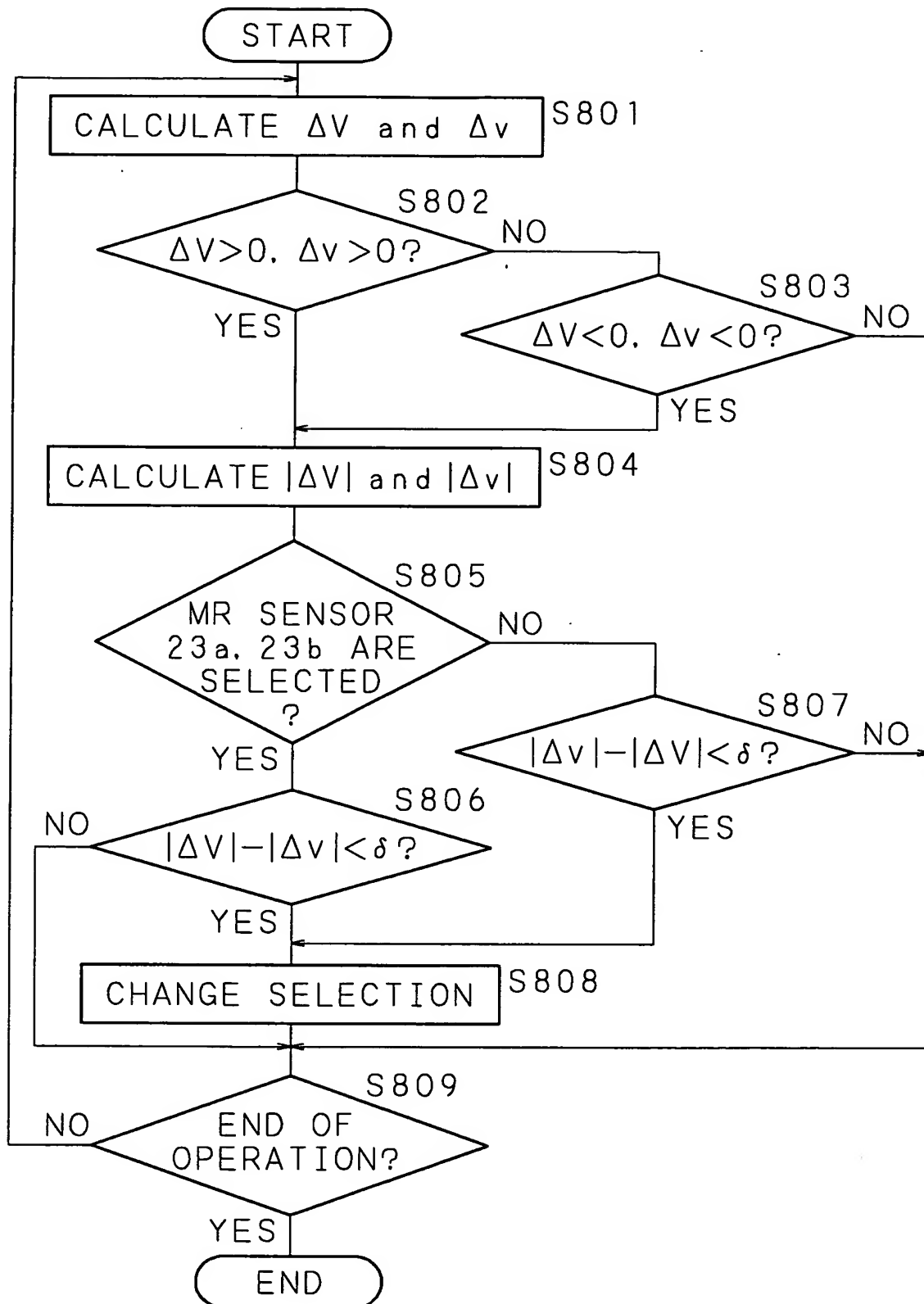
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FIG. 70



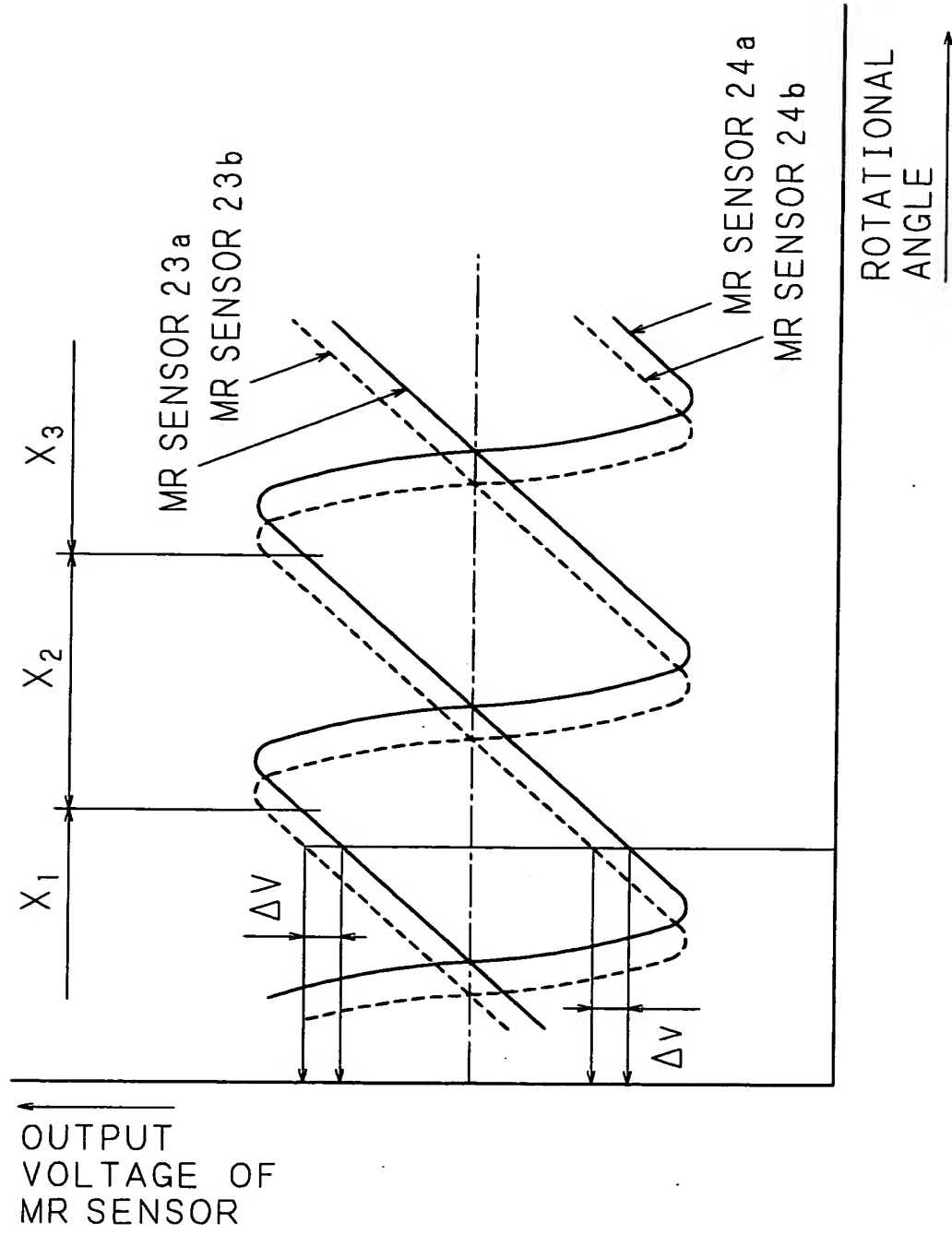
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FIG. 71



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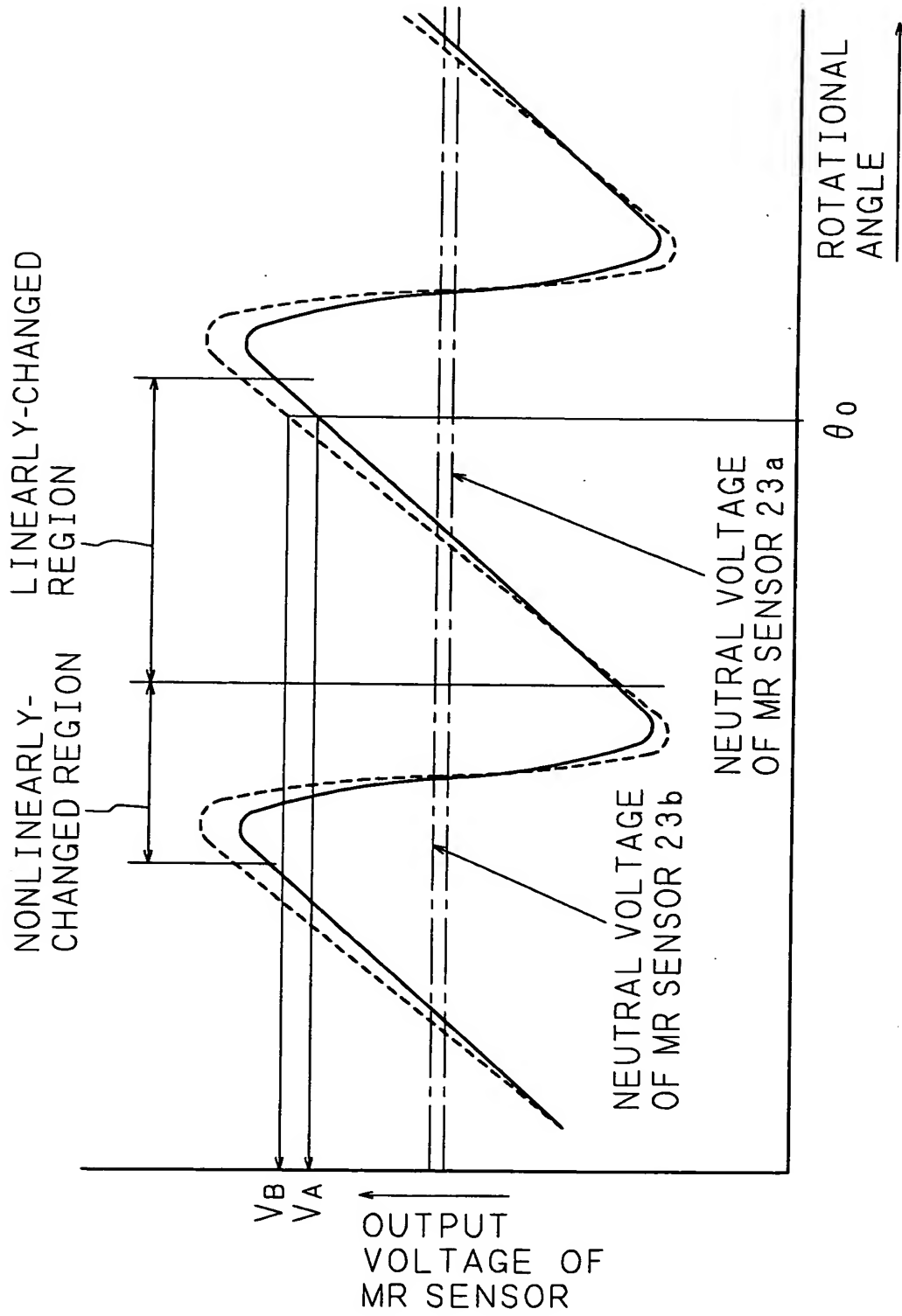
FIG. 72





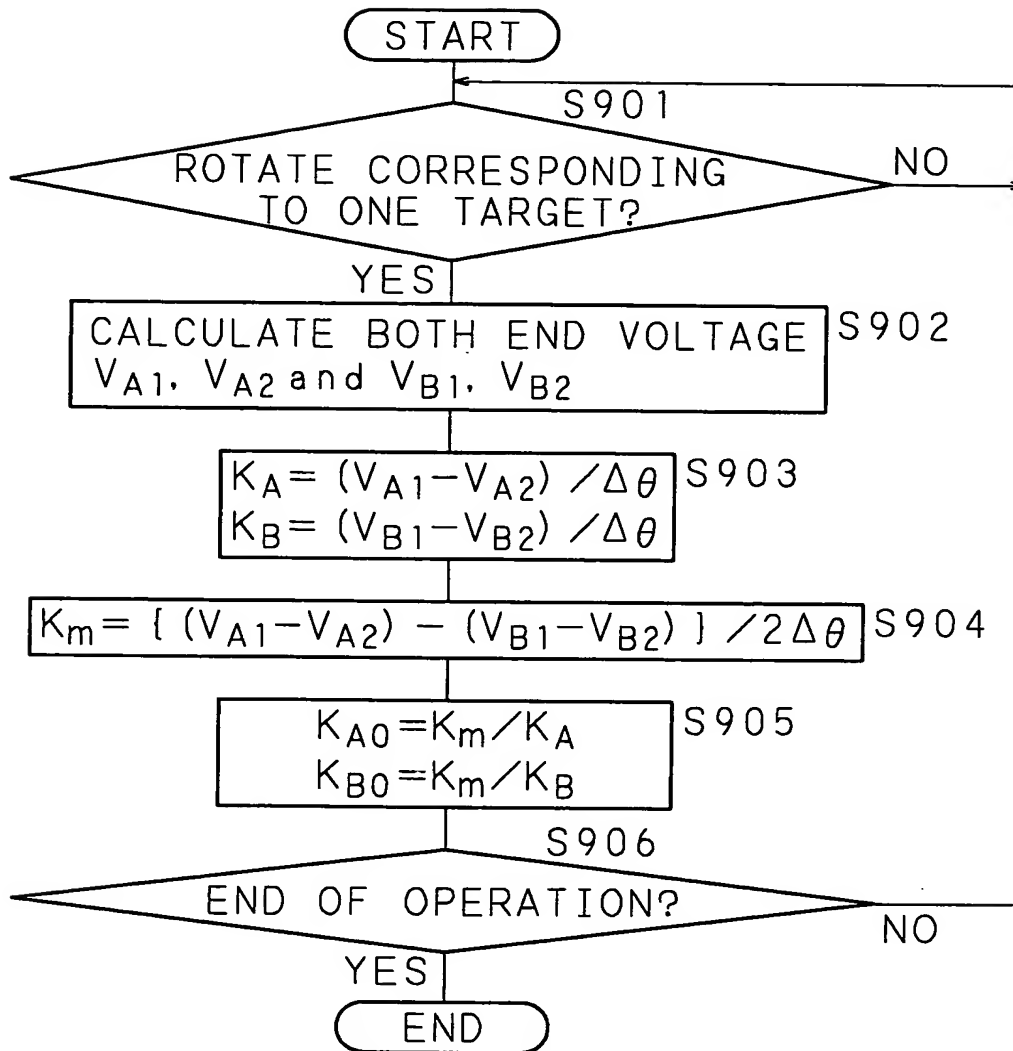
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FIG. 74



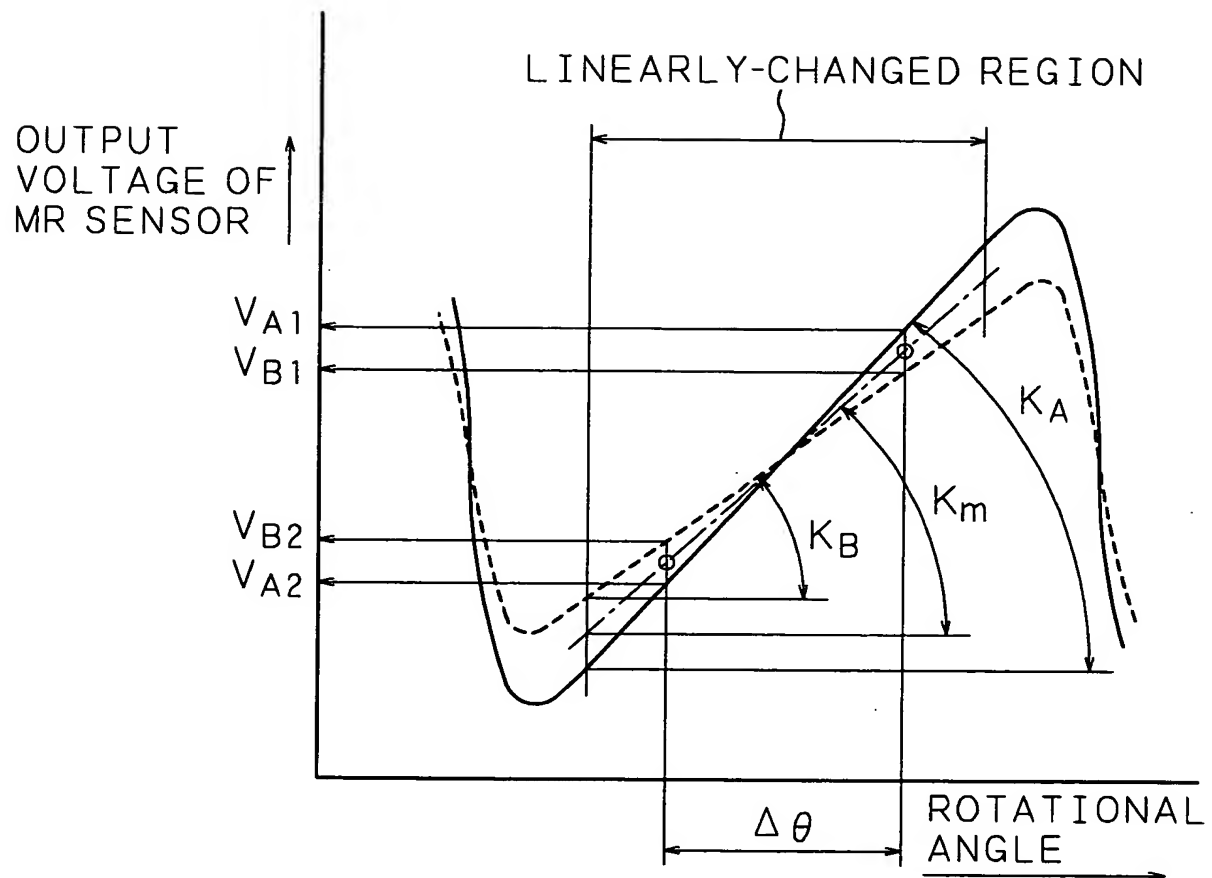
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FIG. 75



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FIG. 76





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FIG. 77

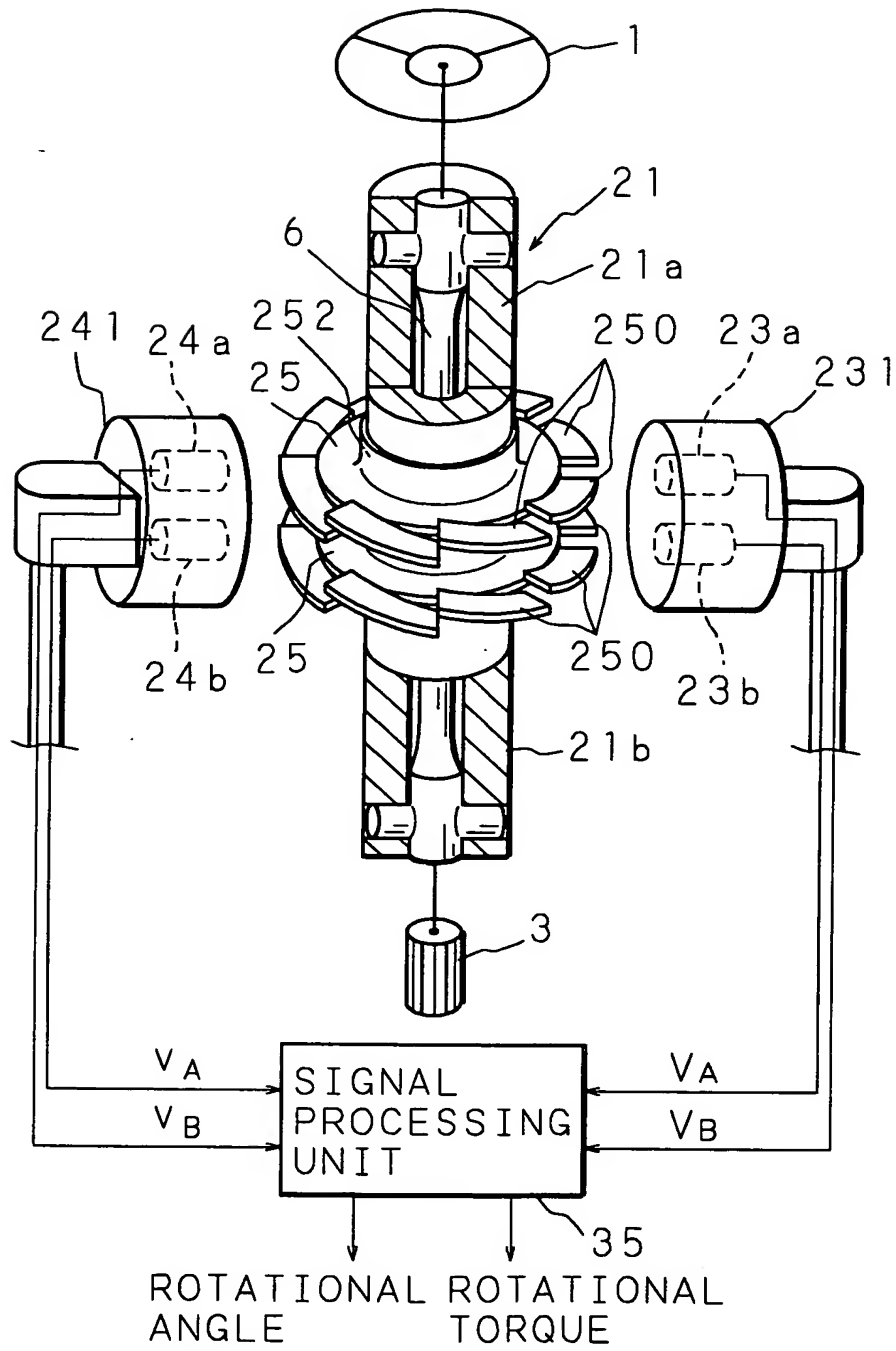
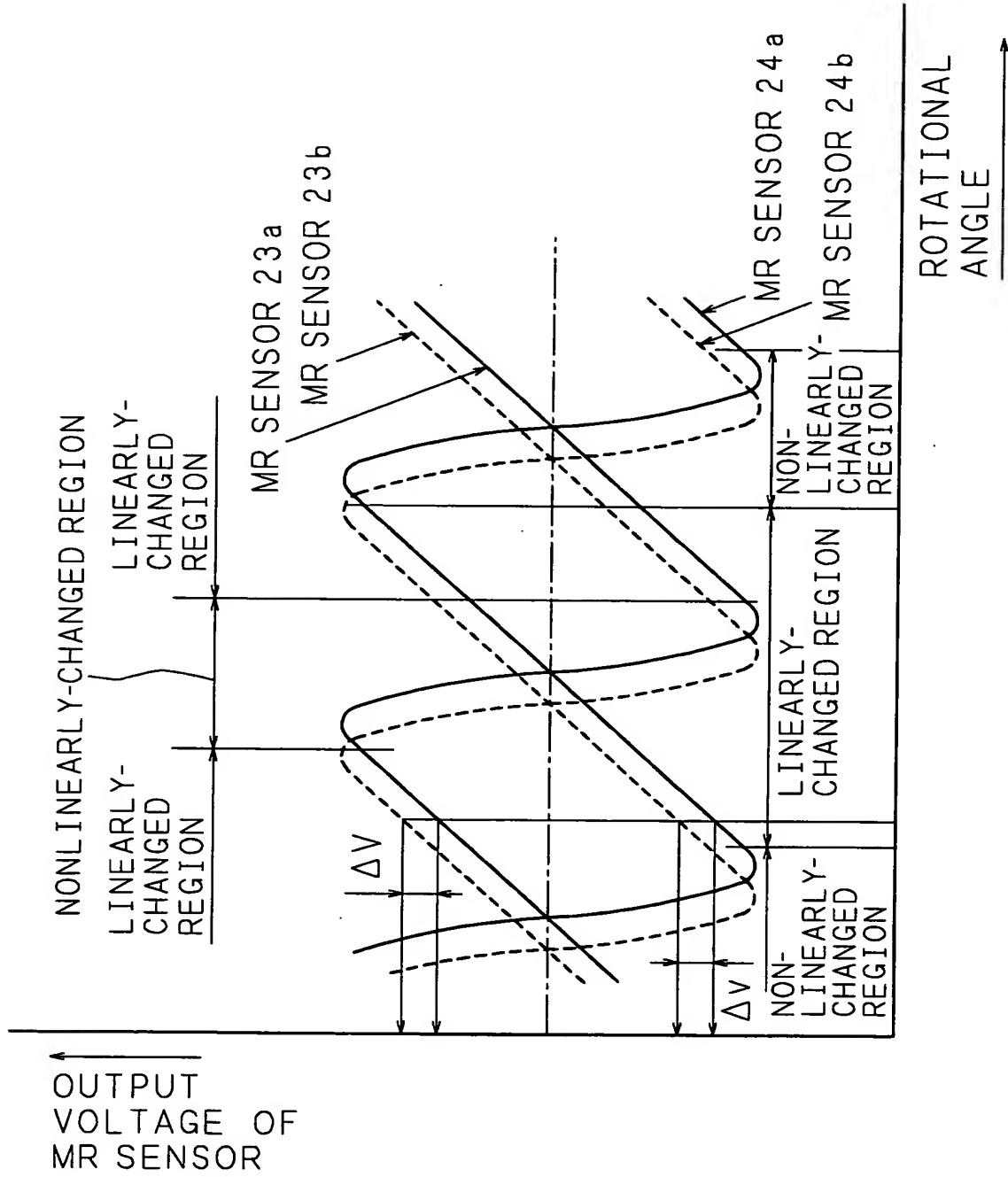


FIG. 78



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FIG. 79

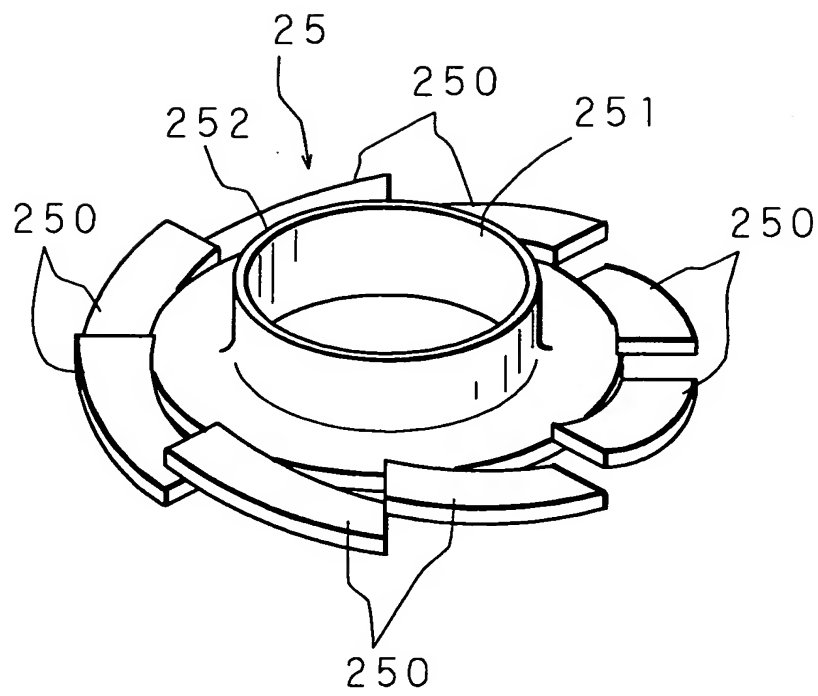


FIG. 80A

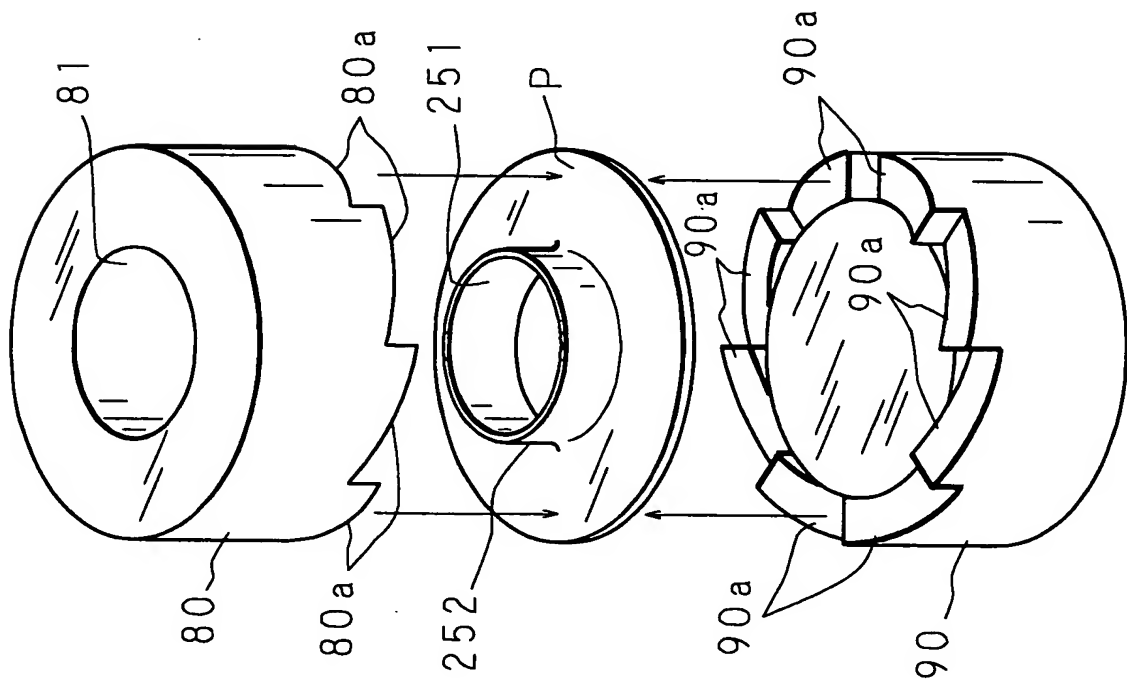
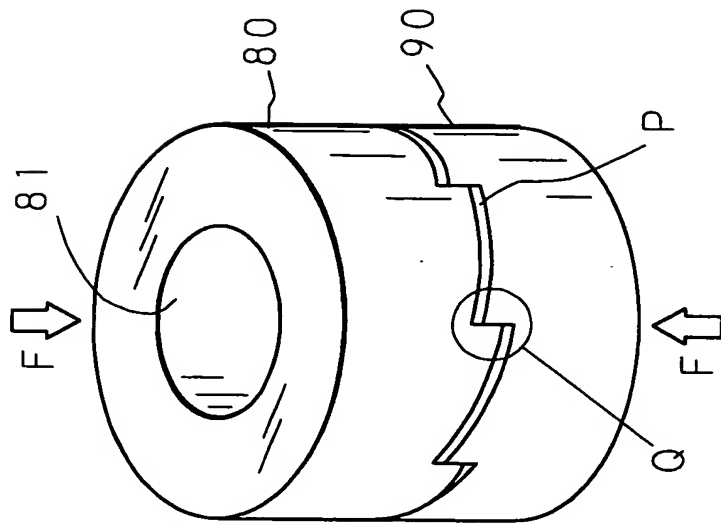


FIG. 80B



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FIG. 81

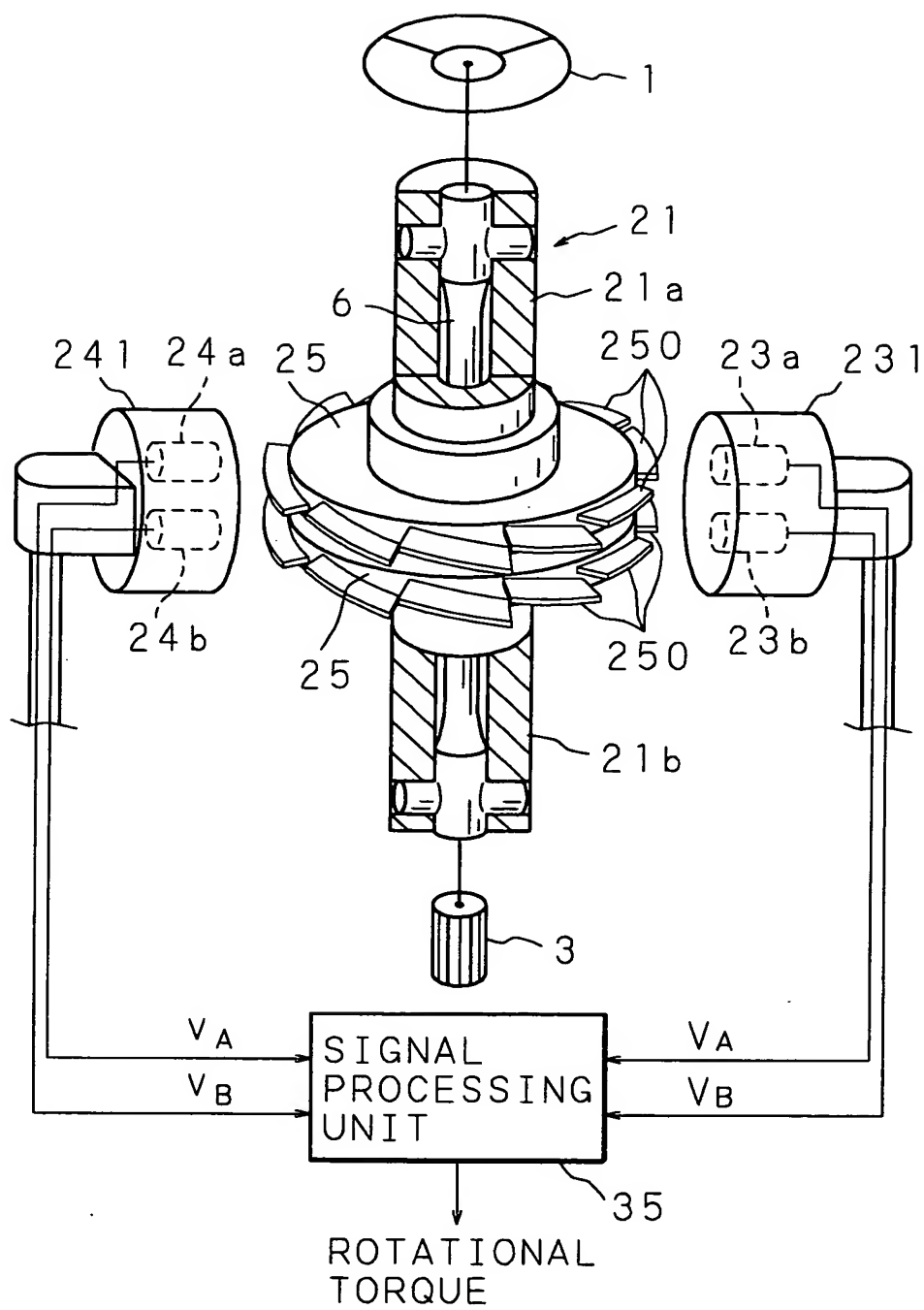


FIG. 82

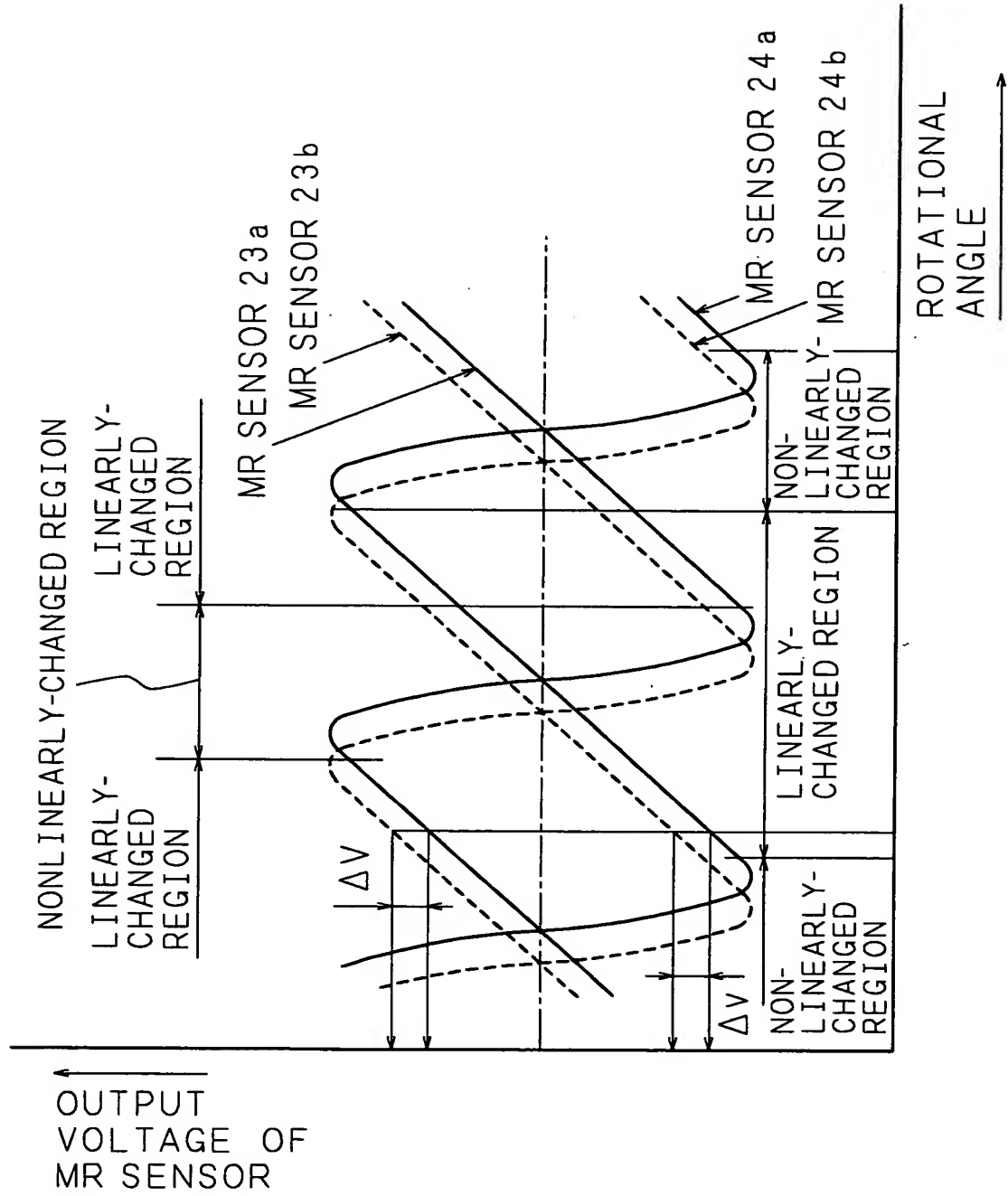
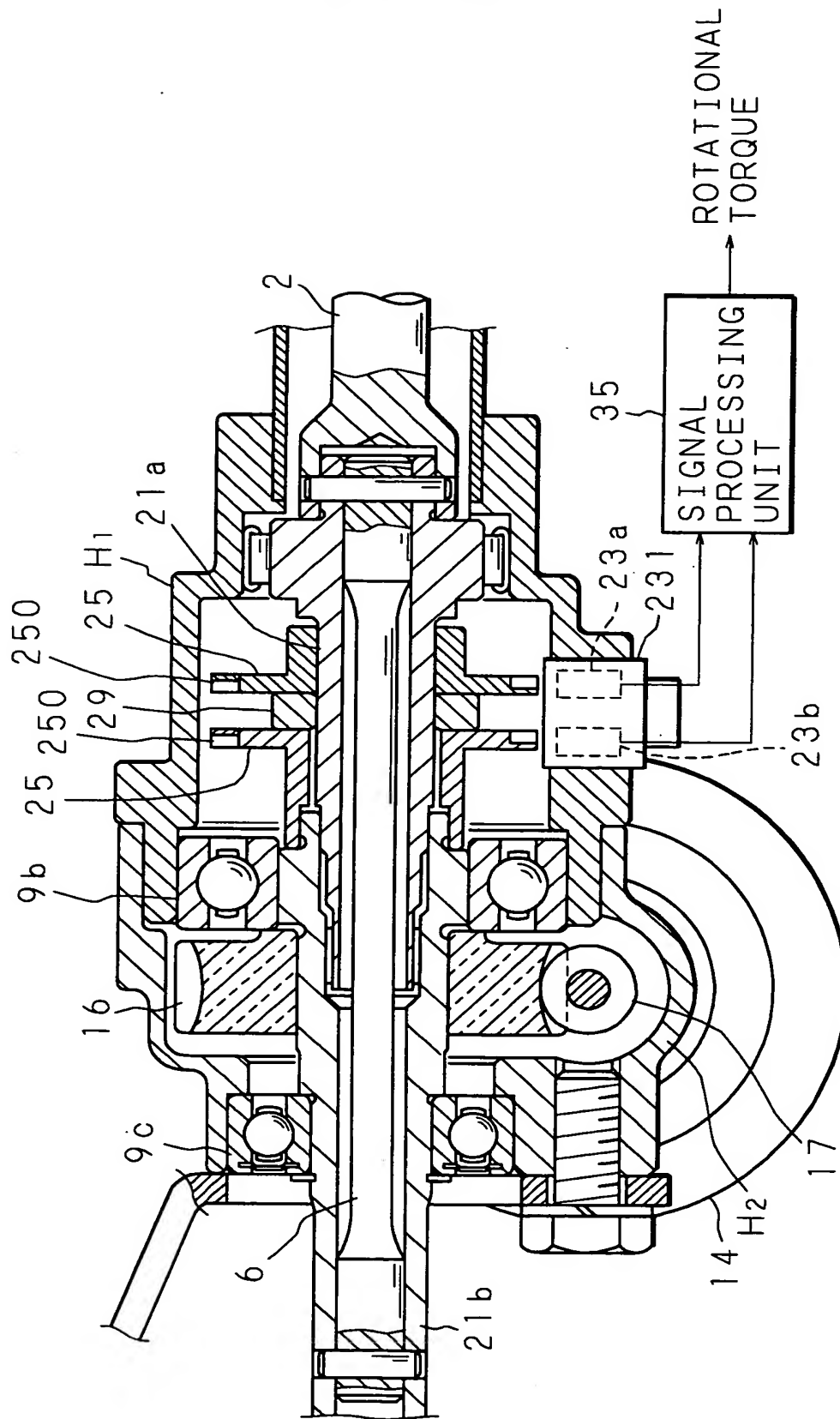




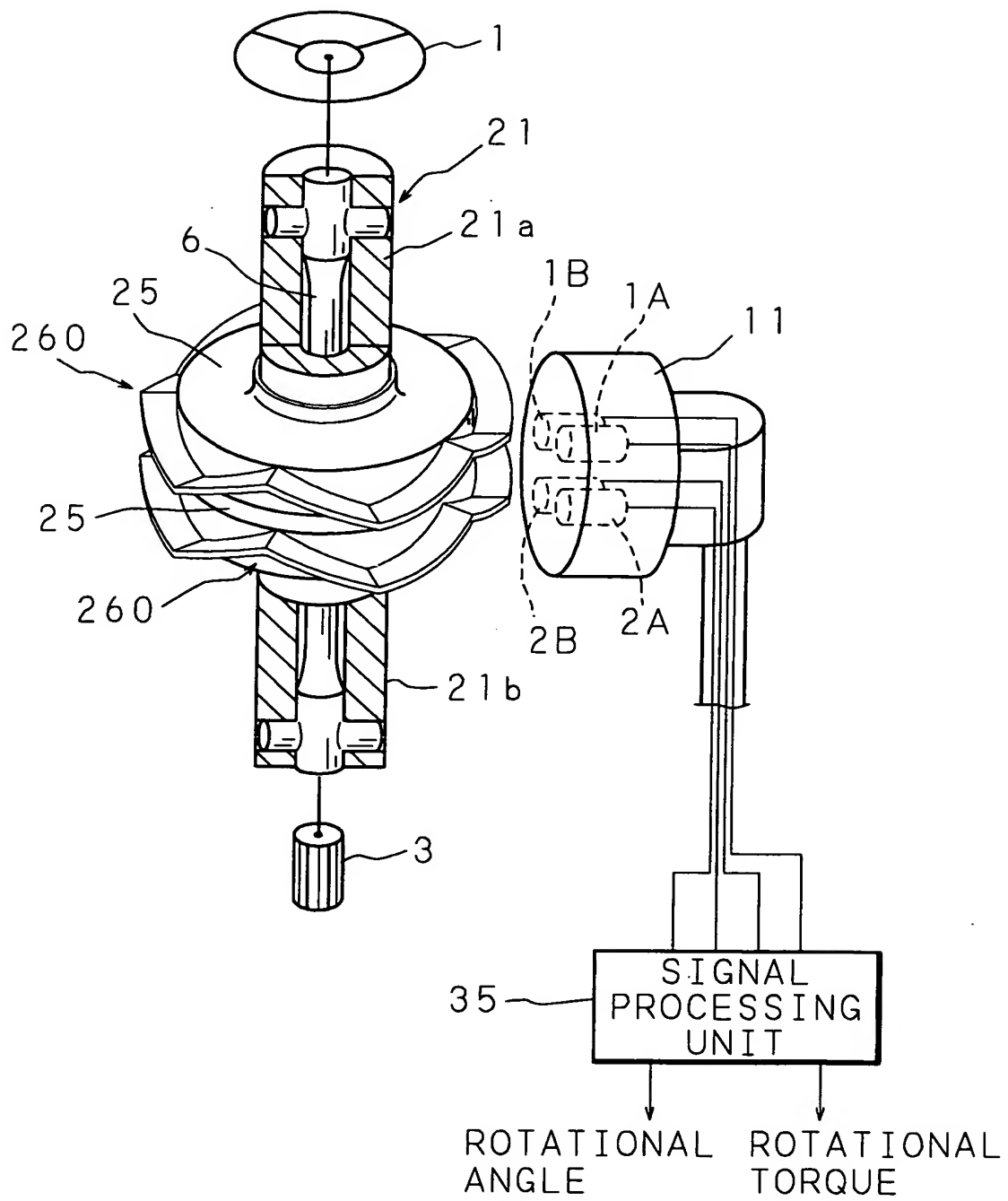
FIG. 84





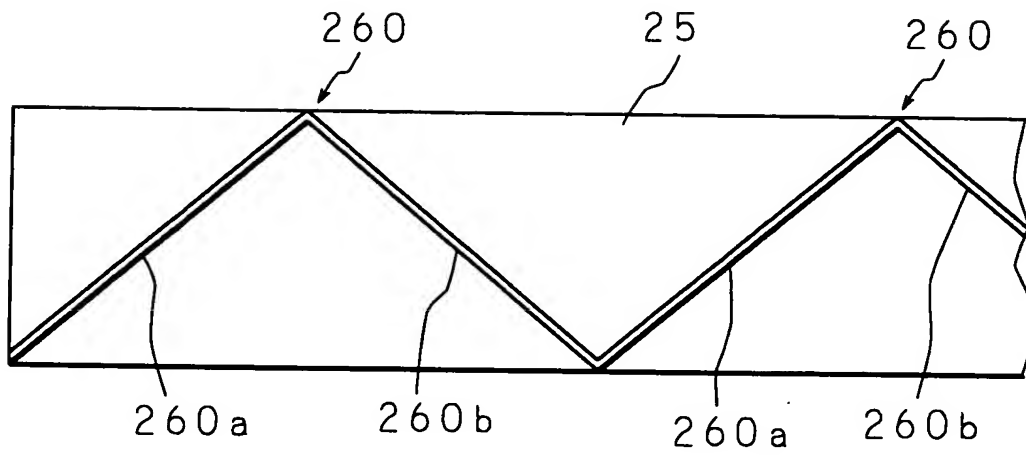
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FIG. 85



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FIG. 86



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FIG. 87

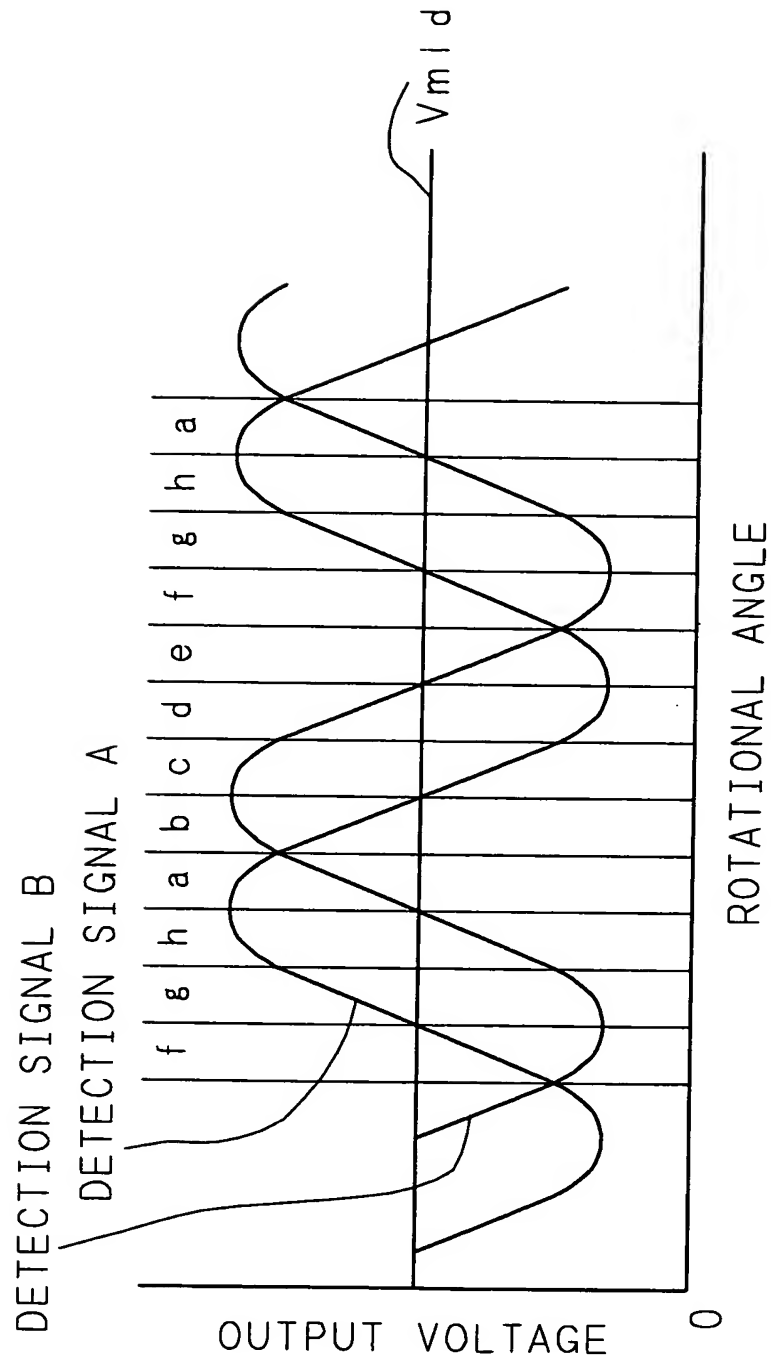
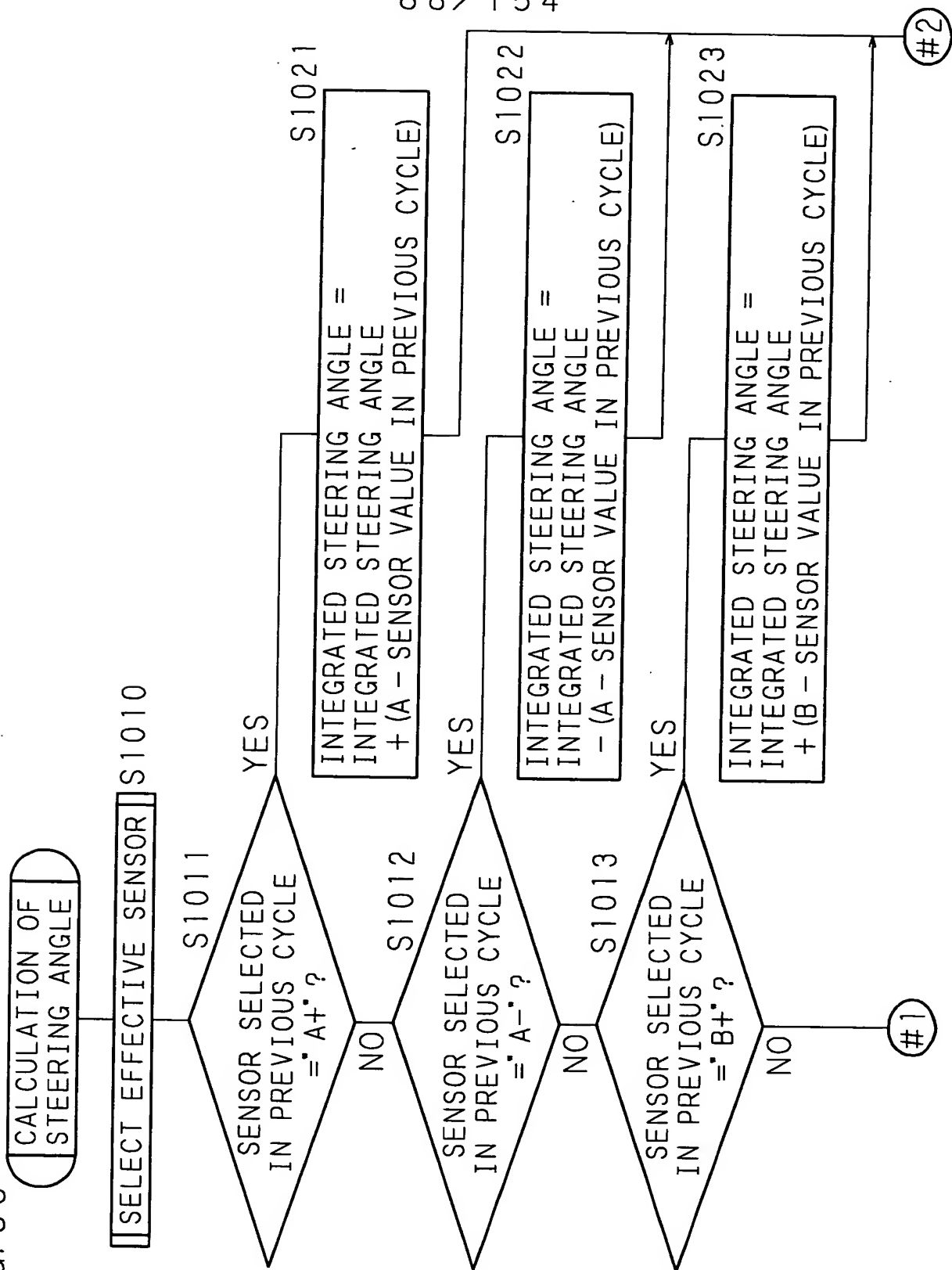


FIG. 88



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FIG. 89

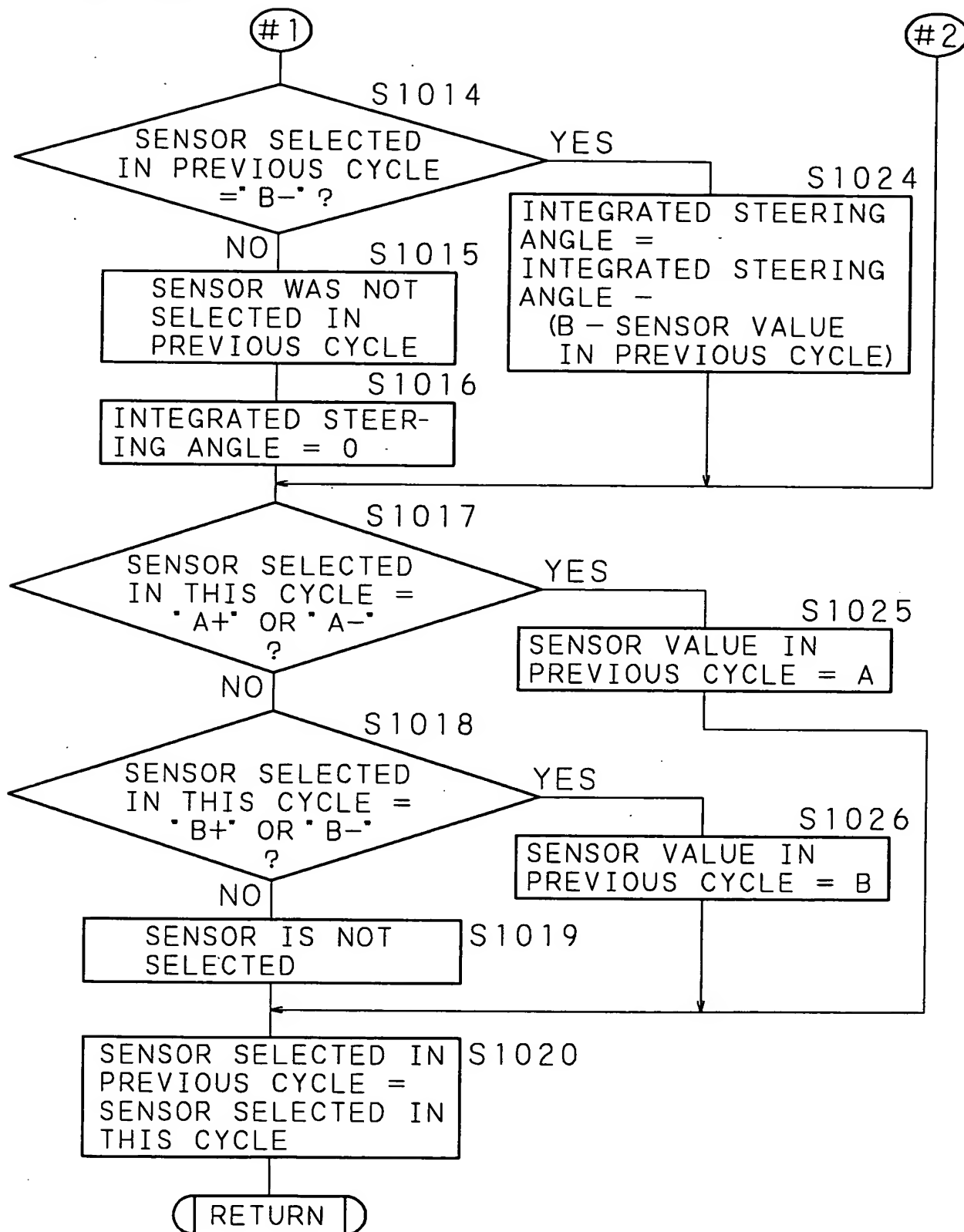


FIG. 90

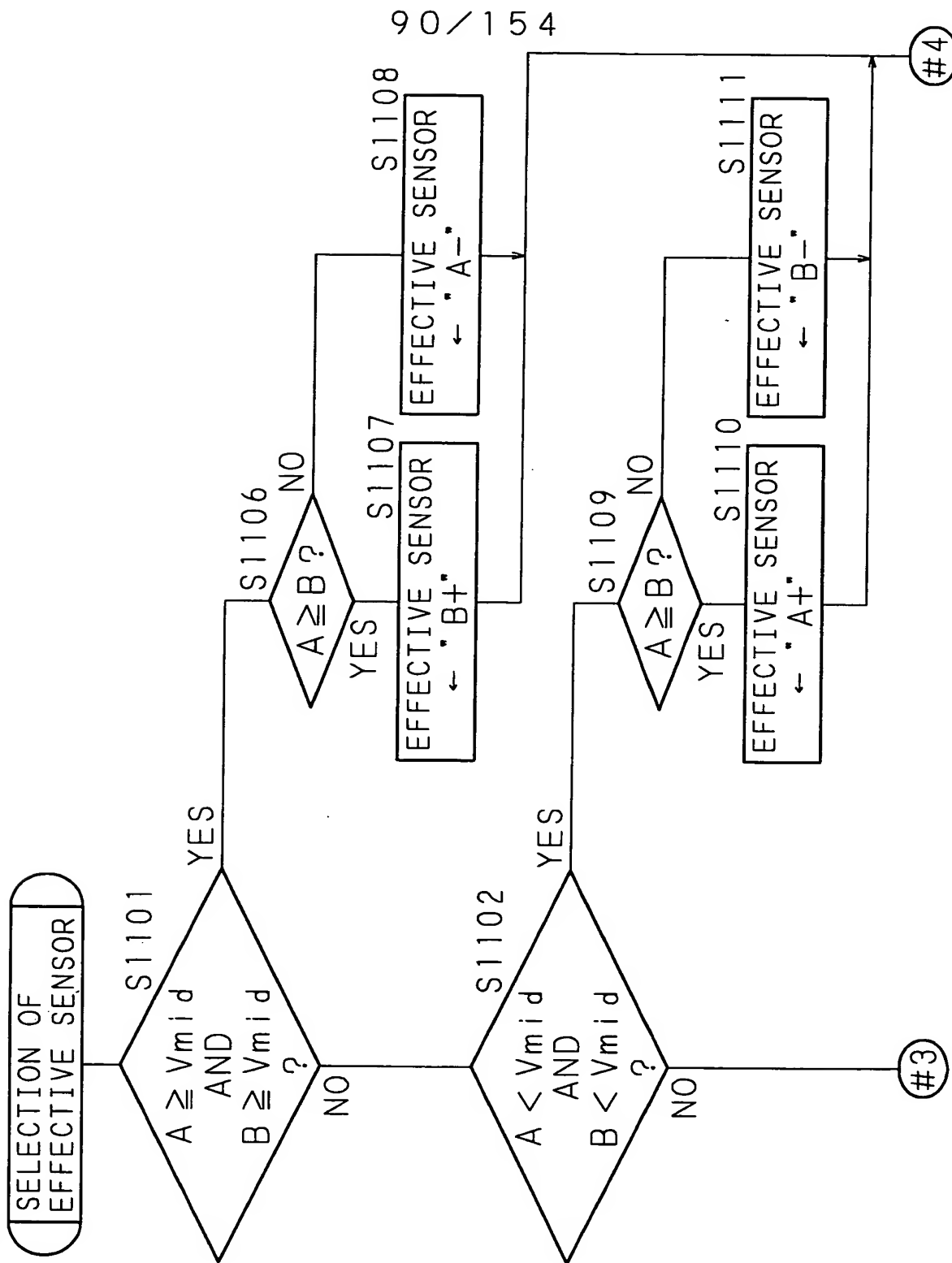
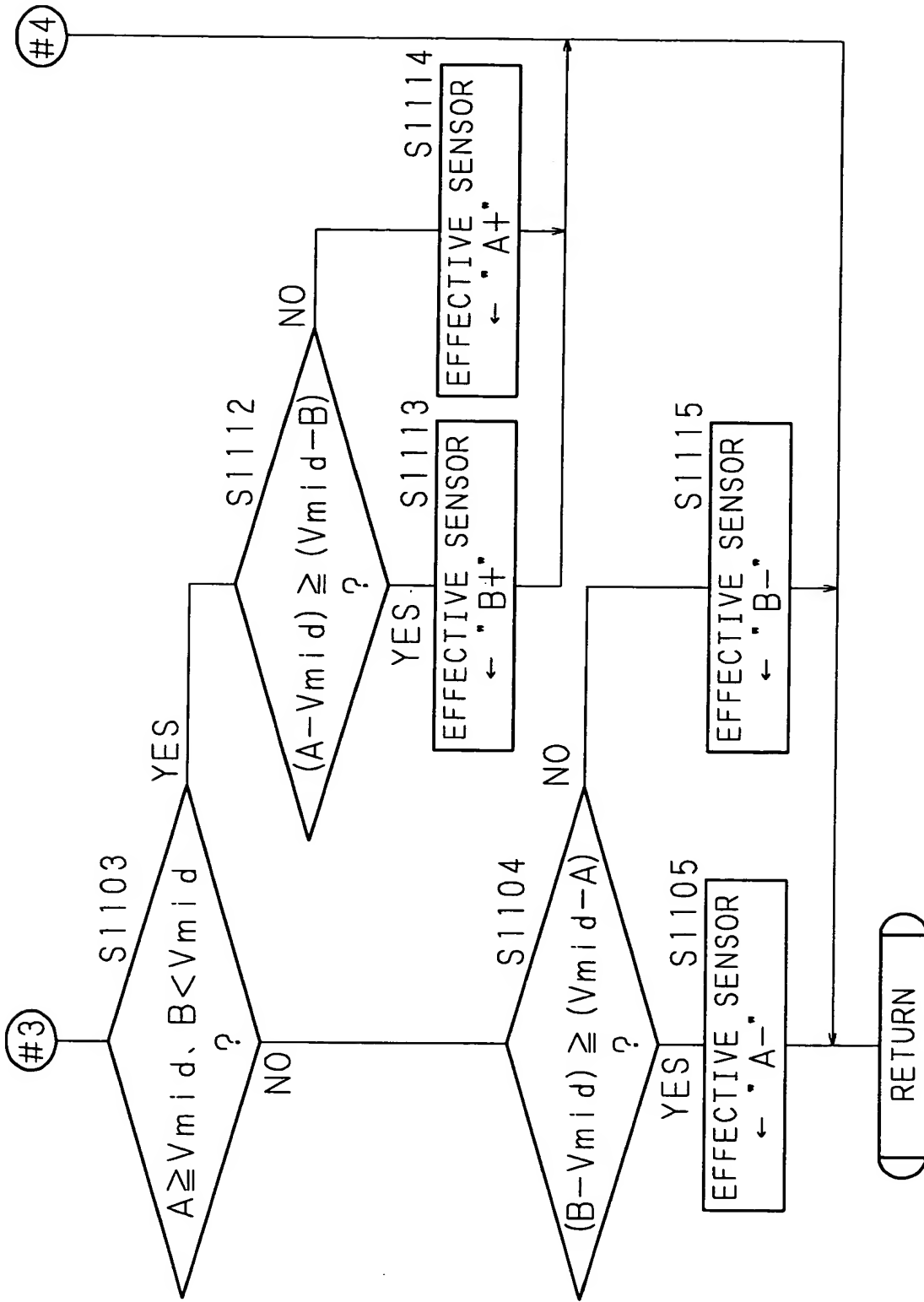
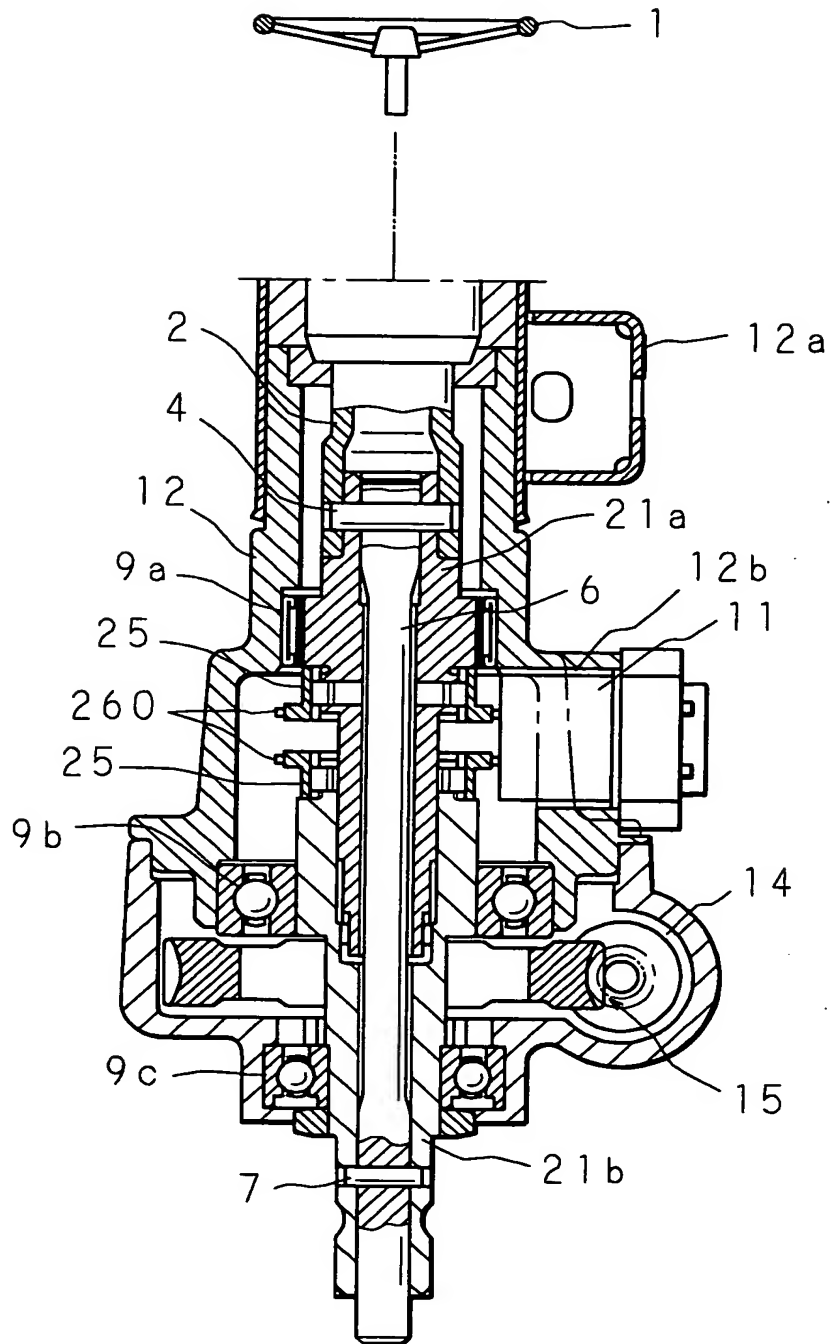


FIG. 91



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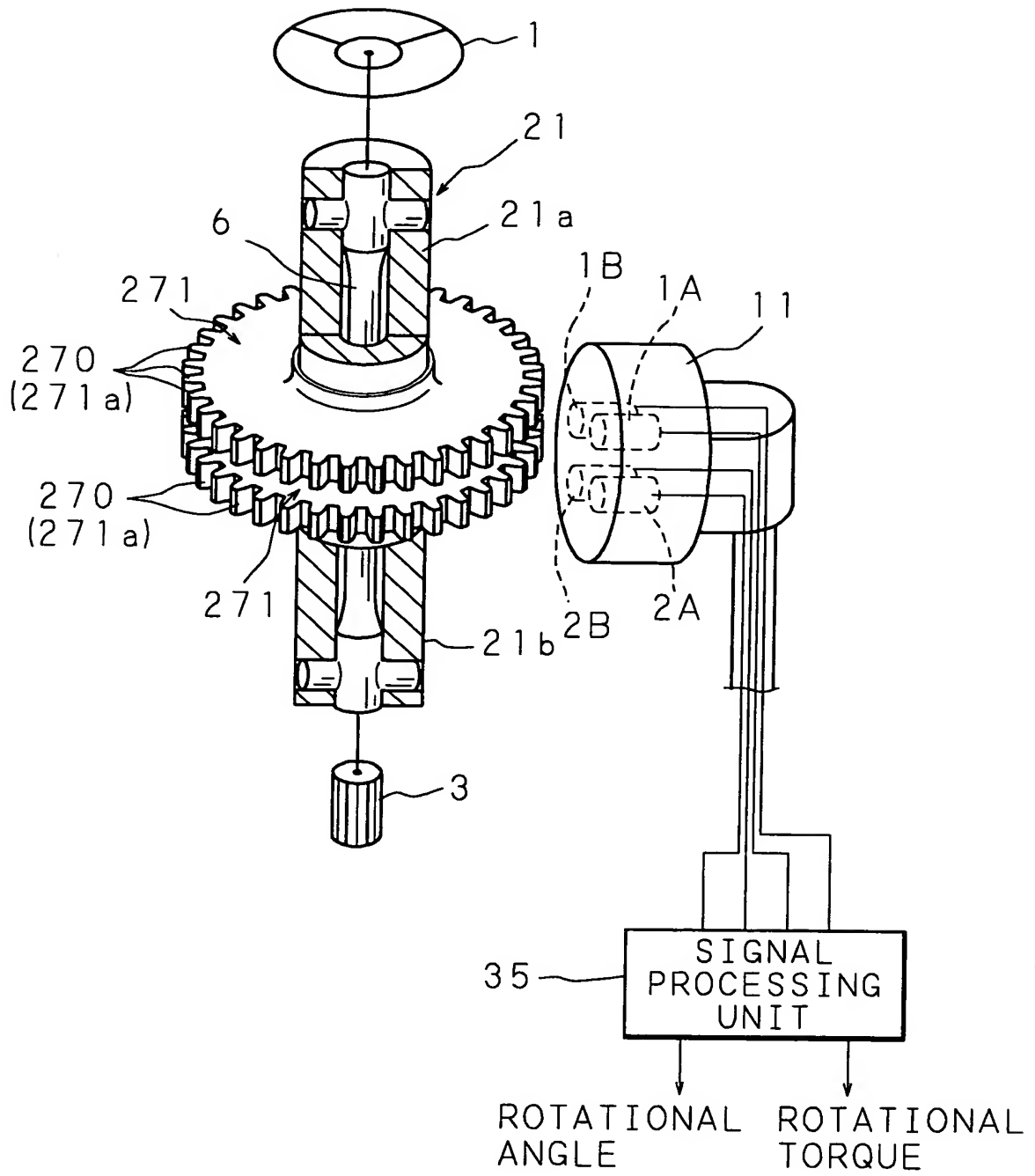
FIG. 92





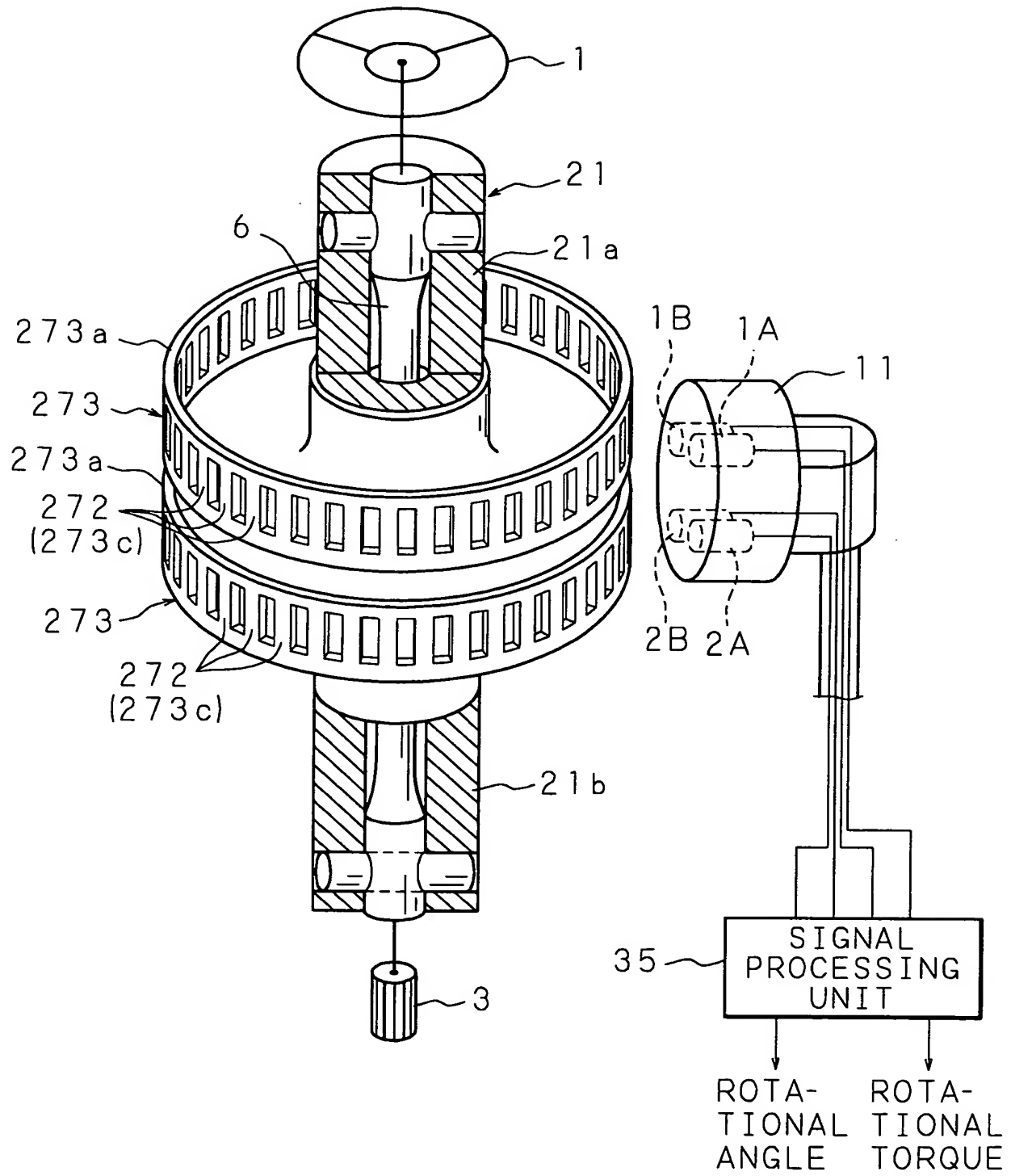
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FIG. 93



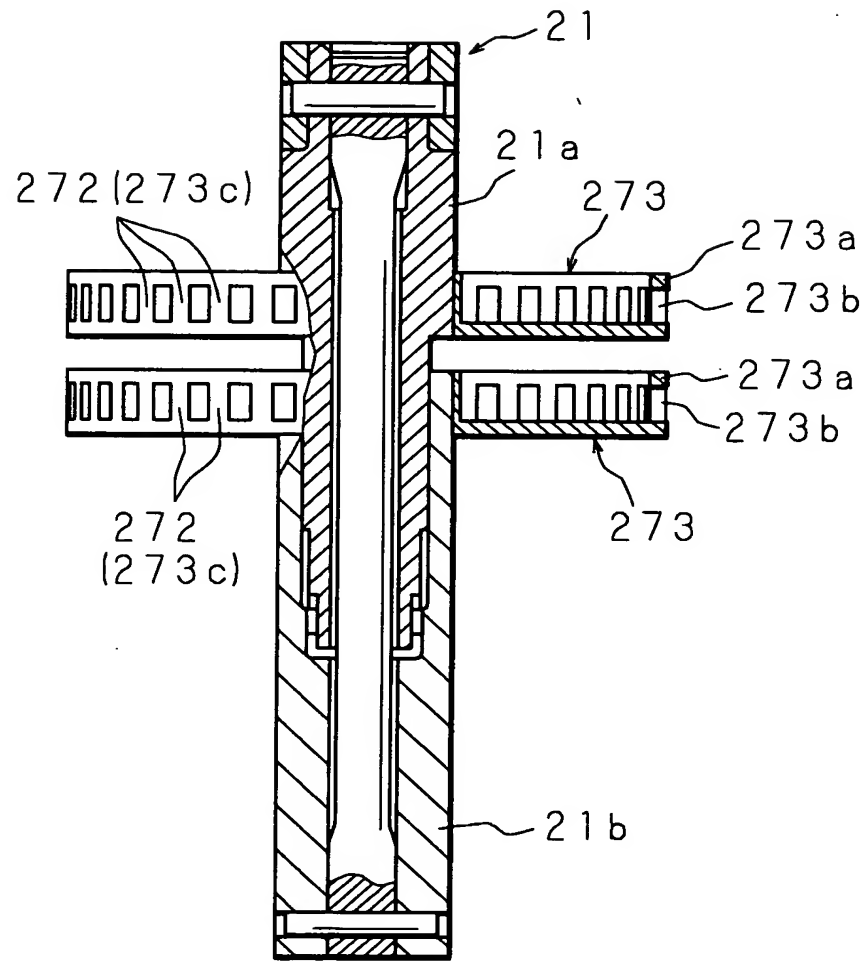
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FIG. 94



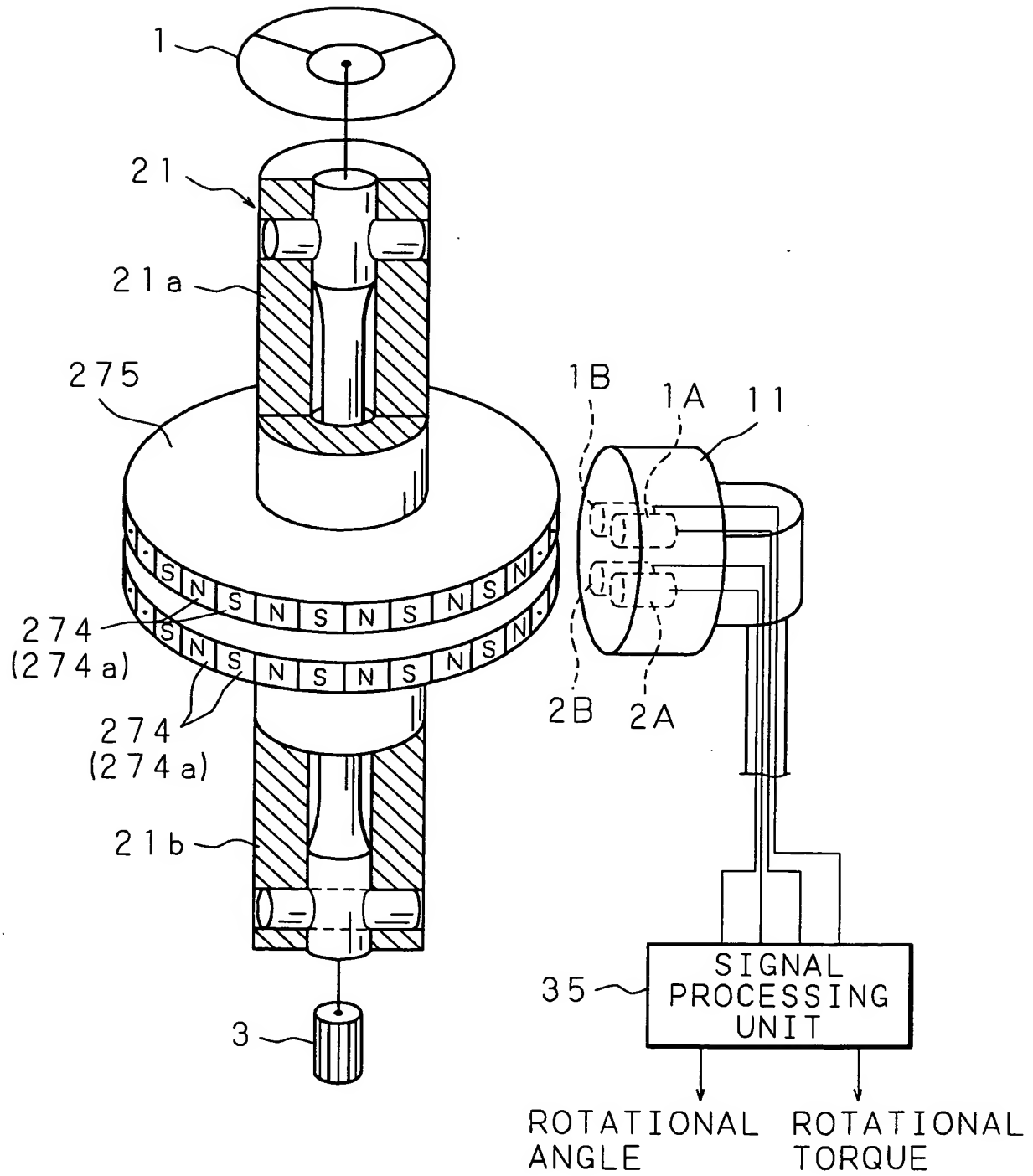
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FIG. 95



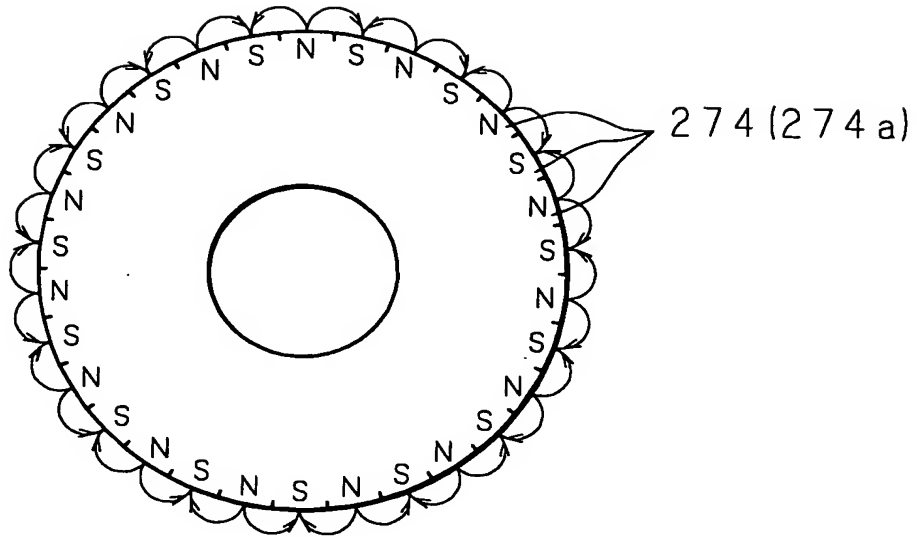
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FIG. 96



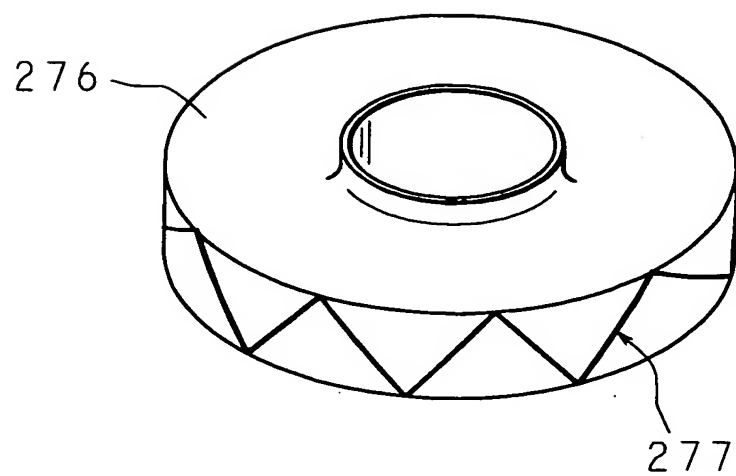
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FIG. 97



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FIG. 98



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FIG. 99

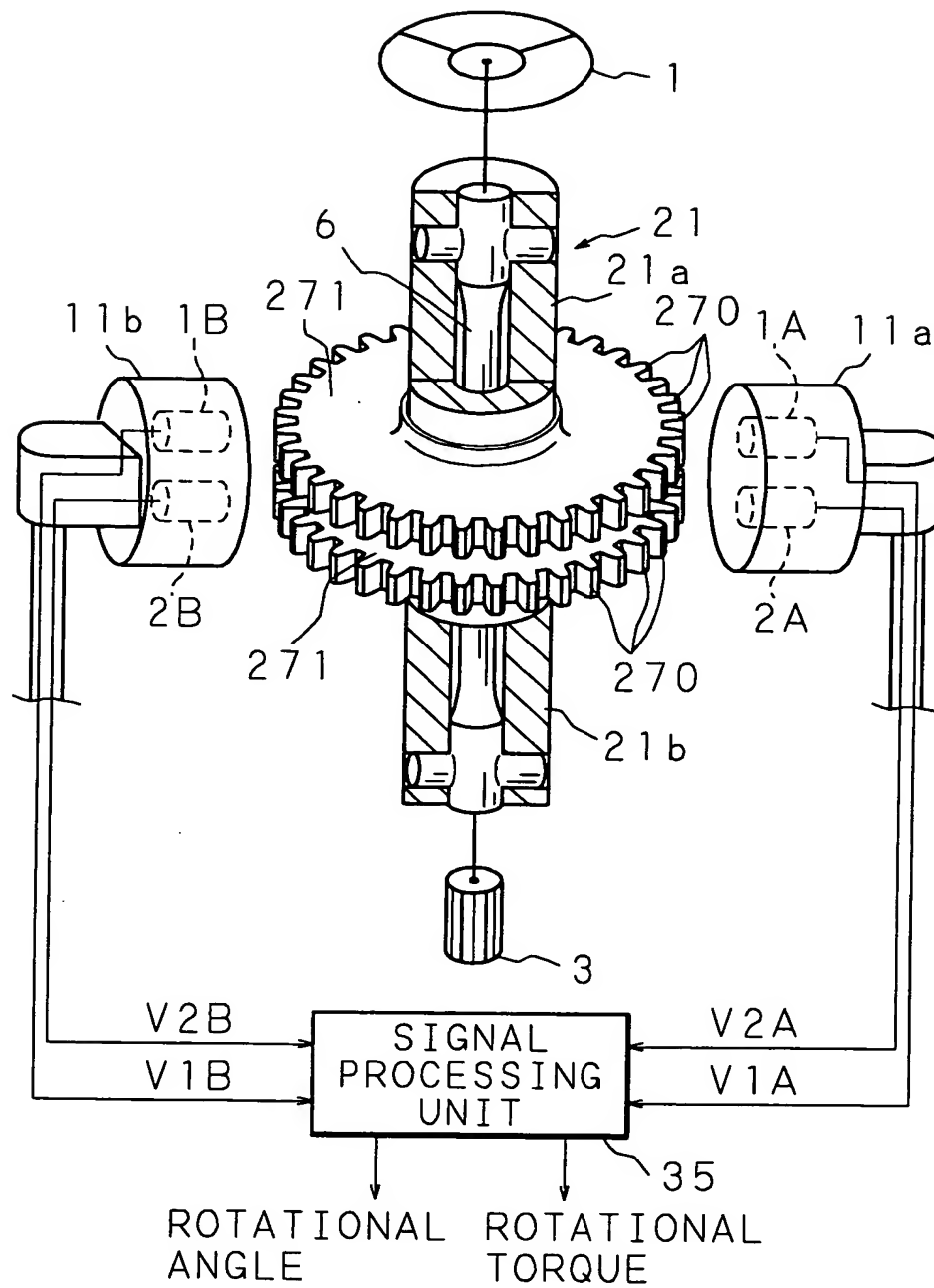
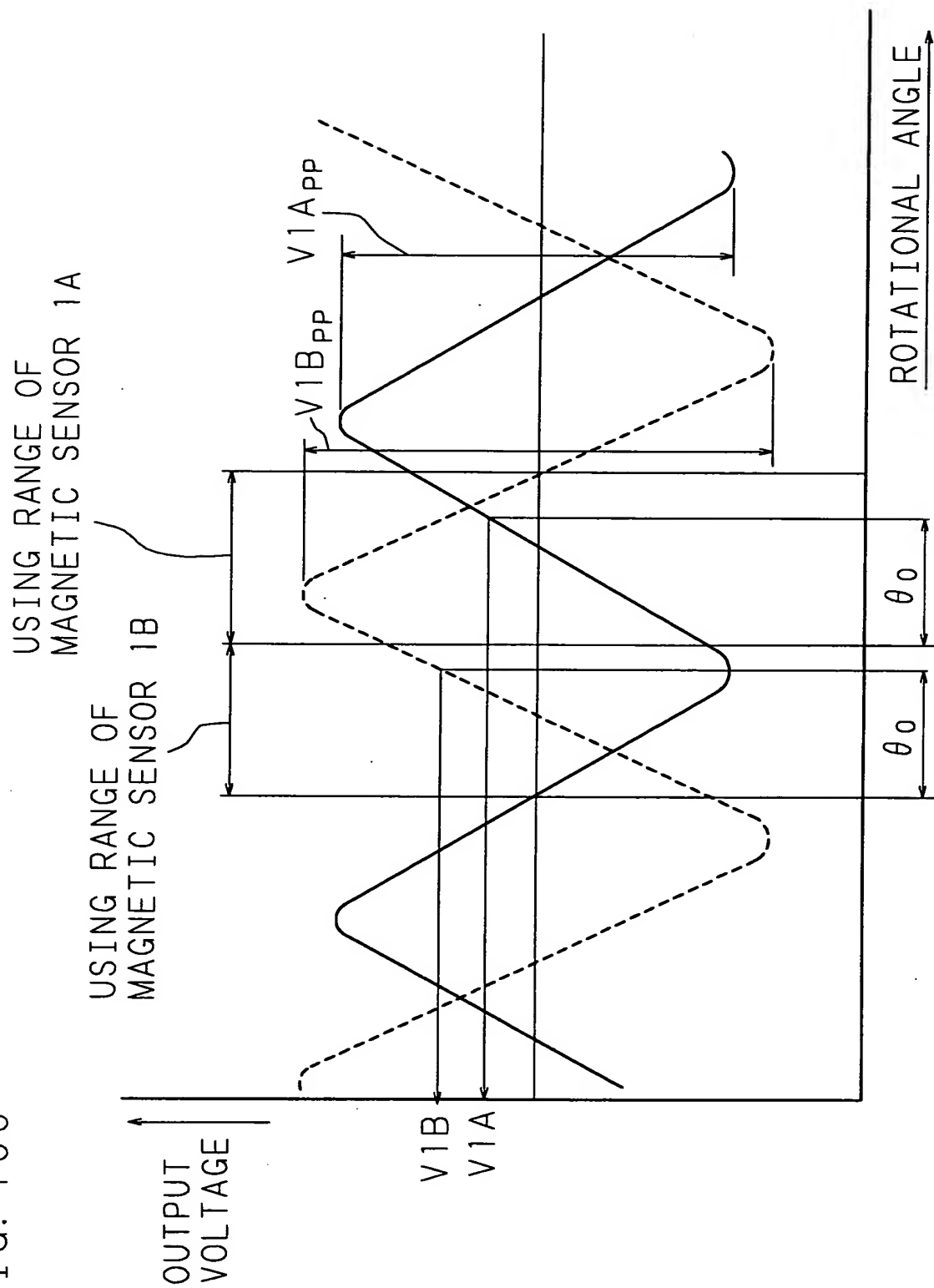


FIG. 100





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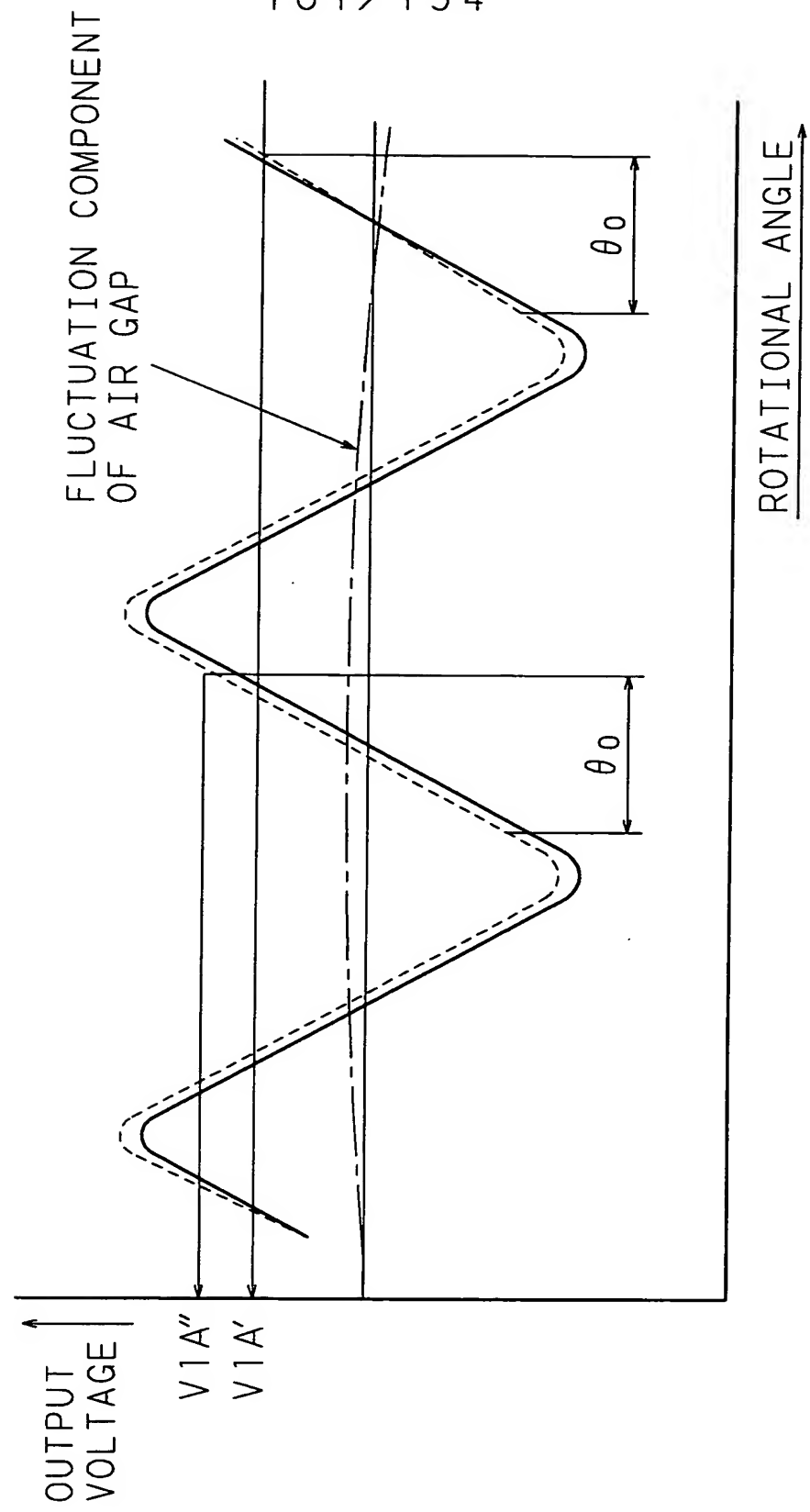
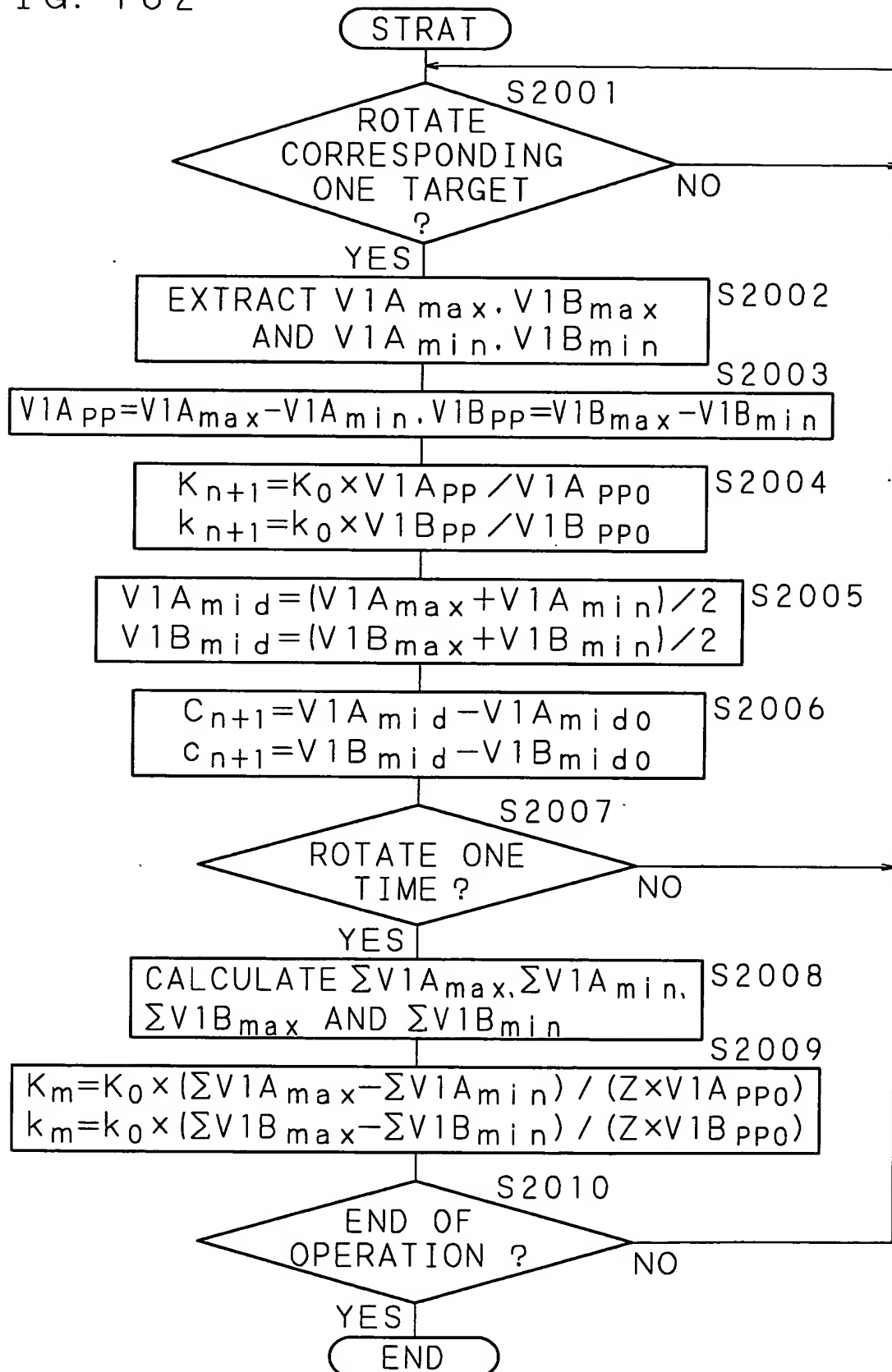


FIG. 101

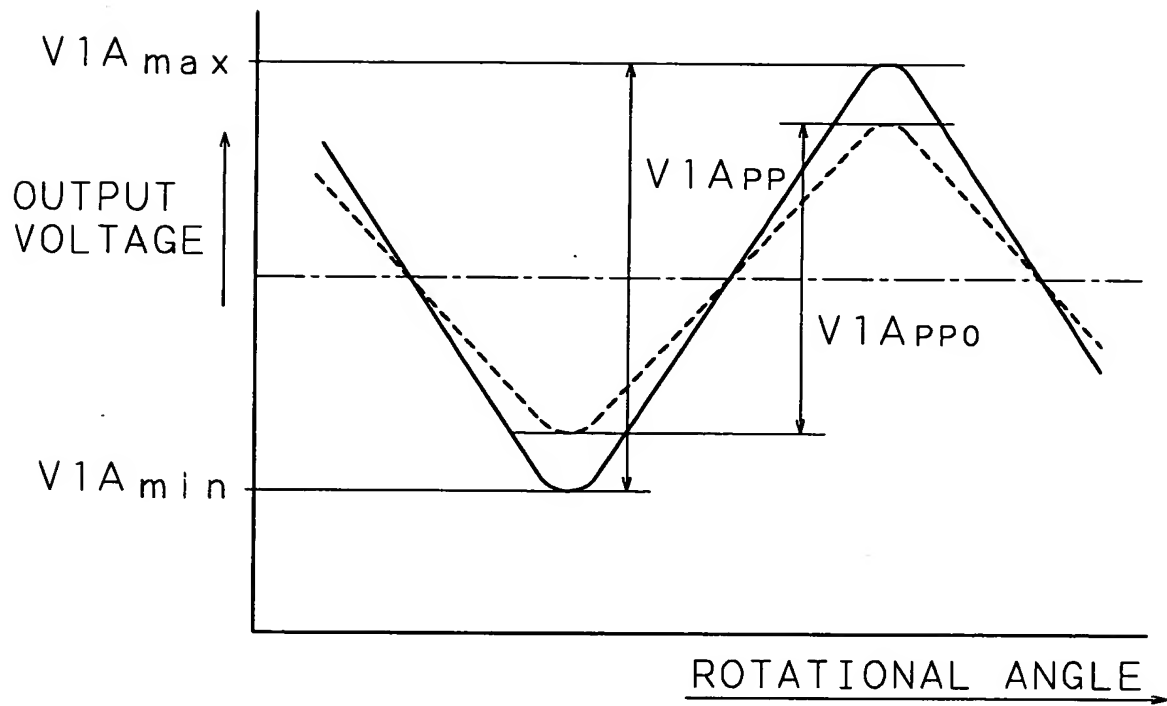
FIG. 102

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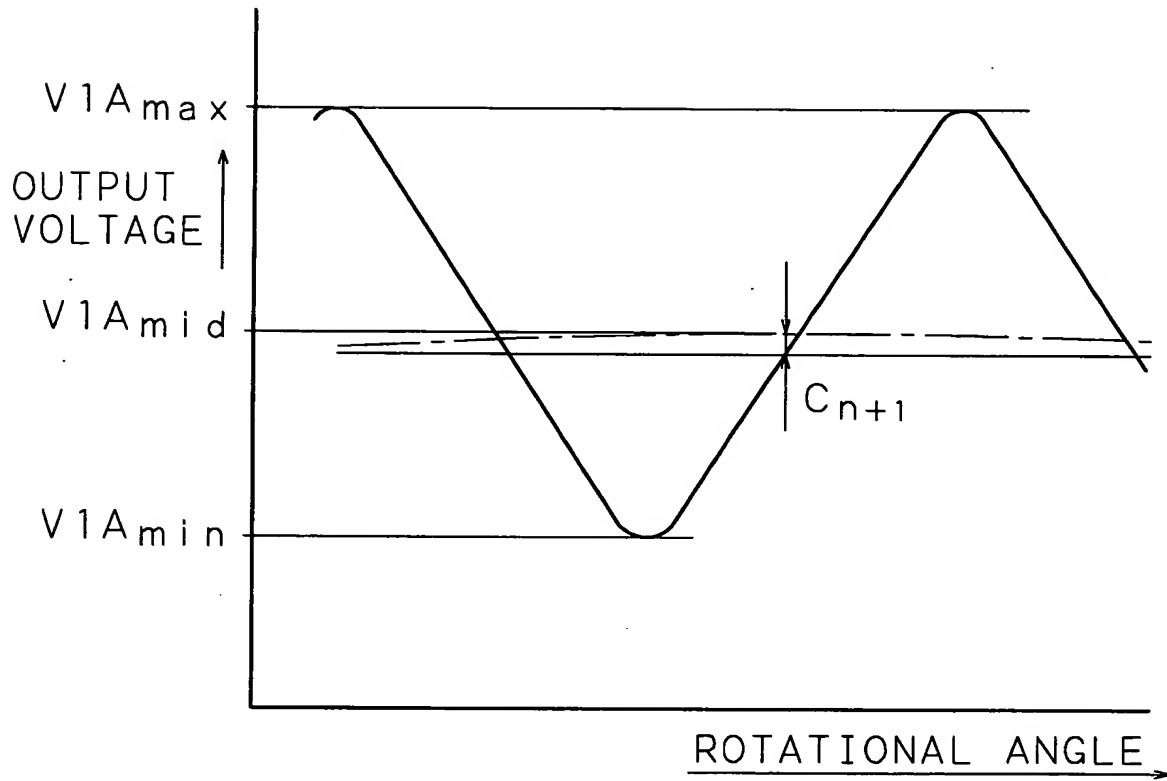
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FIG. 103



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FIG. 104



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FIG. 105

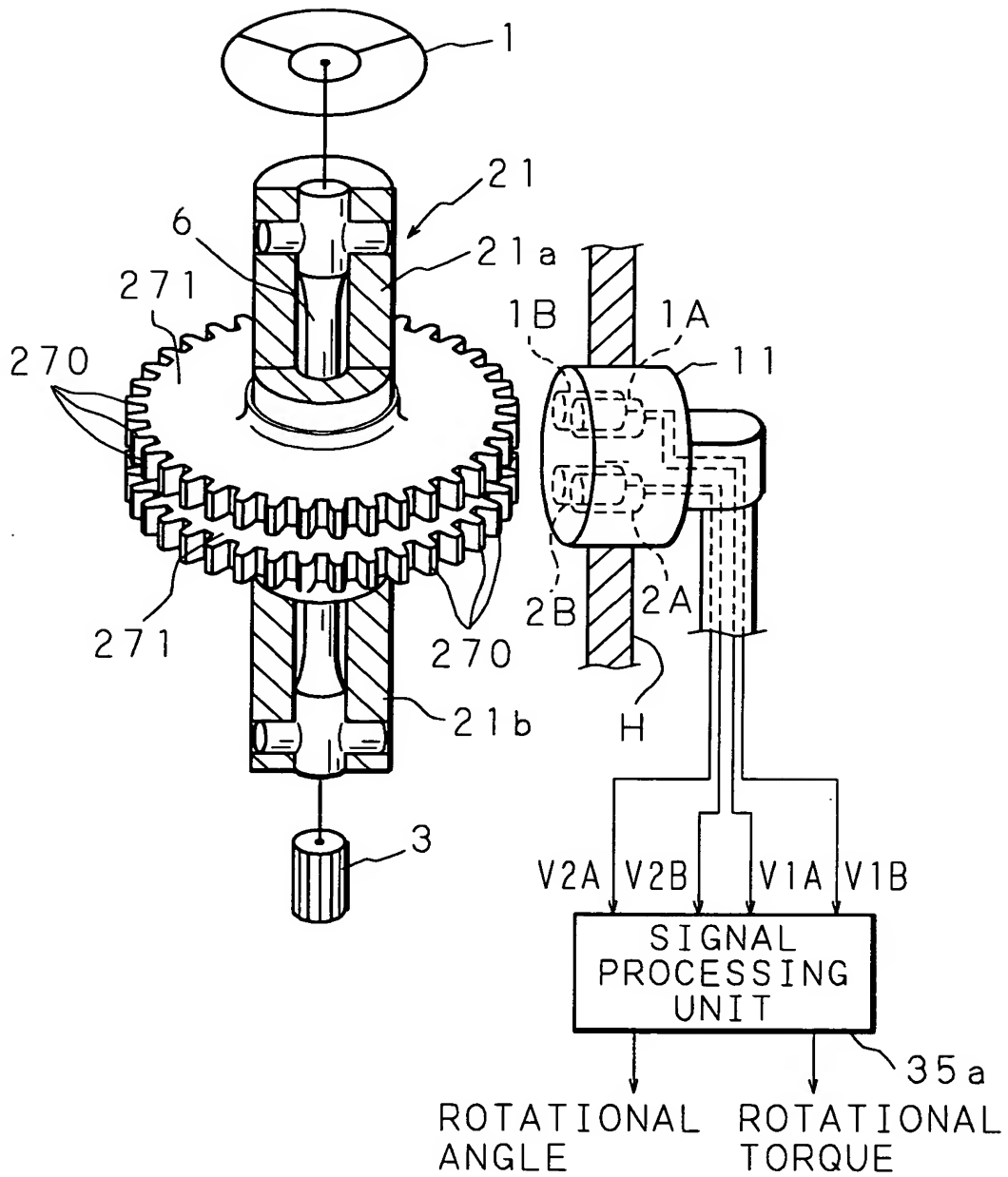
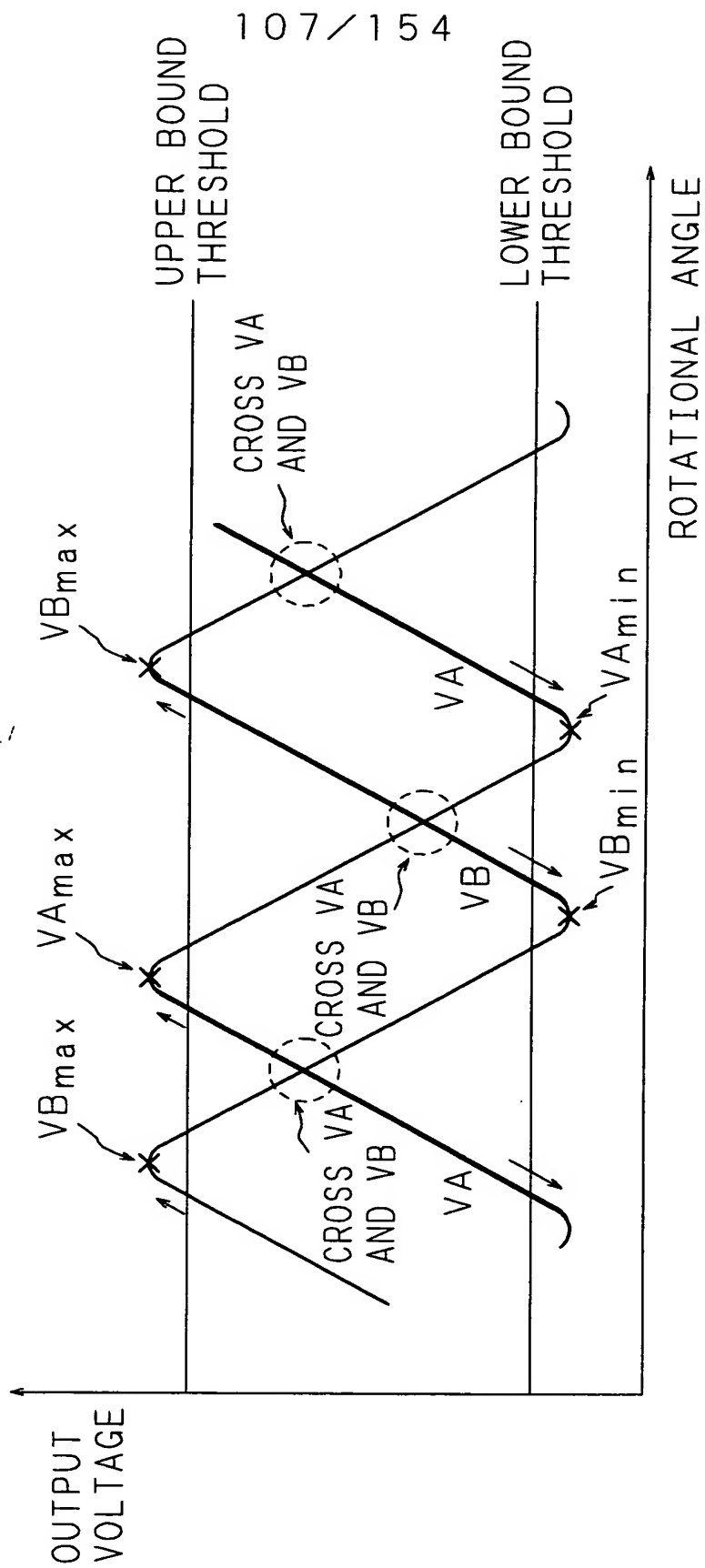


FIG. 106

DETEC- TION MODE	START CONDITION OF DETECTION	DEFINITION / INTERRUPTION	OPERATION	MODE AFTER CHANGED
0	INITIAL MODE (NOT YET DETECTED)	*	*	*
1	VA > UPPER BOUND THRESHOLD VA <sub>max</sub> START OF DETECTION	CROSS VA AND VB	VA <sub>max</sub> IS DEFINED	2
		VB < LOWER BOUND THRESHOLD	INTERRUPTION	6
2	VB AT LINEAR REGION. CROSS VA AND VB VA <sub>min</sub> START OF DETECTION	VB > UPPER BOUND THRESHOLD	VA <sub>min</sub> IS DEFINED	4
3	VA < LOWER BOUND THRESHOLD VA <sub>min</sub> START OF DETECTION	CROSS VA AND VB	VA <sub>min</sub> IS DEFINED	2
		VB > UPPER BOUND THRESHOLD	INTERRUPTION	4
4	VB > UPPER BOUND THRESHOLD VB <sub>max</sub> START OF DETECTION	CROSS VA AND VB	VB <sub>max</sub> IS DEFINED	5
		VA < LOWER BOUND THRESHOLD	INTERRUPTION	3
5	VA AT LINEAR REGION. CROSS VA AND VB VB <sub>max</sub> START OF DETECTION	VA < LOWER BOUND THRESHOLD	VB <sub>max</sub> IS DEFINED	3
6	VB < LOWER BOUND THRESHOLD VB <sub>min</sub> START OF DETECTION	CROSS VA AND VB	VB <sub>min</sub> IS DEFINED	5
		VA > UPPER BOUND THRESHOLD	INTERRUPTION	1

FIG. 107



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FIG. 108

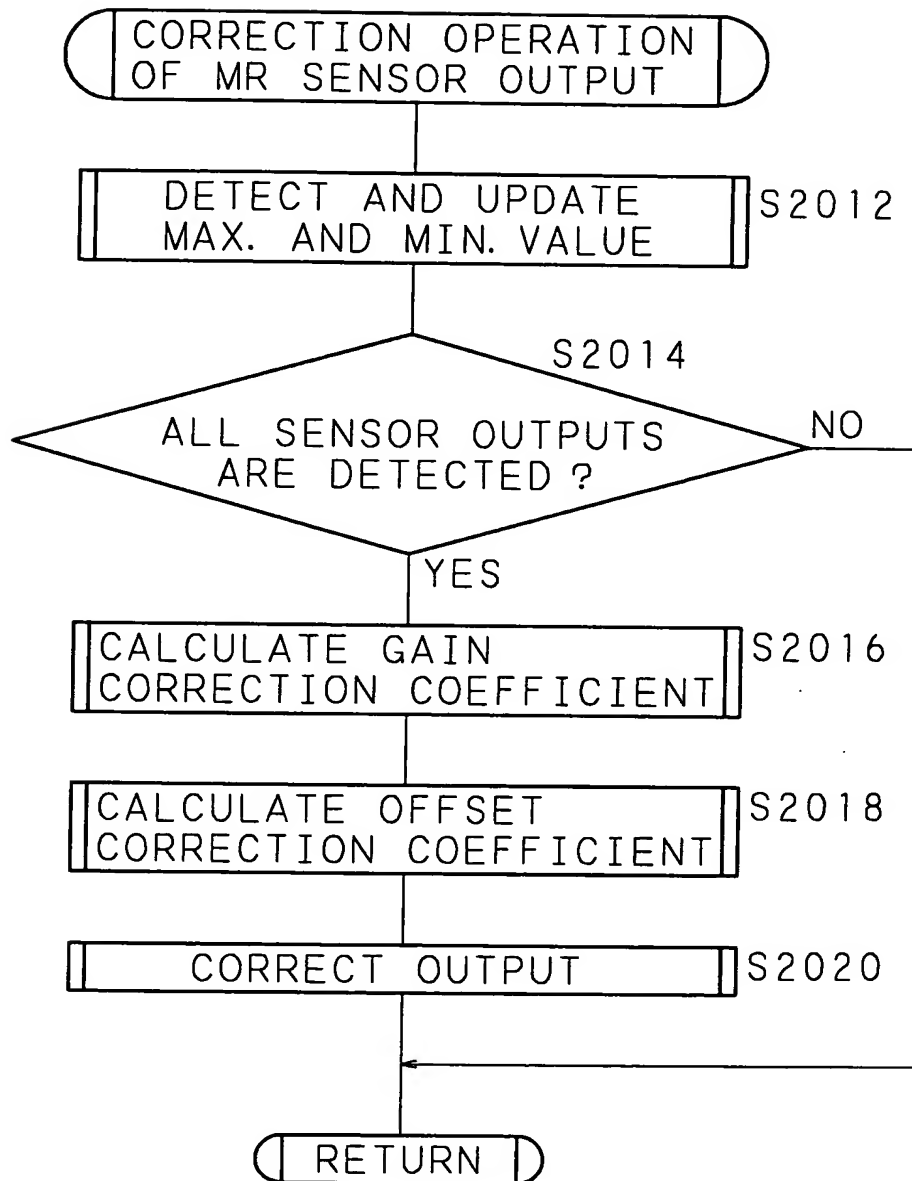
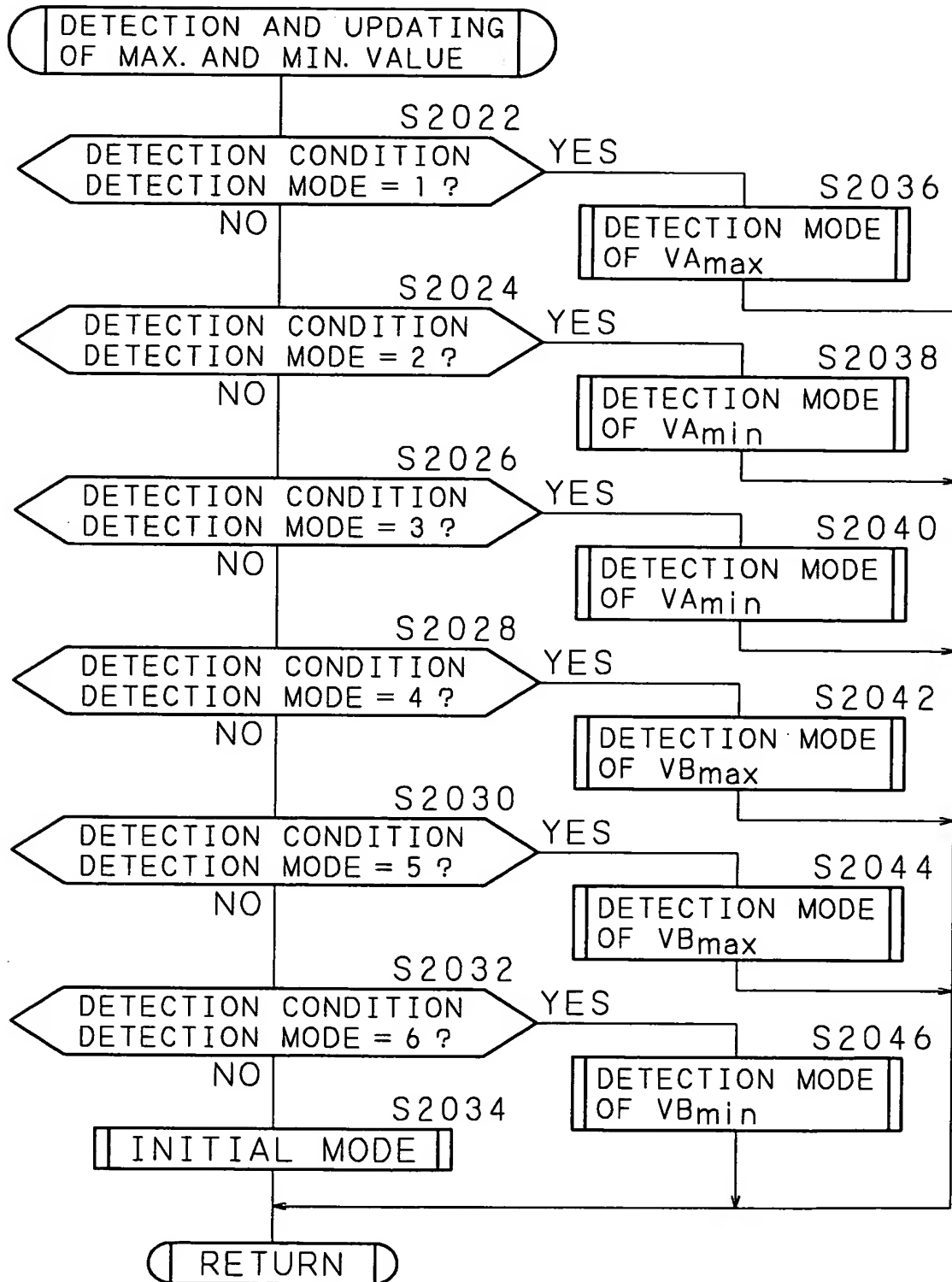




FIG. 109

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FIG. 110A

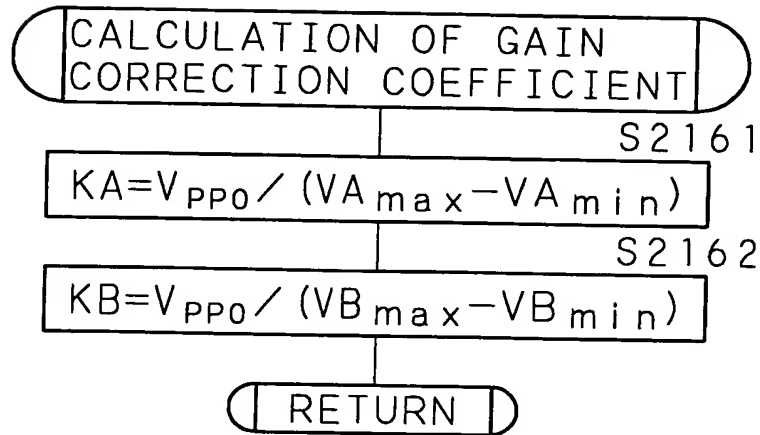


FIG. 110B

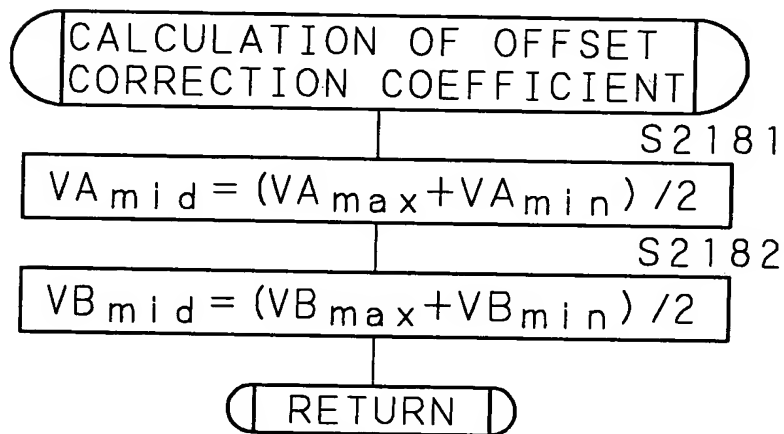


FIG. 110C

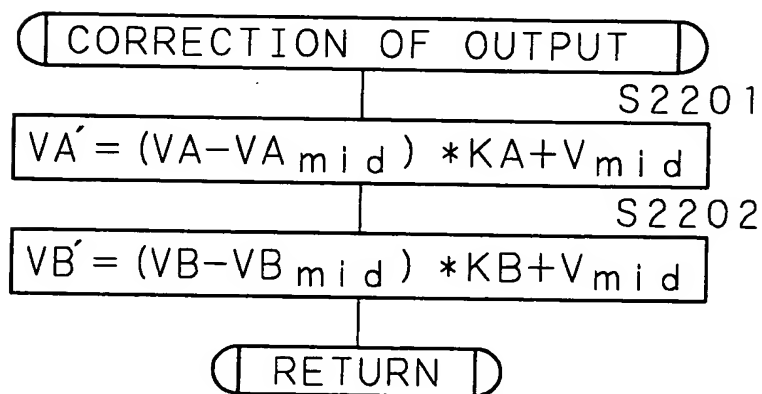
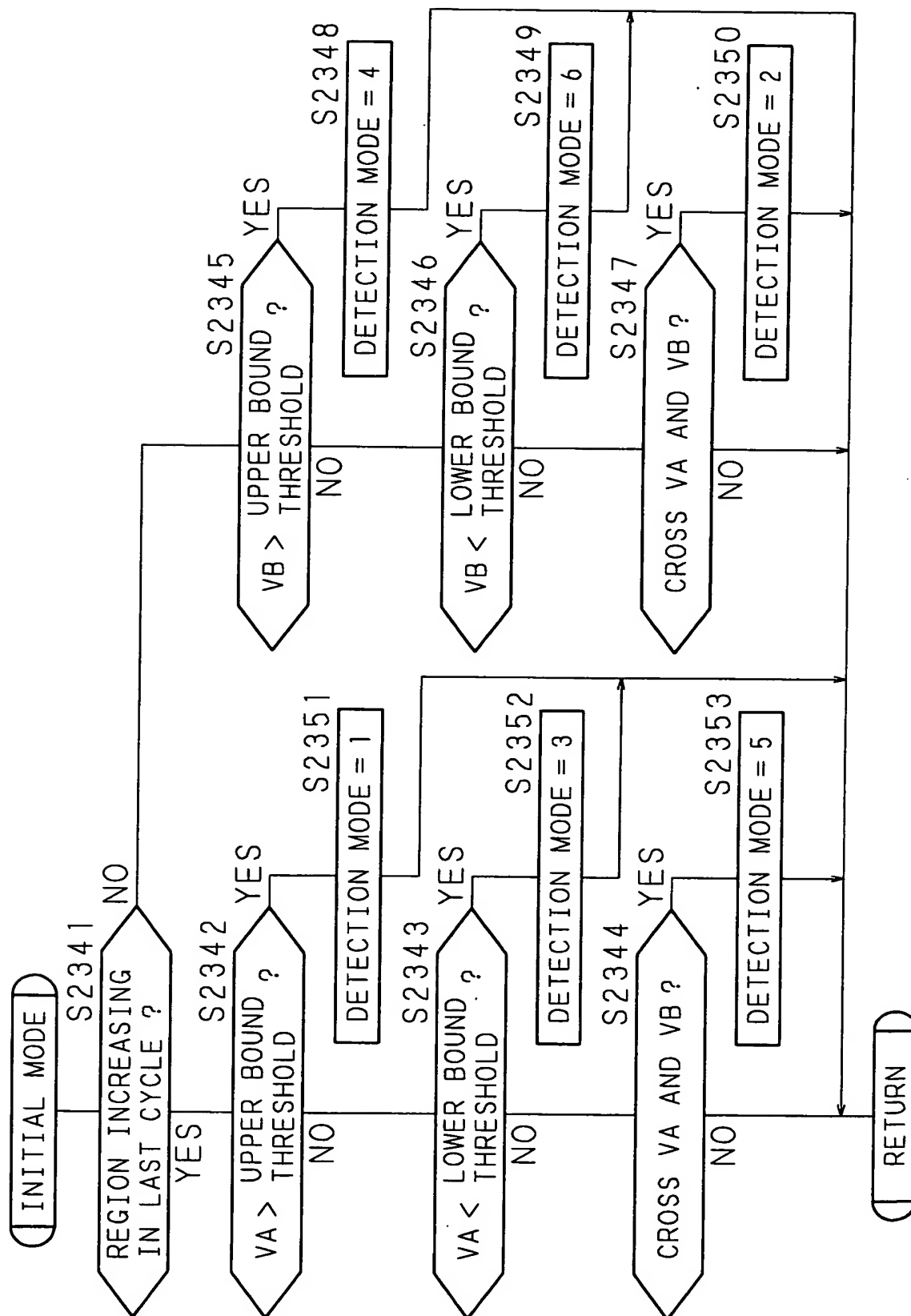
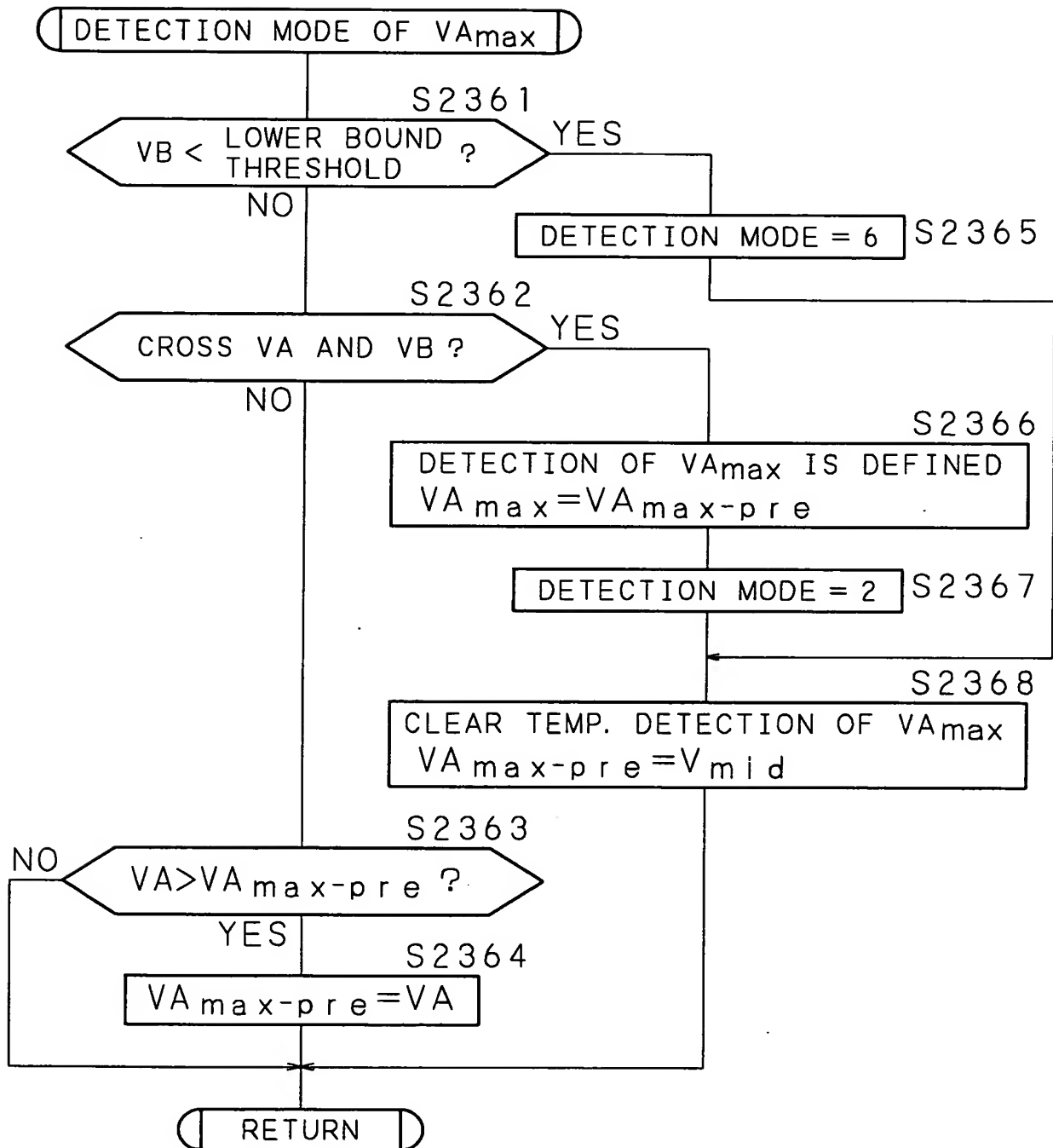


FIG. 111



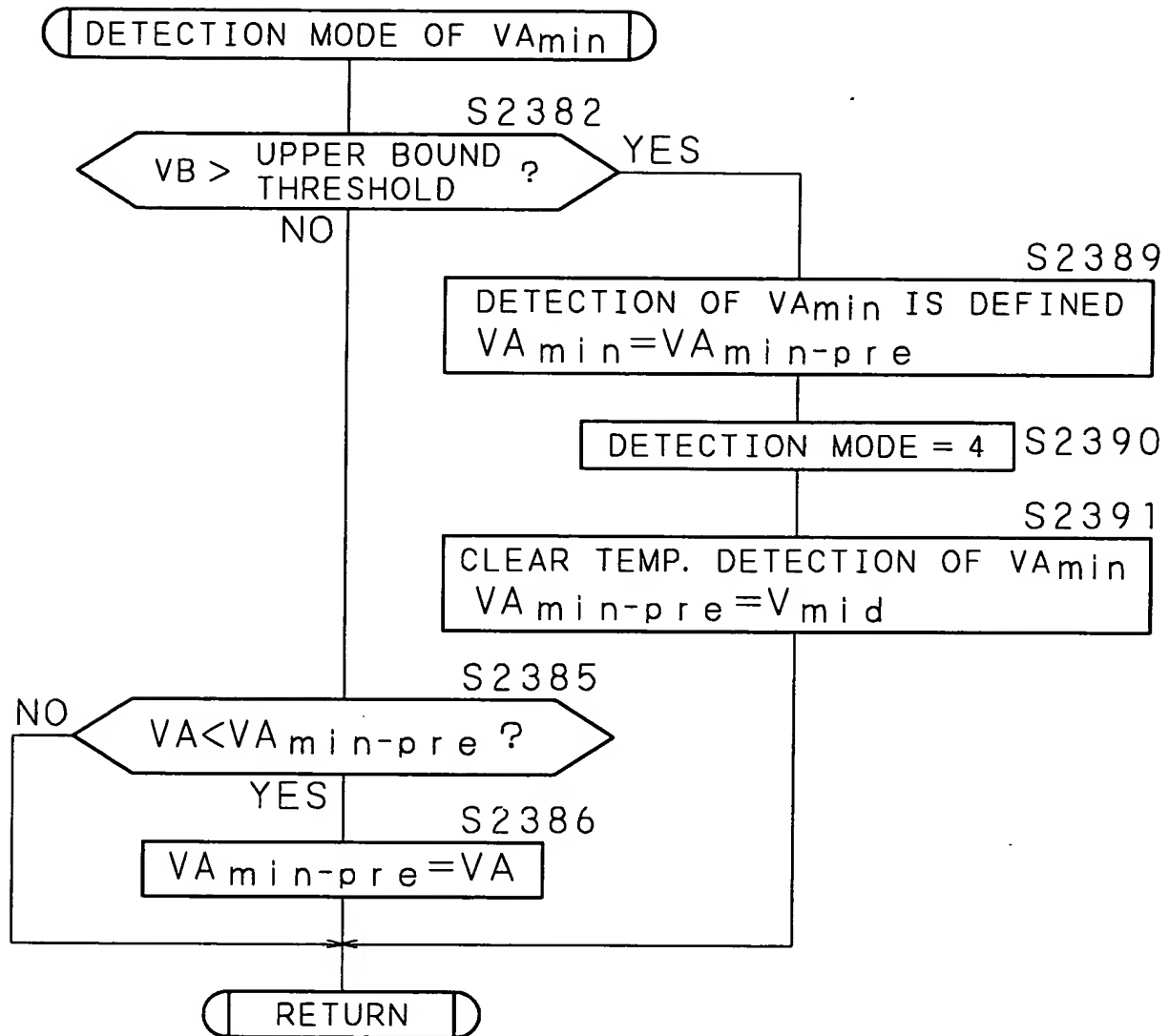
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FIG. 112



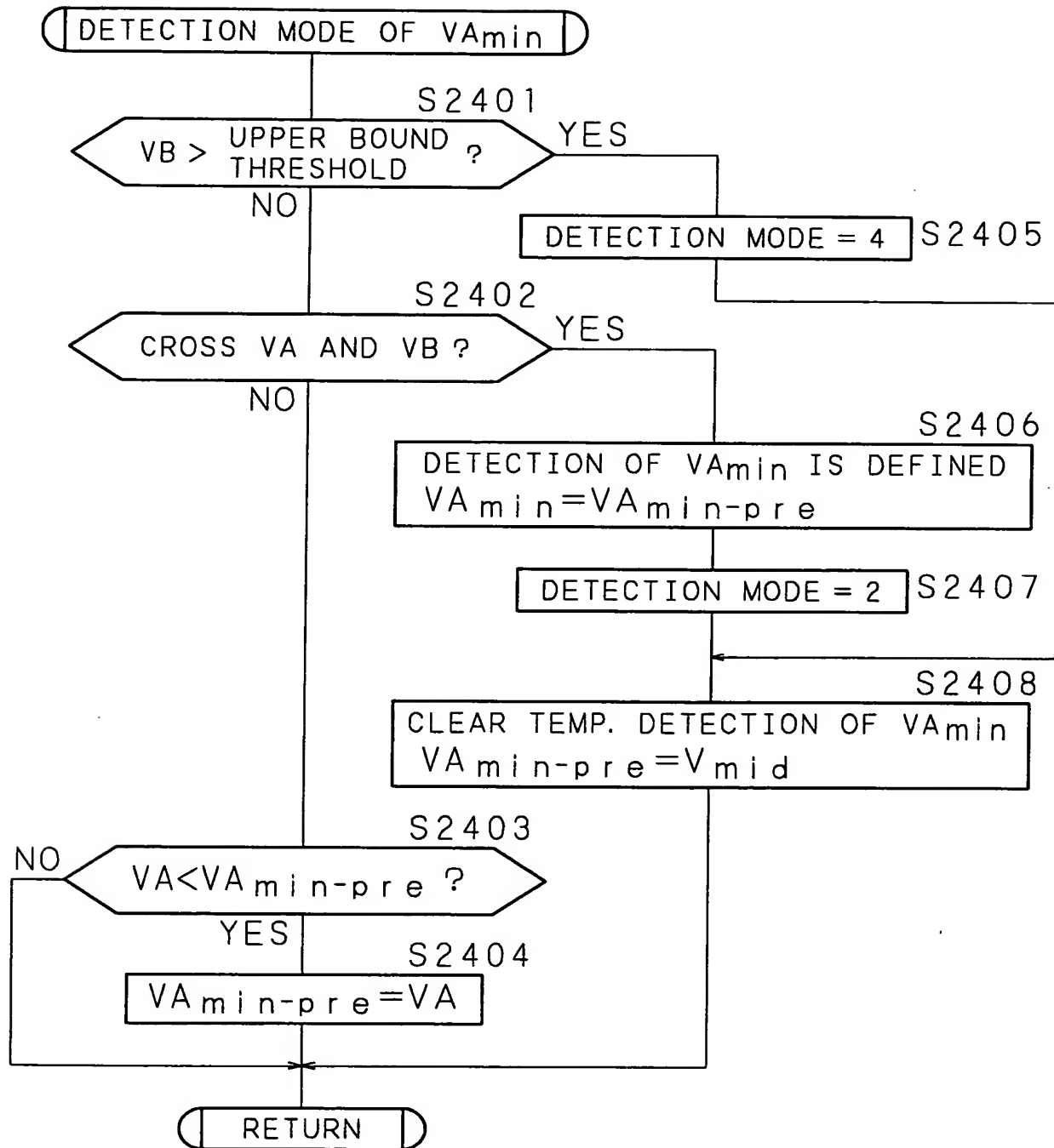
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FIG. 113



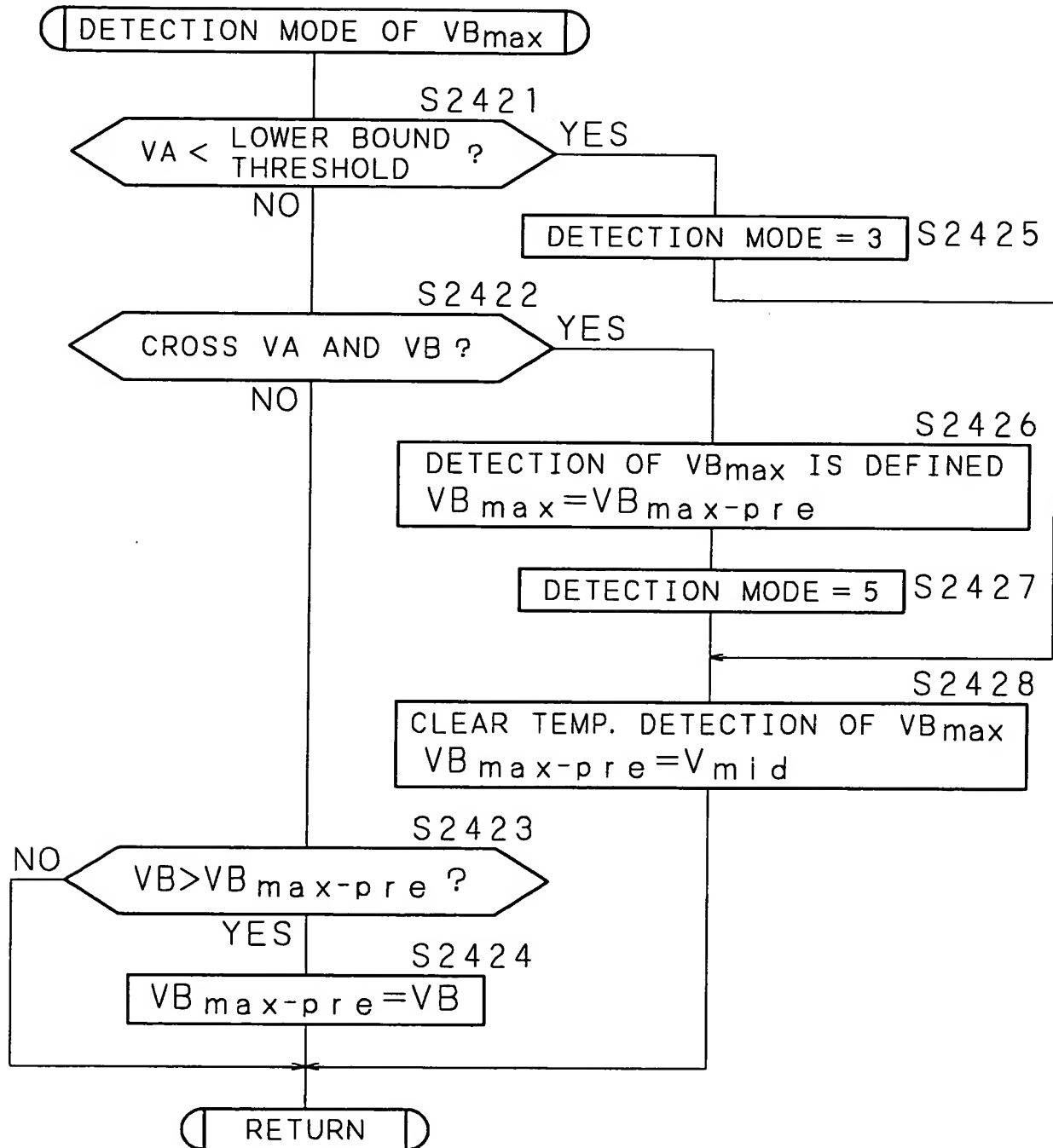
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FIG. 114



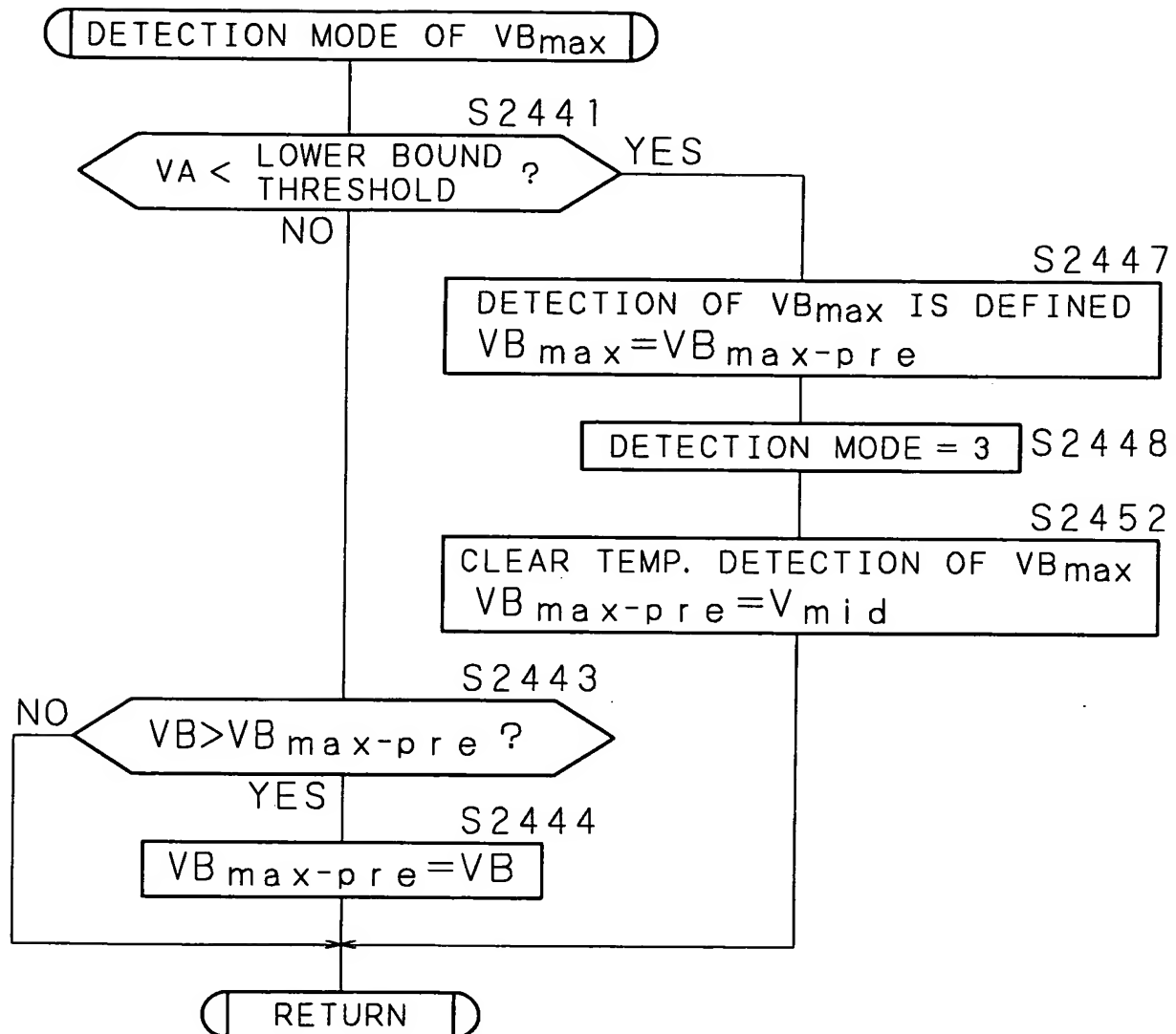
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FIG. 115



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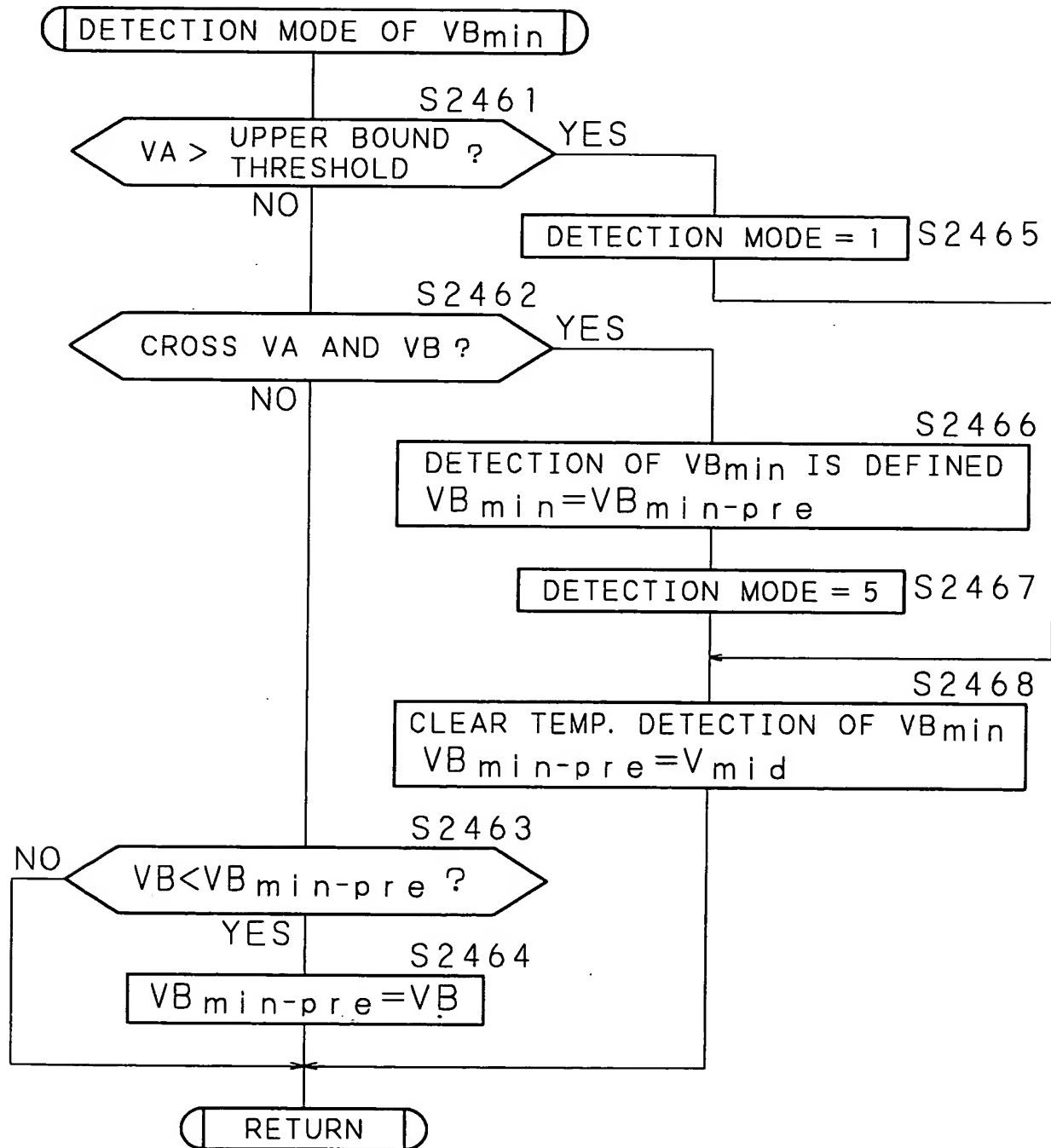
FIG. 116





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FIG. 117



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FIG. 118

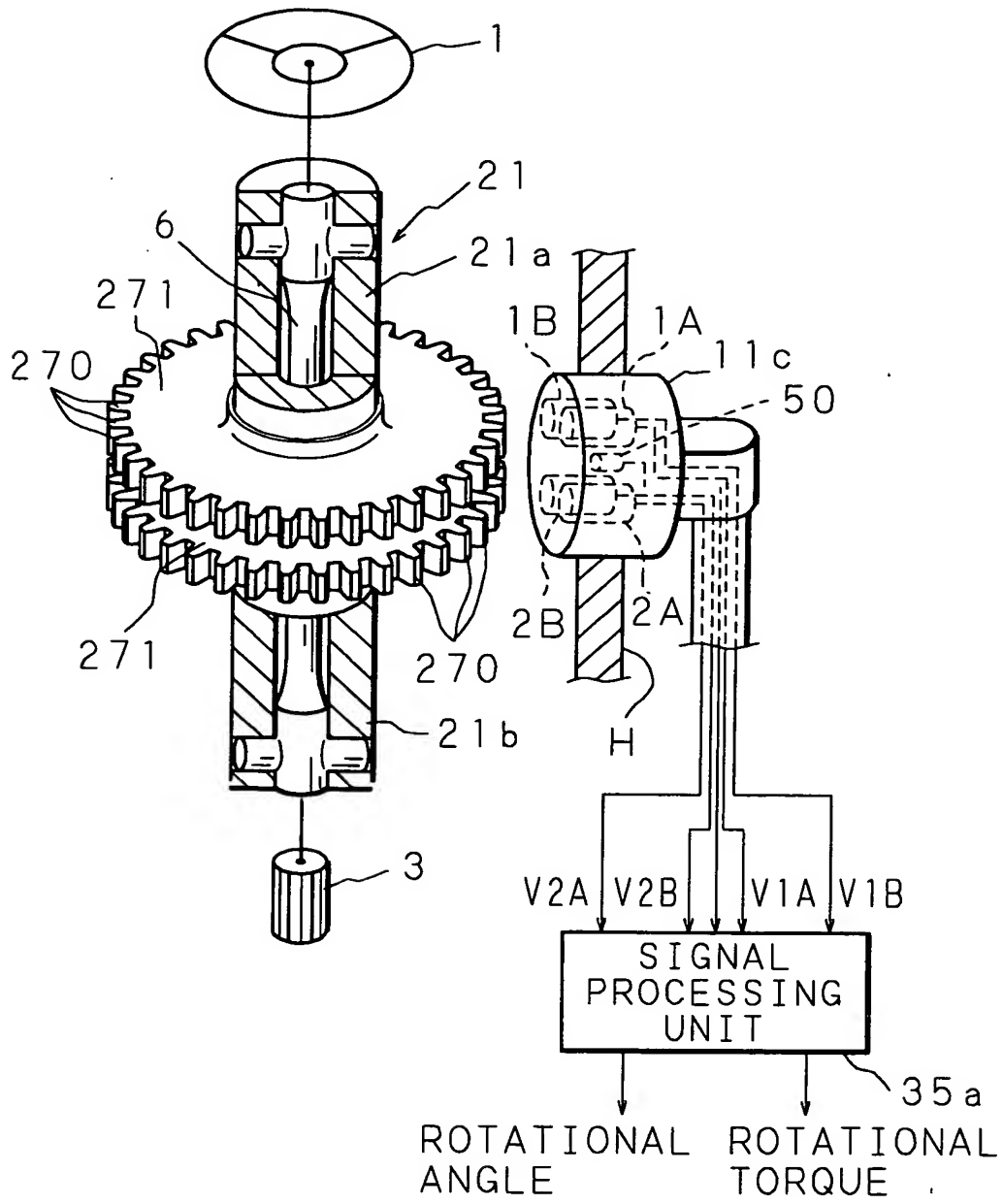


FIG. 119

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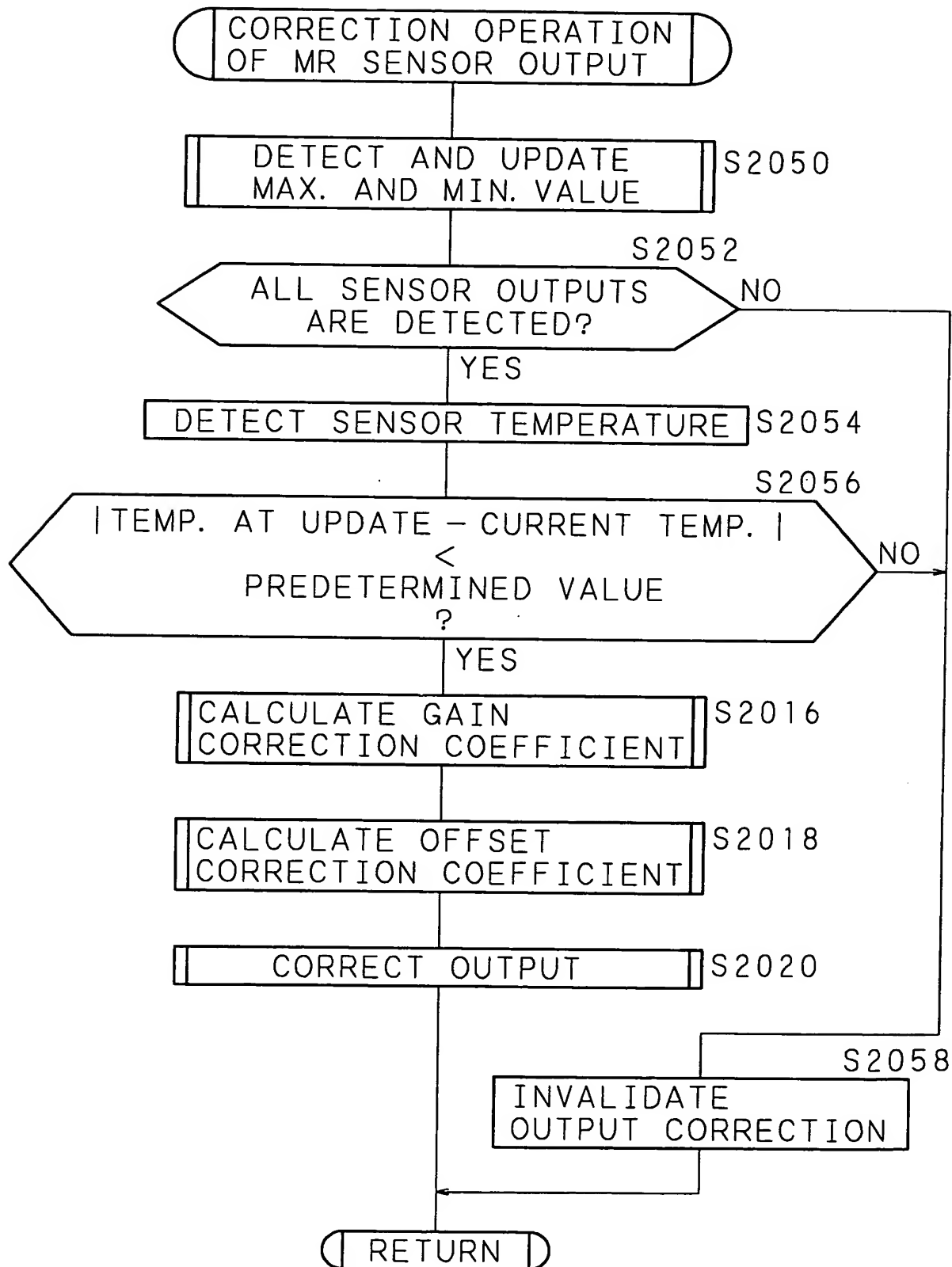
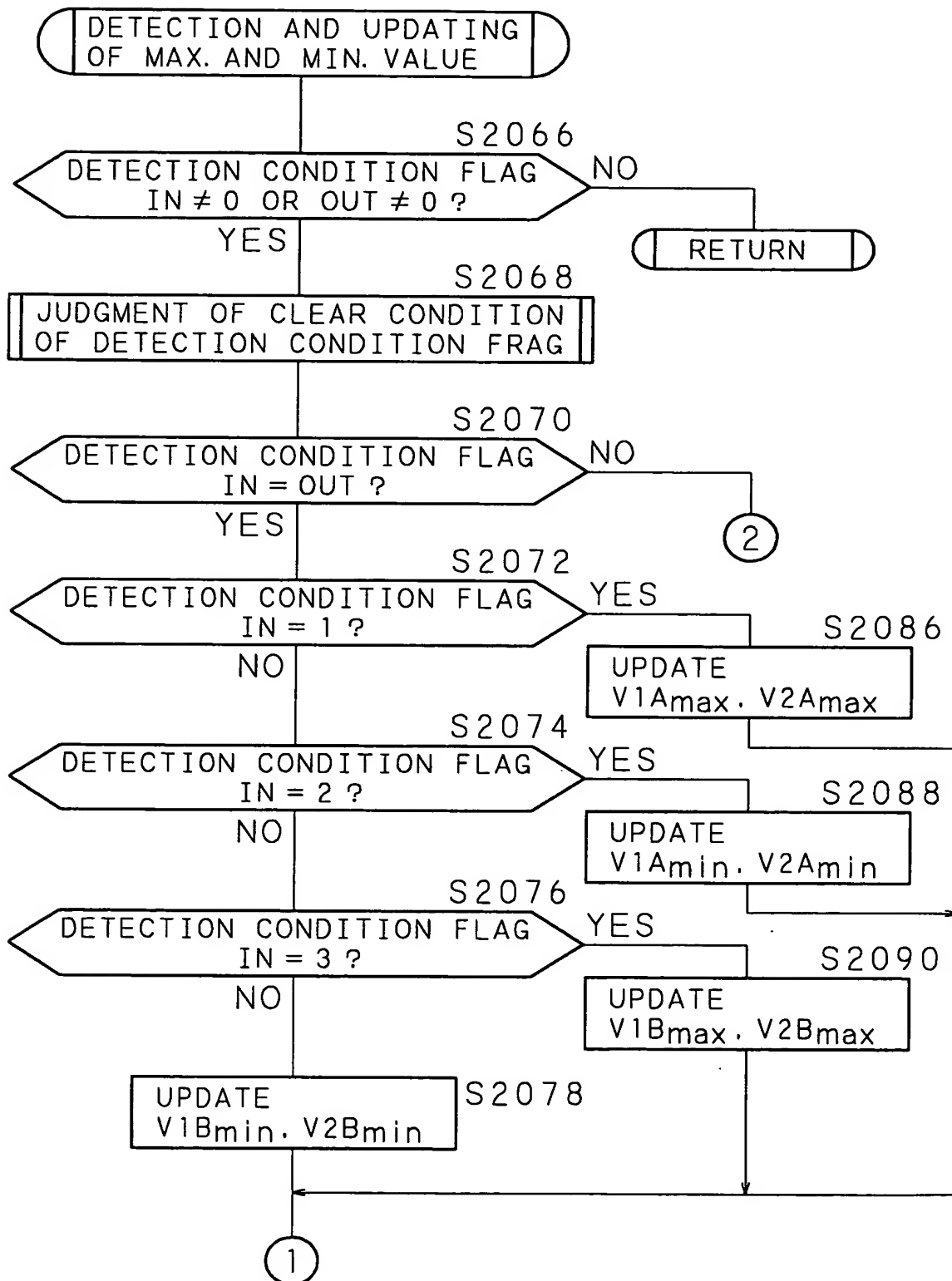


FIG. 120

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FIG. 121

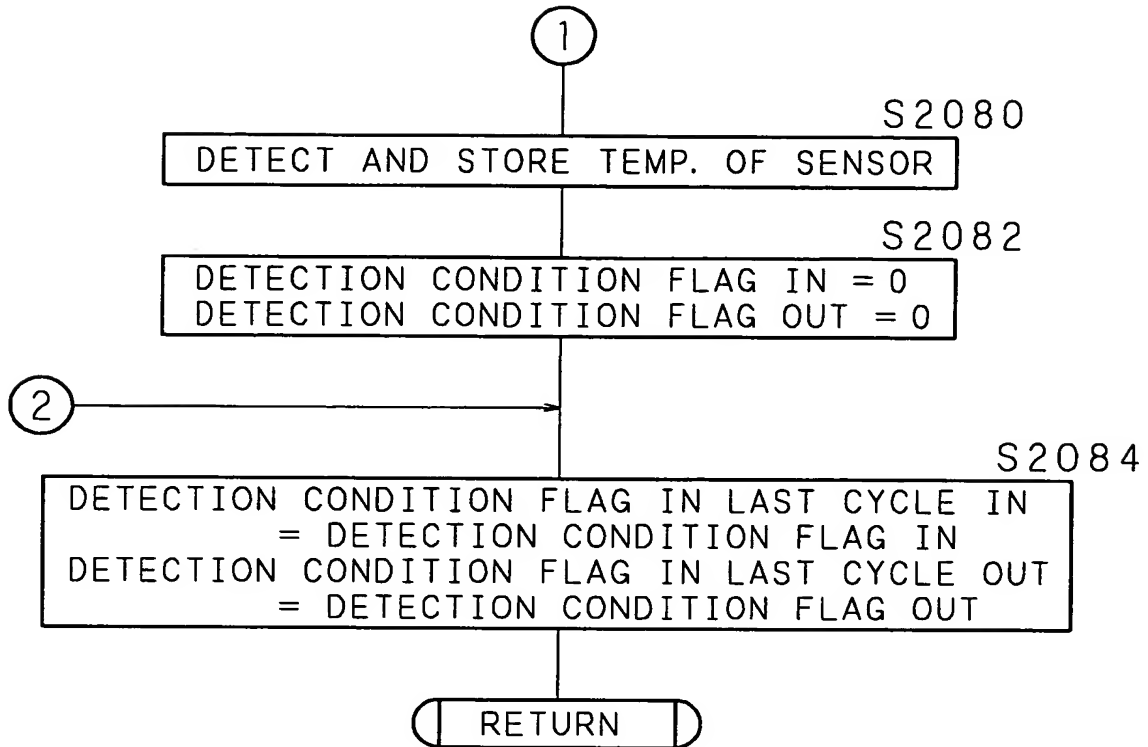
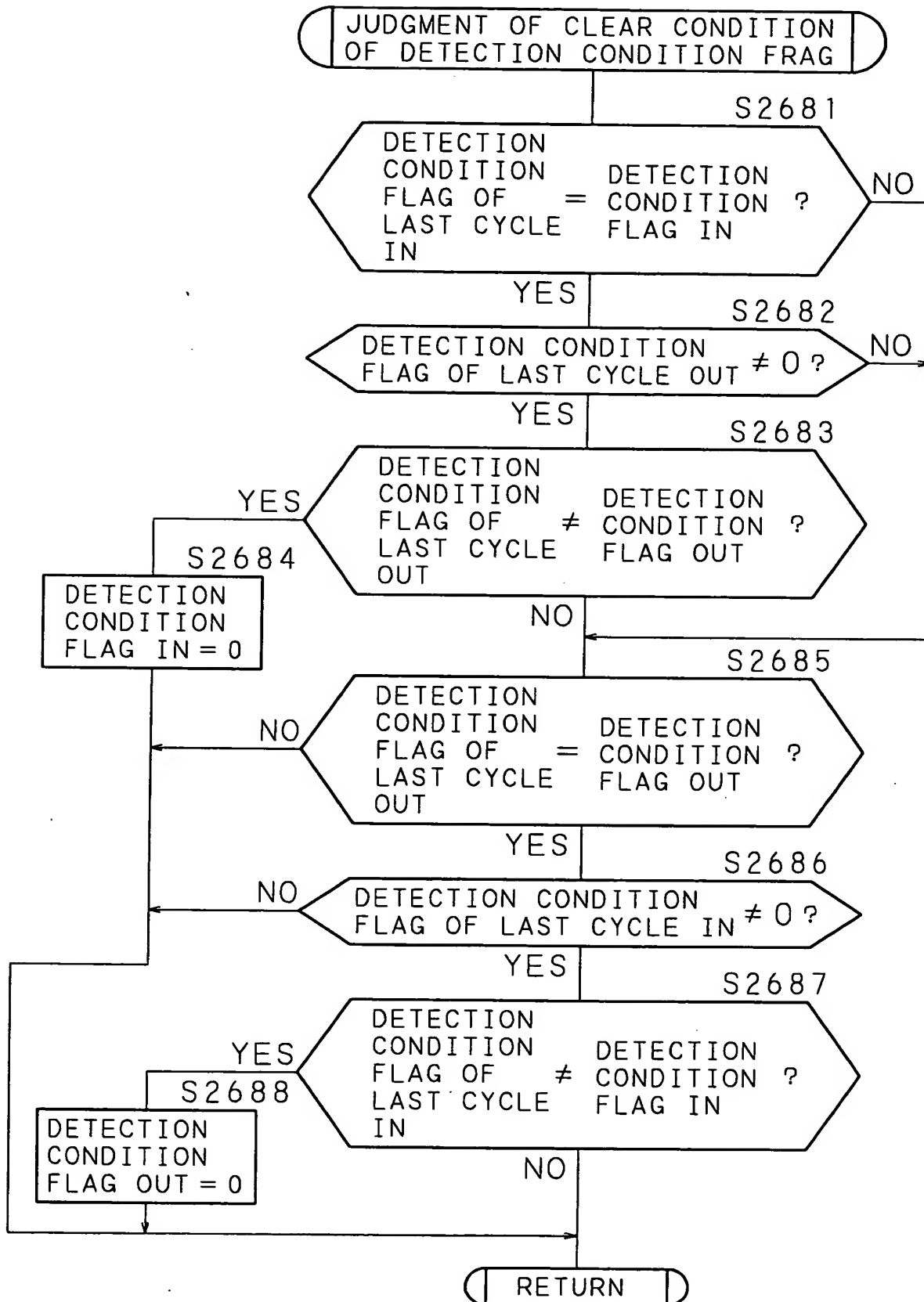


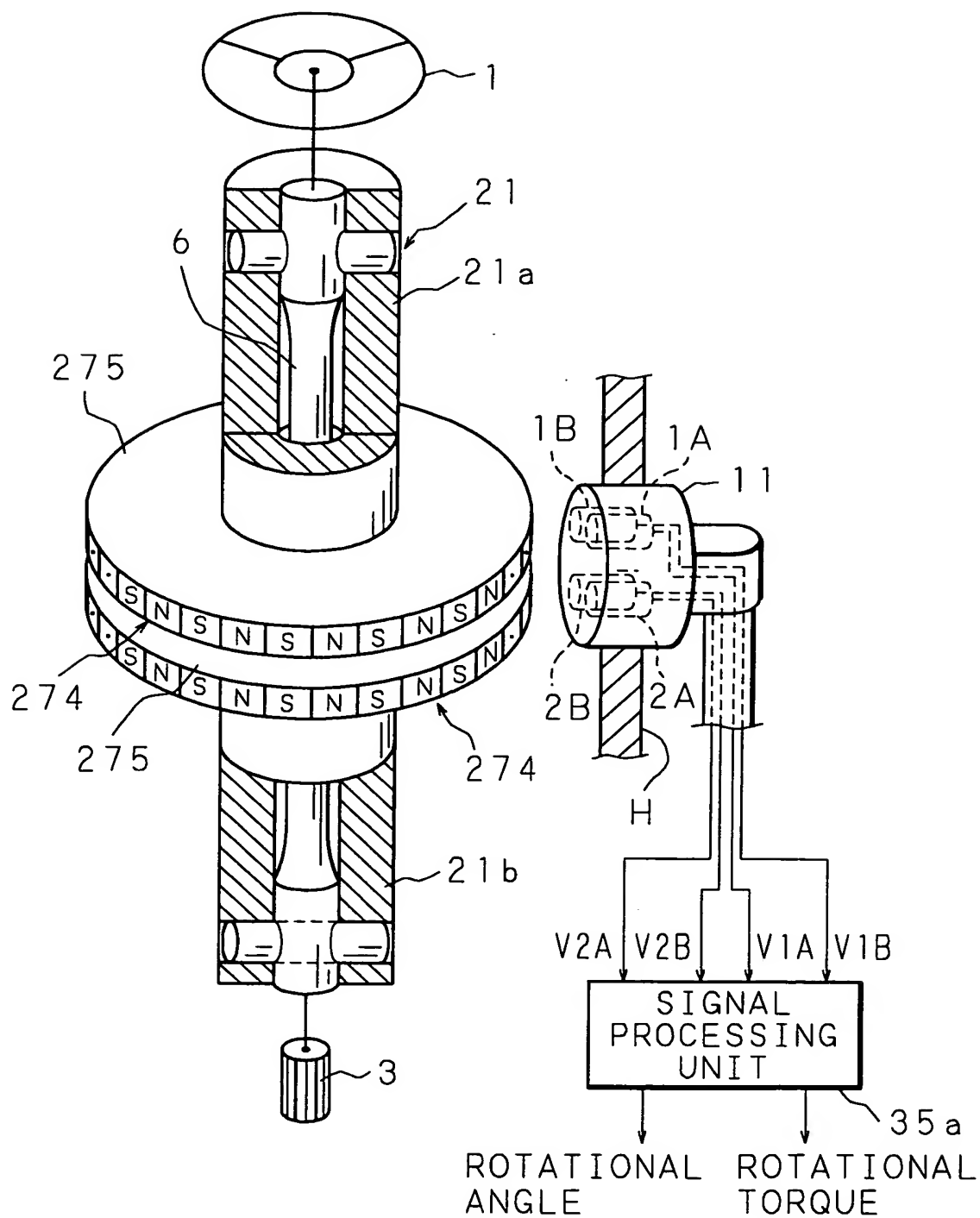
FIG. 122

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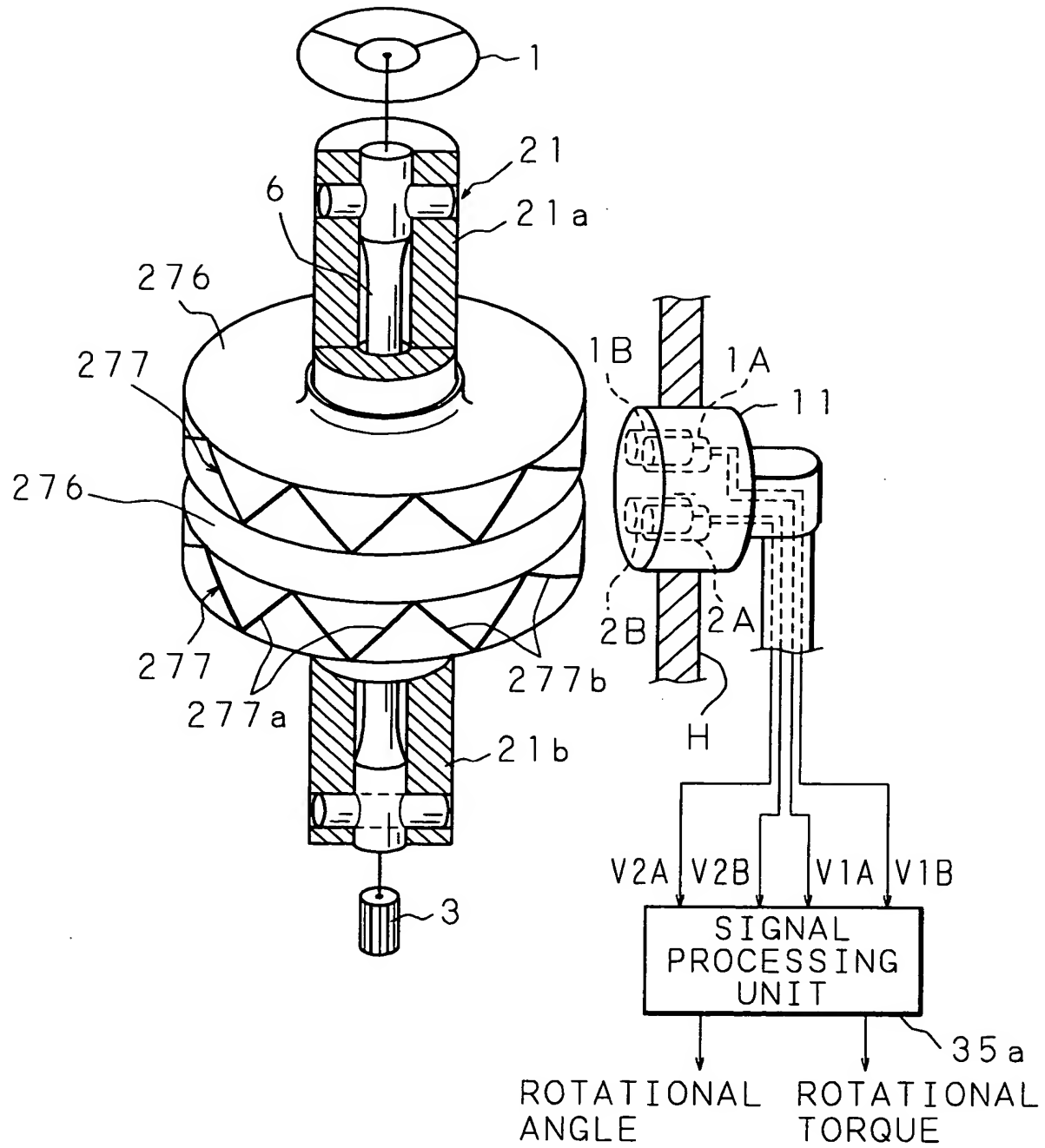
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FIG. 123



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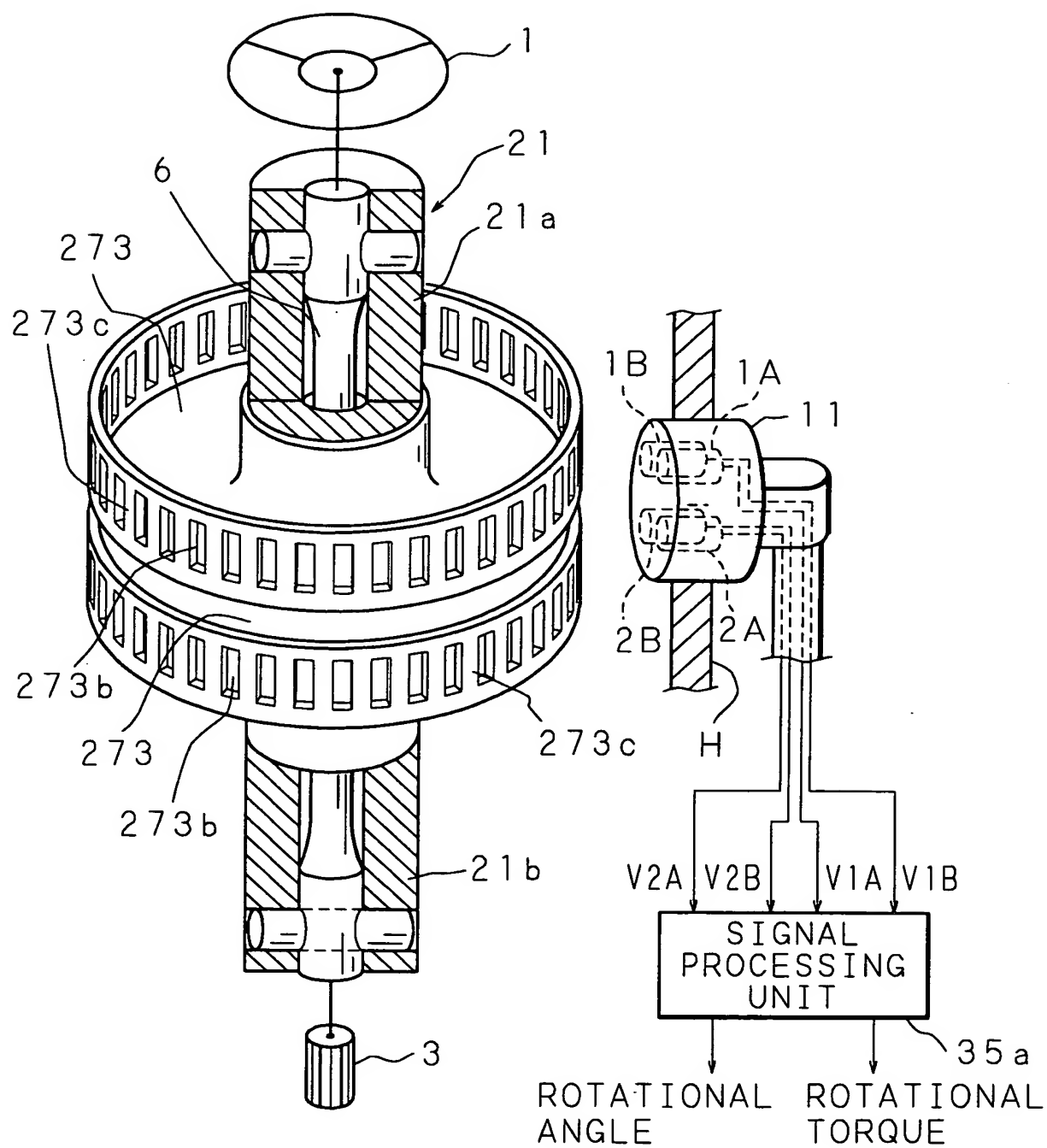
FIG. 124





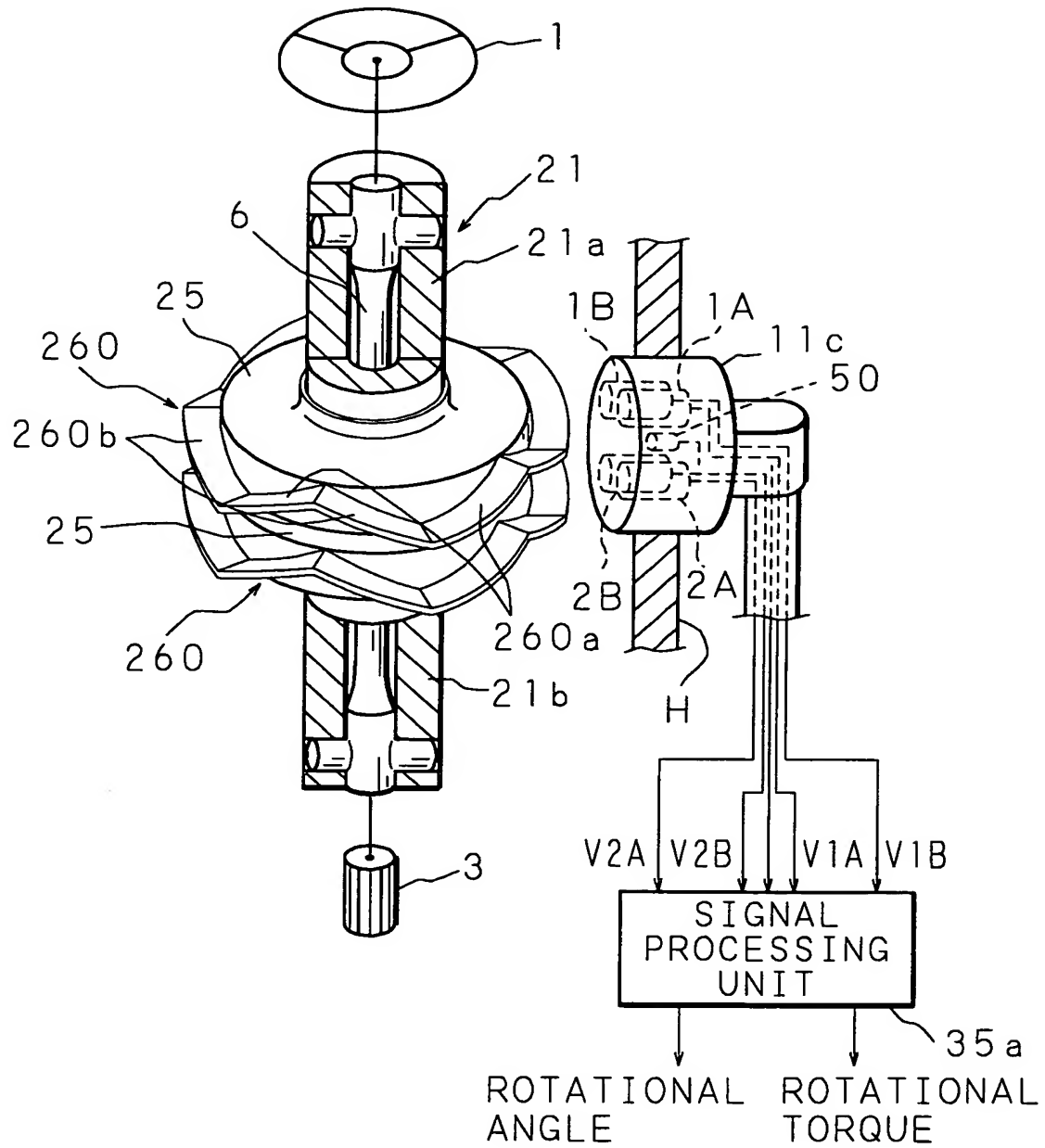
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FIG. 125



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FIG. 126



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FIG. 127

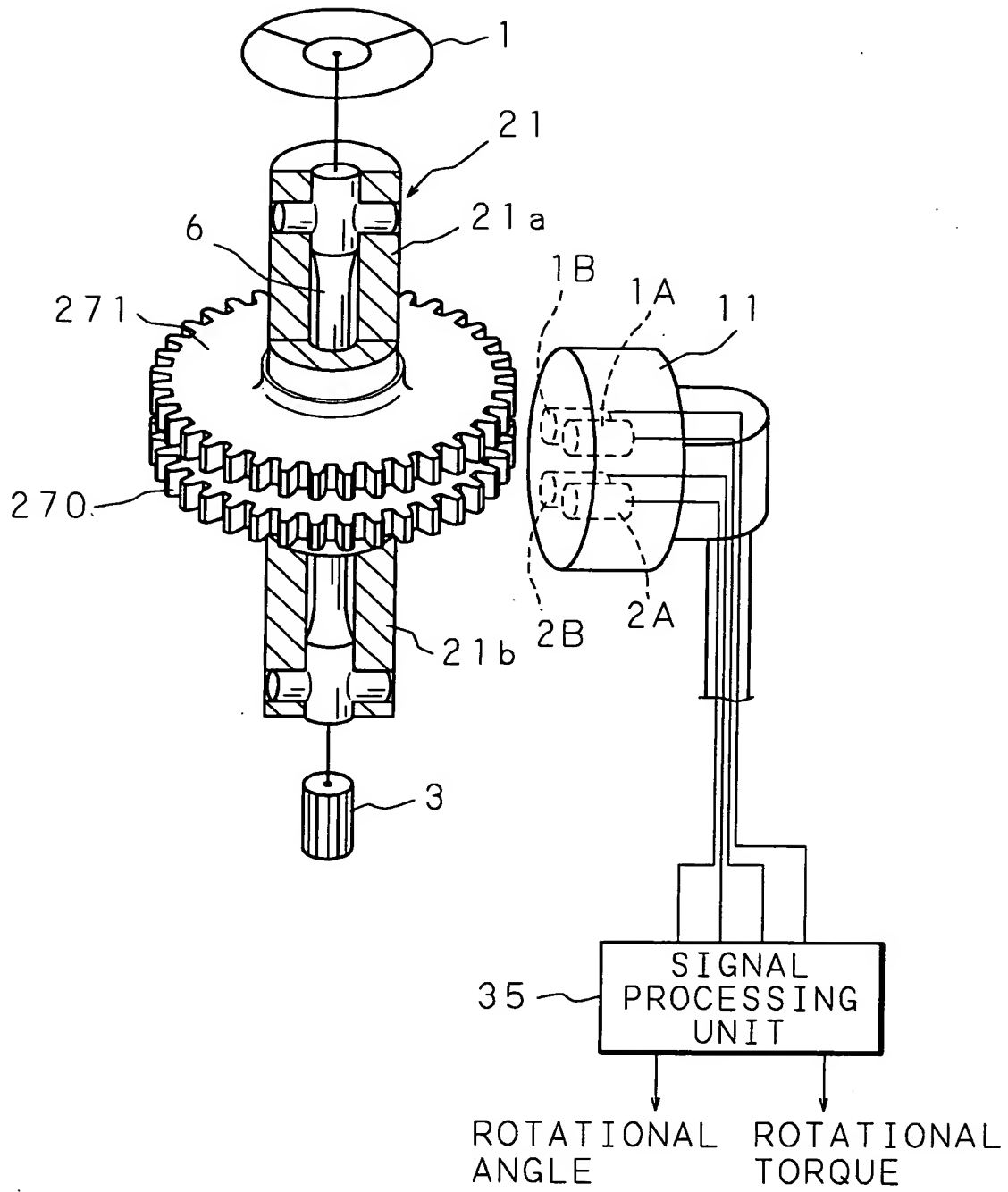


FIG. 128A

CALCULATION OF  
STEERING ANGLE

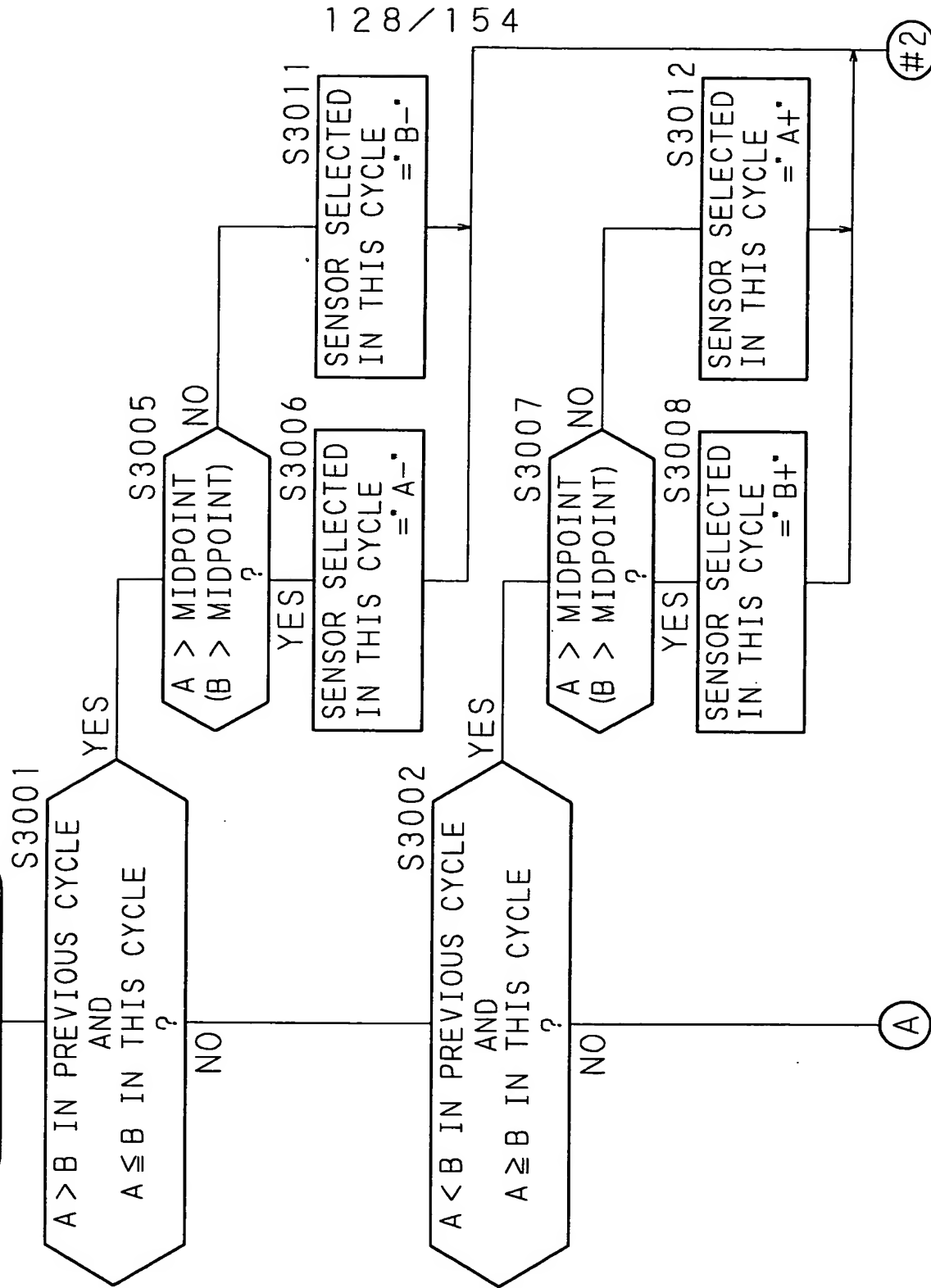


FIG. 128B

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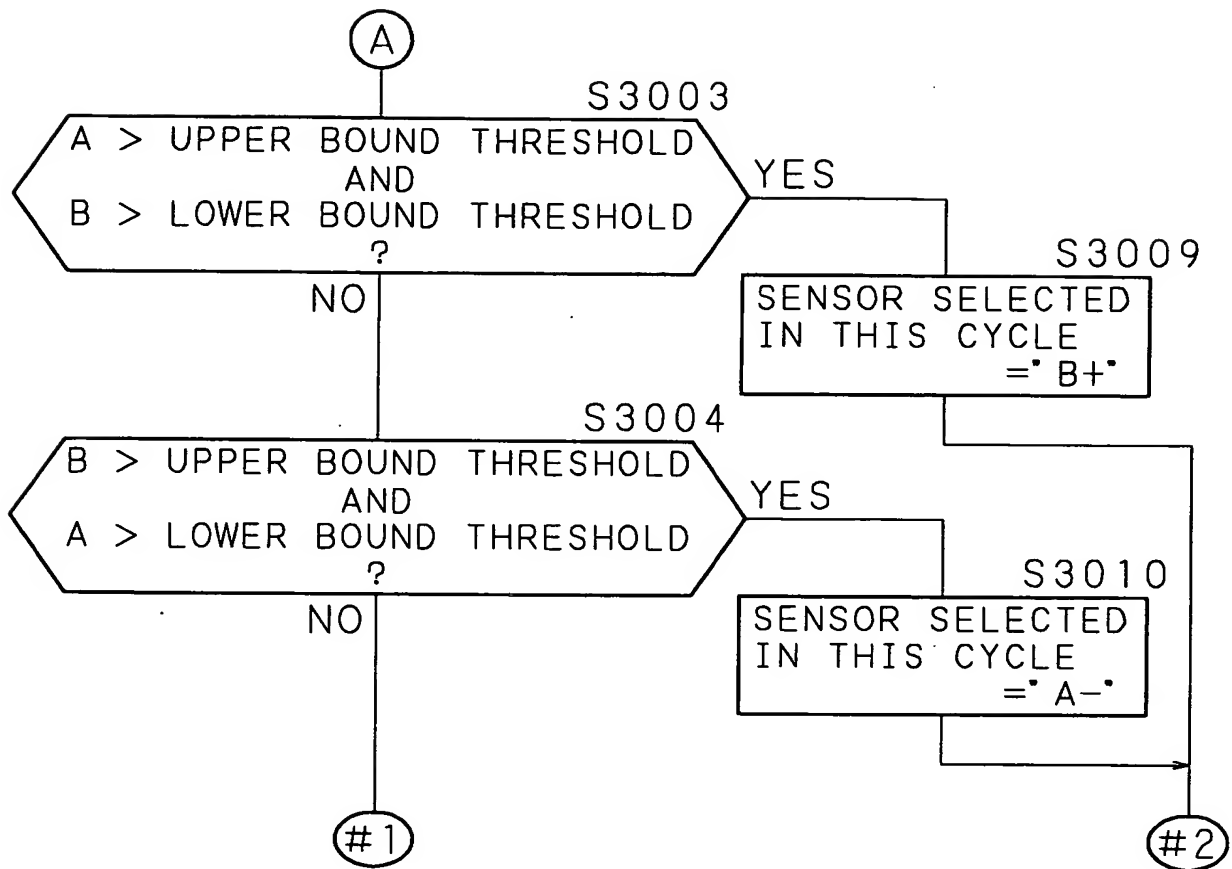


FIG. 129A

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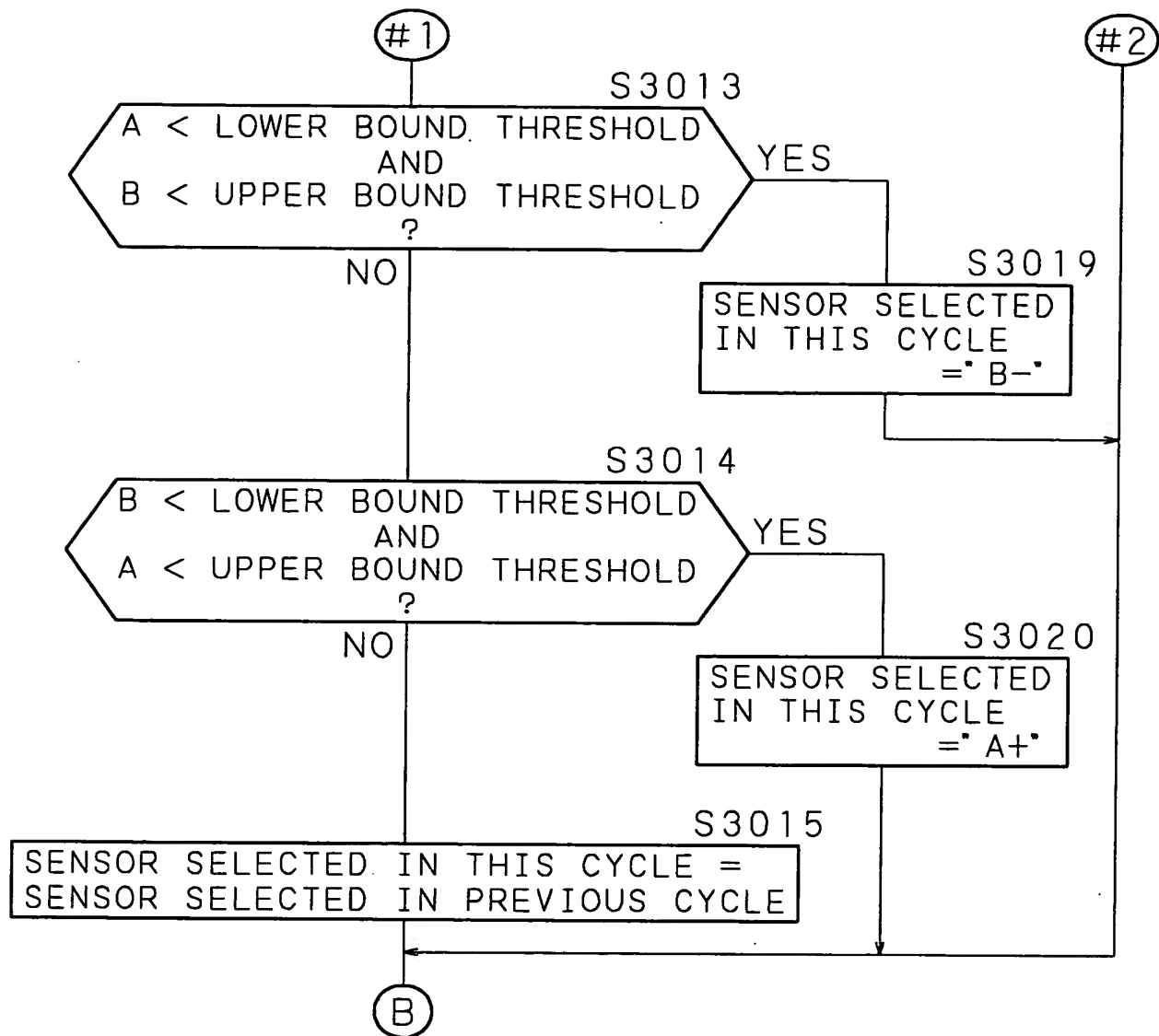
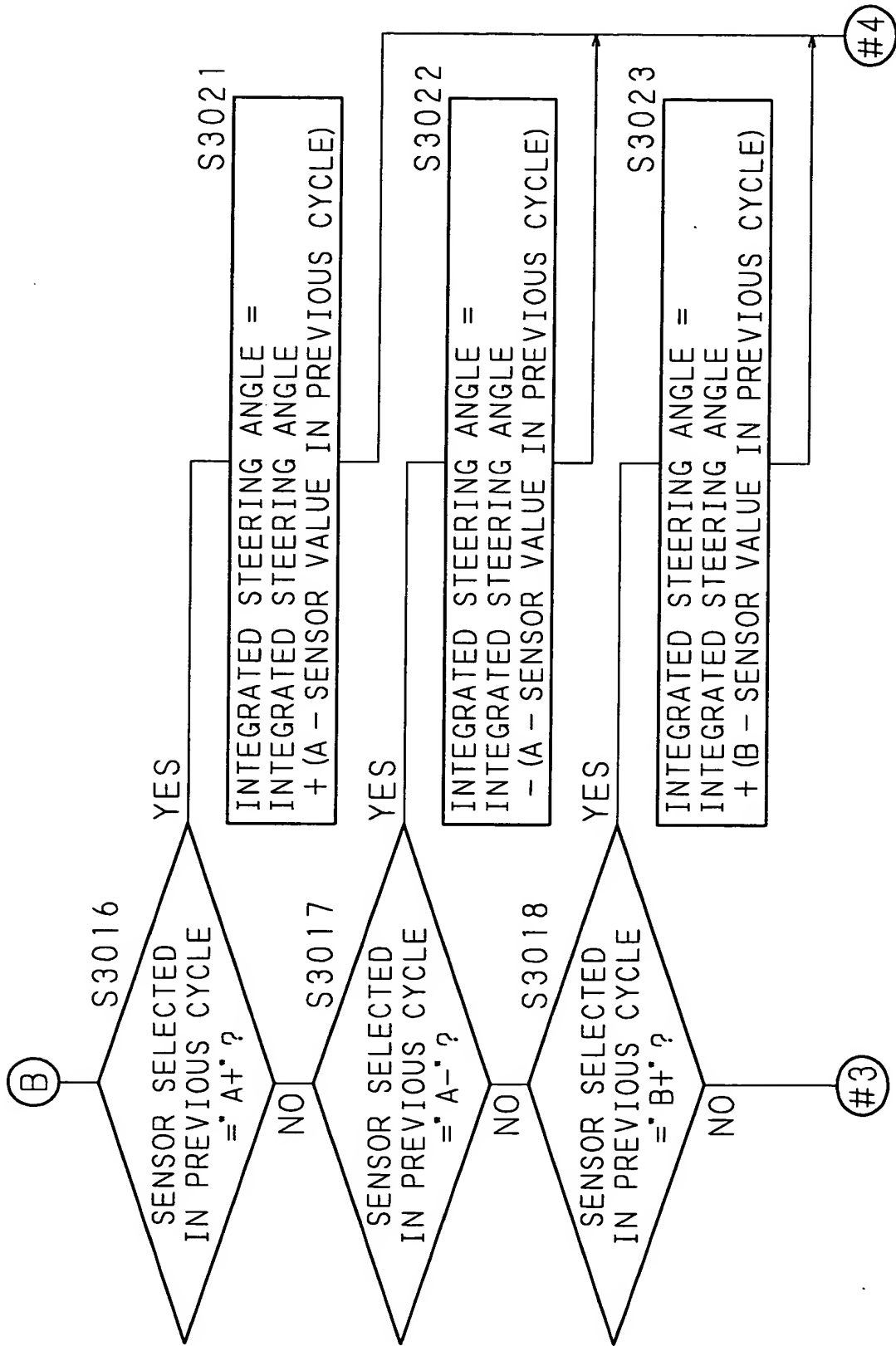
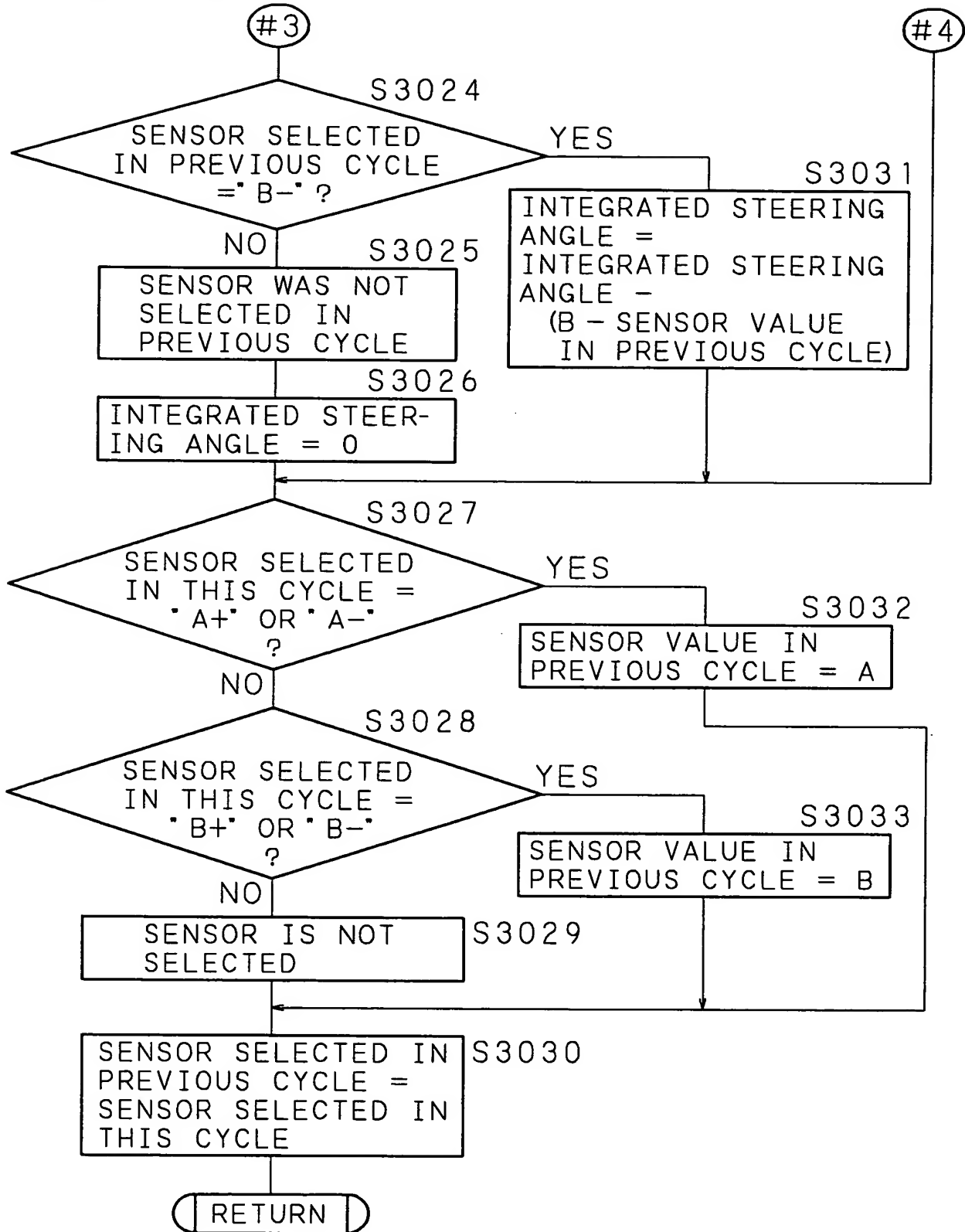


FIG. 129B



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FIG. 130





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FIG. 131

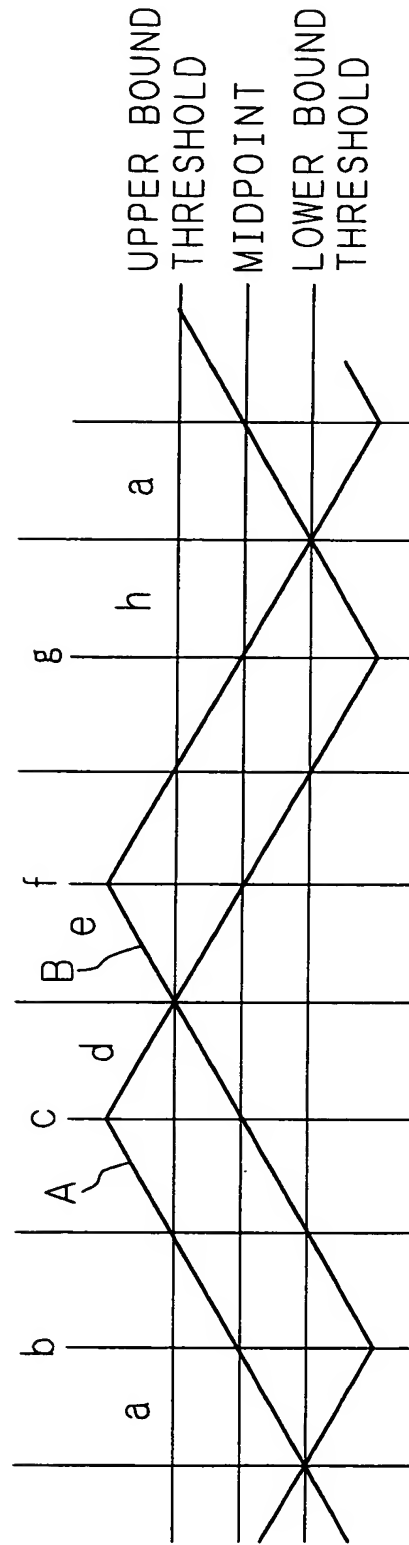
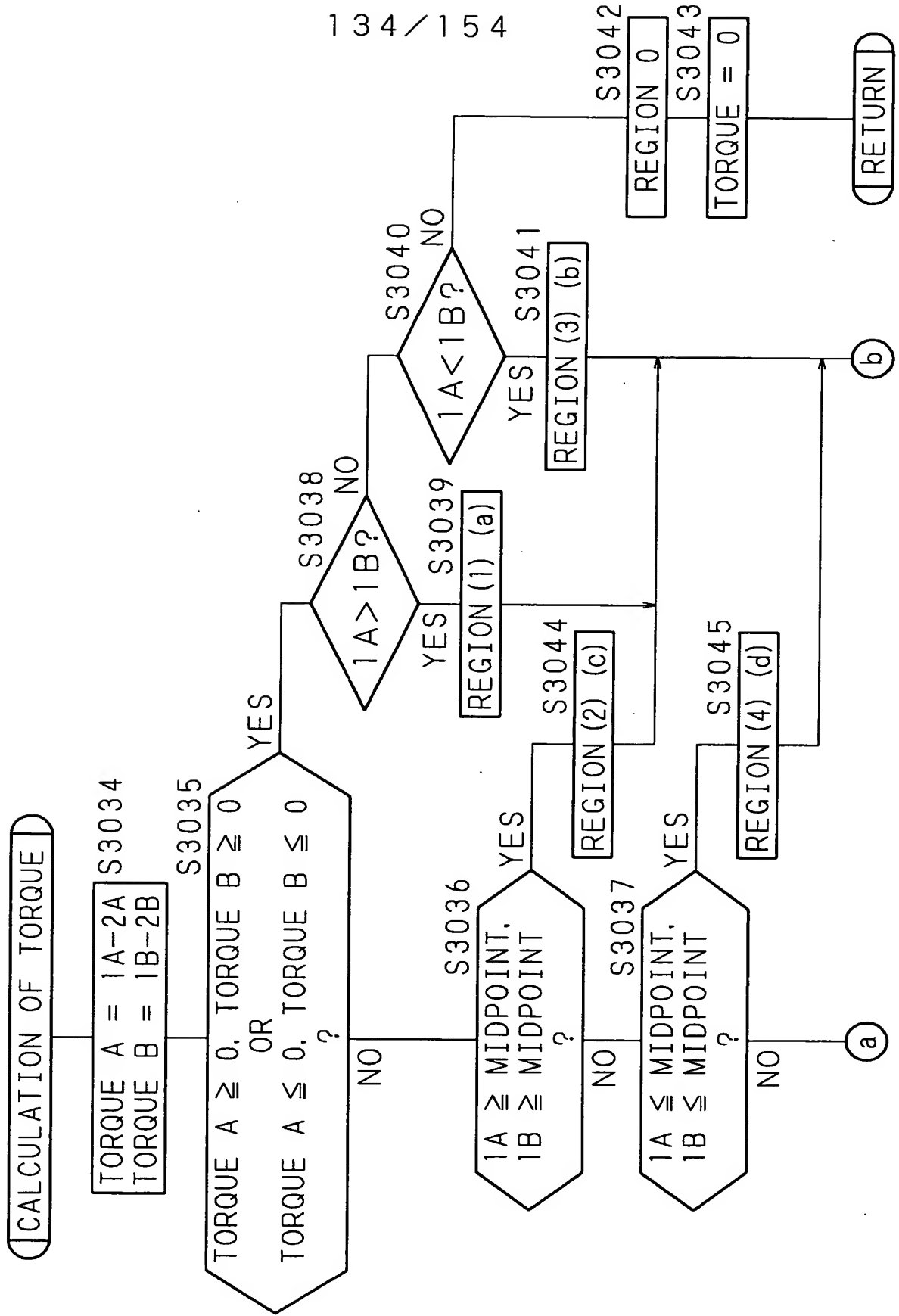
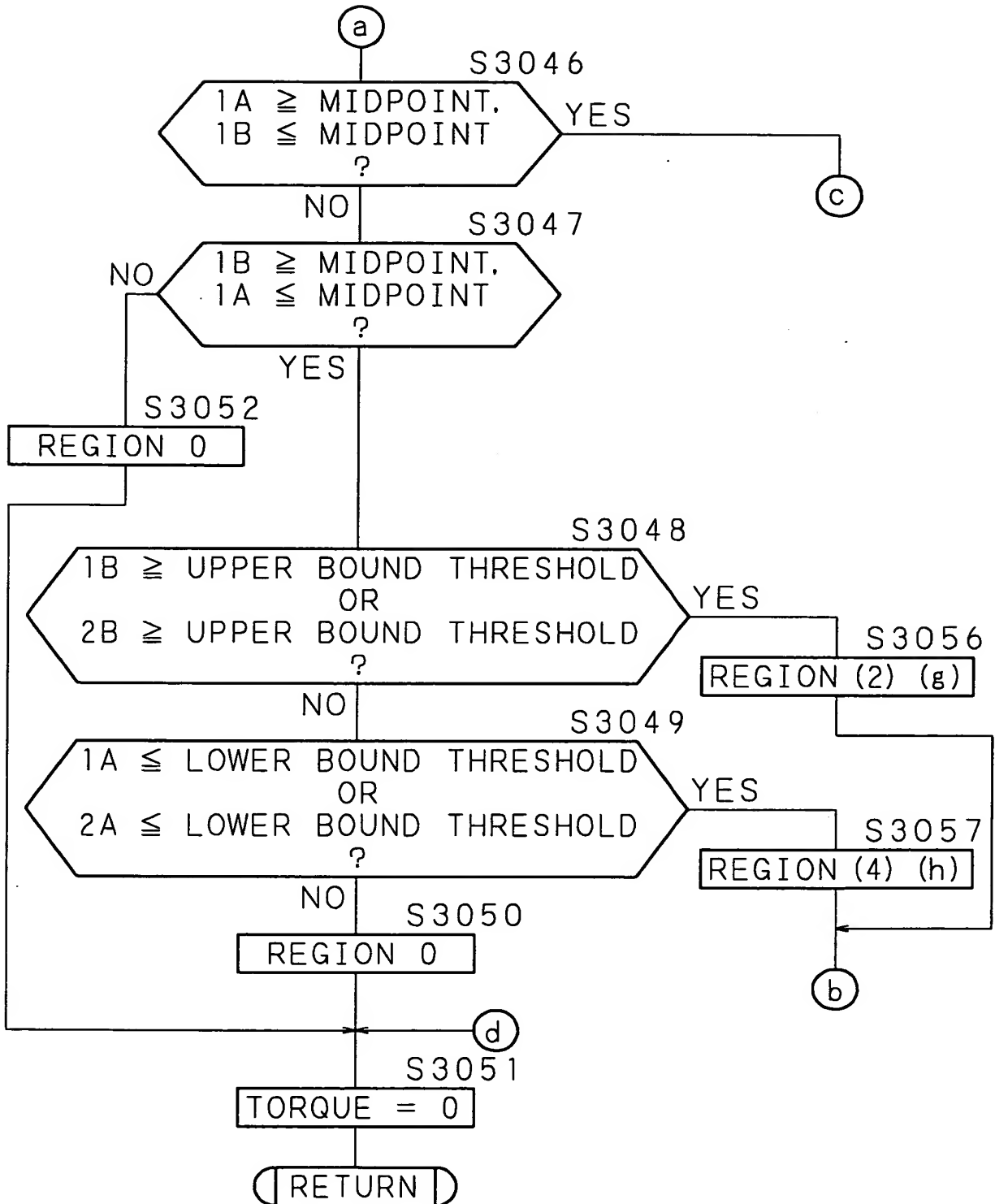


FIG. 132



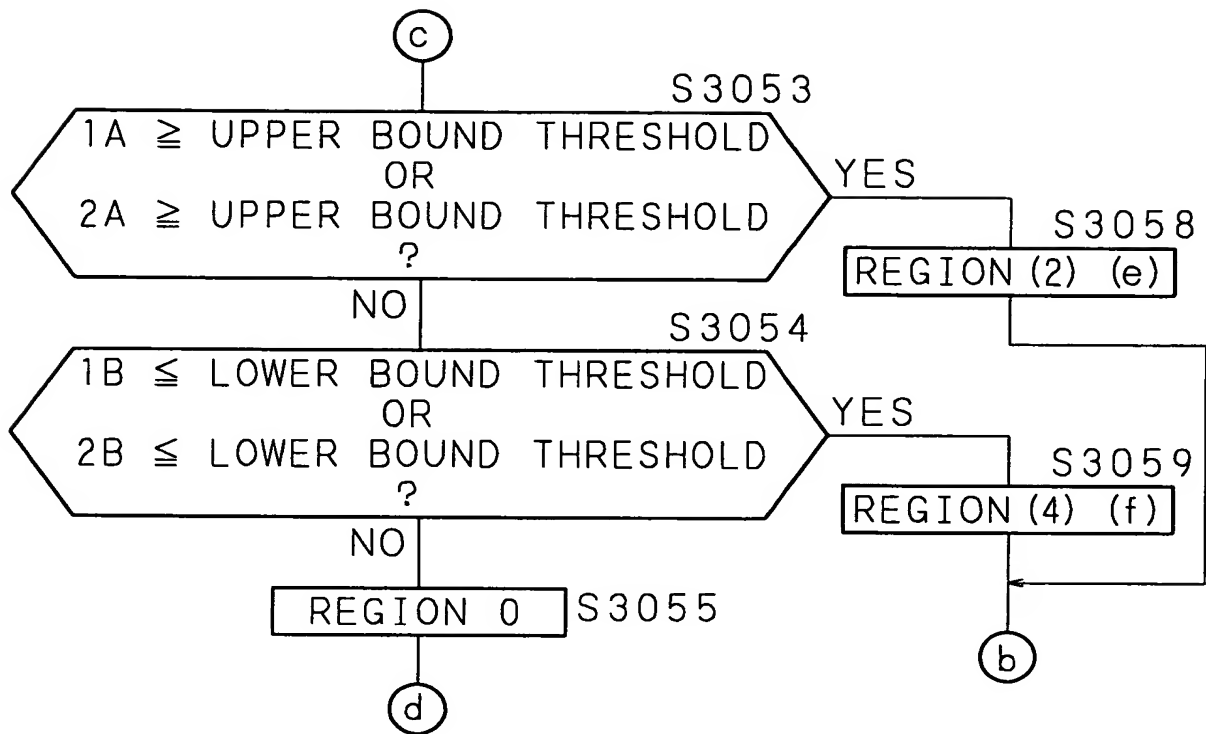
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FIG. 133A



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FIG. 133B



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FIG. 134

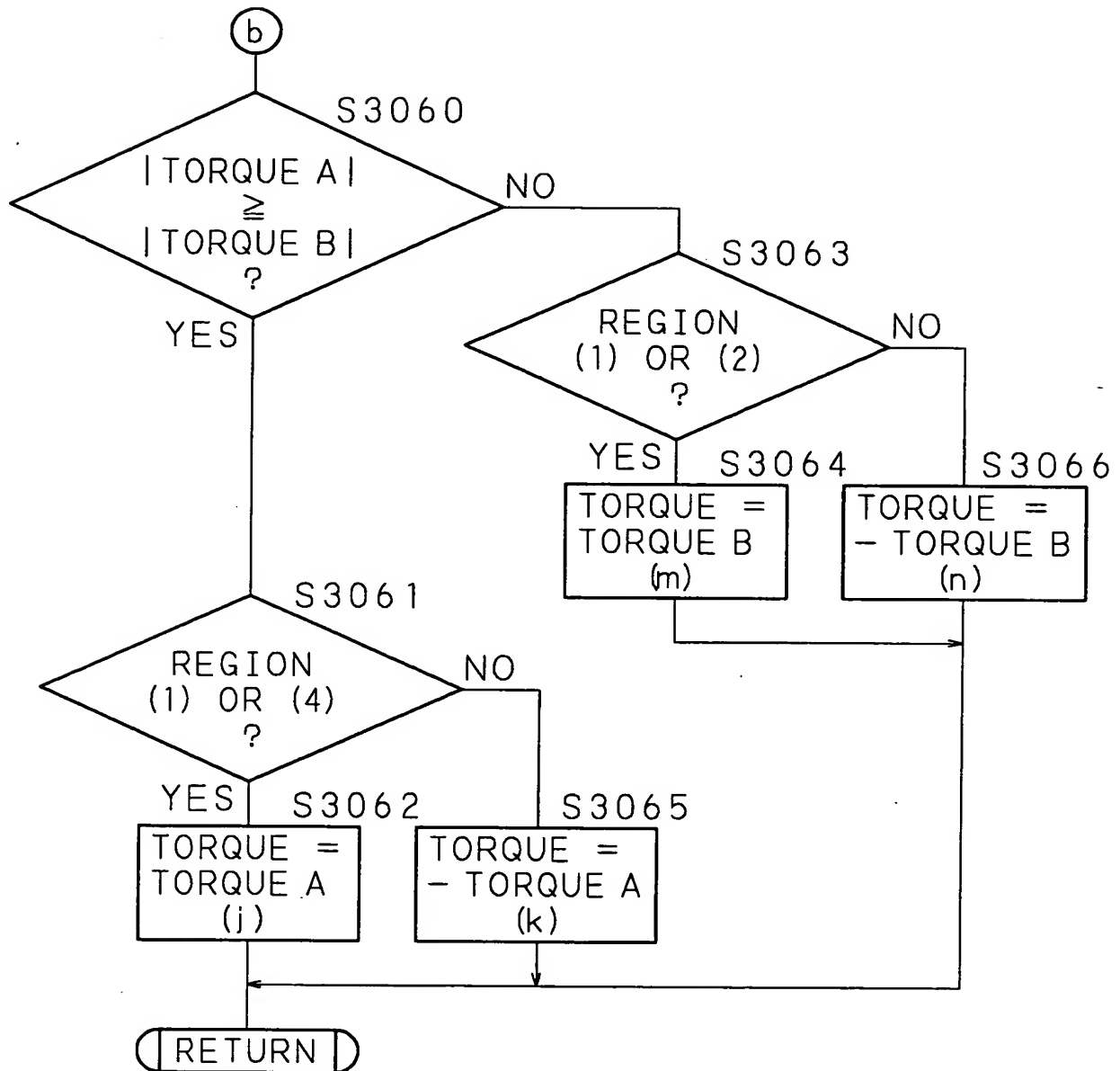
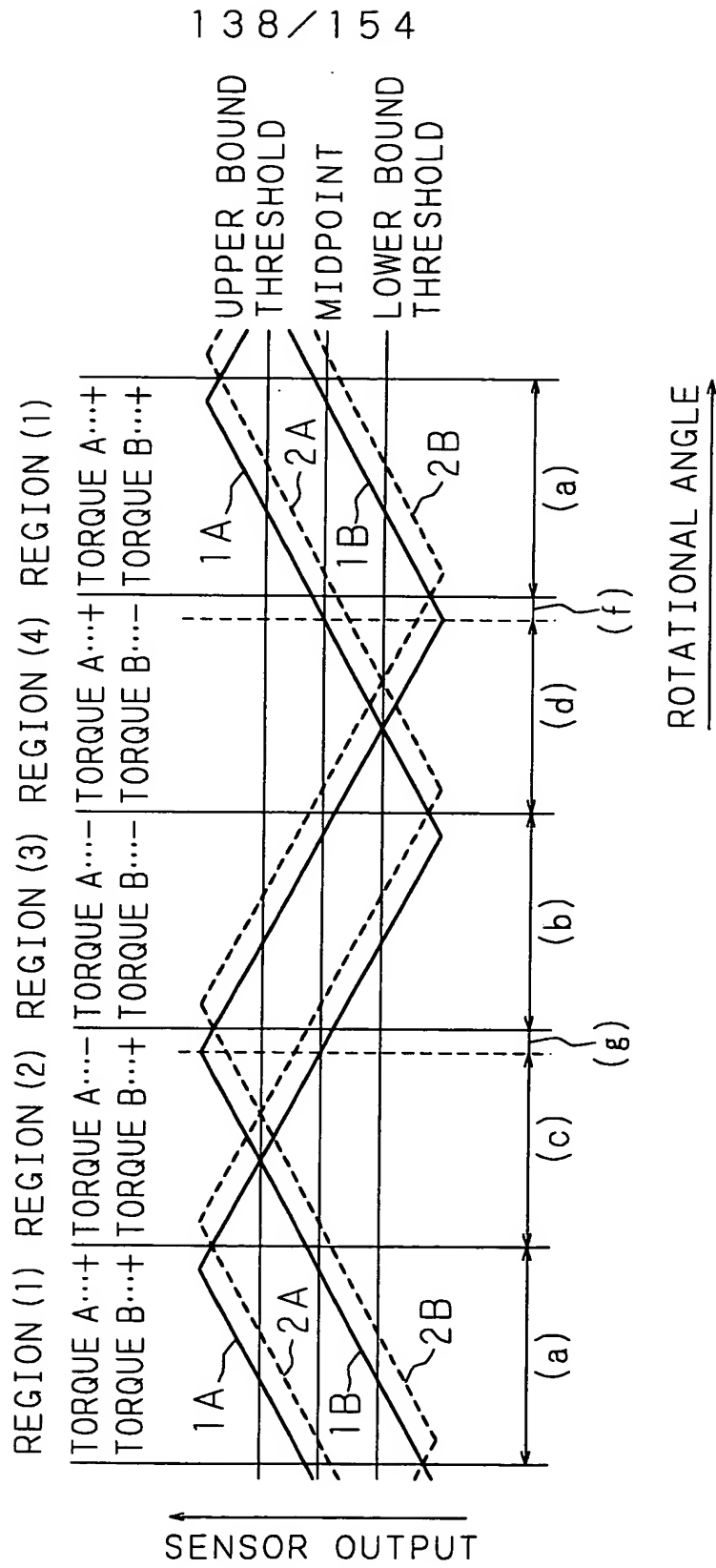


FIG. 135



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FIG. 136

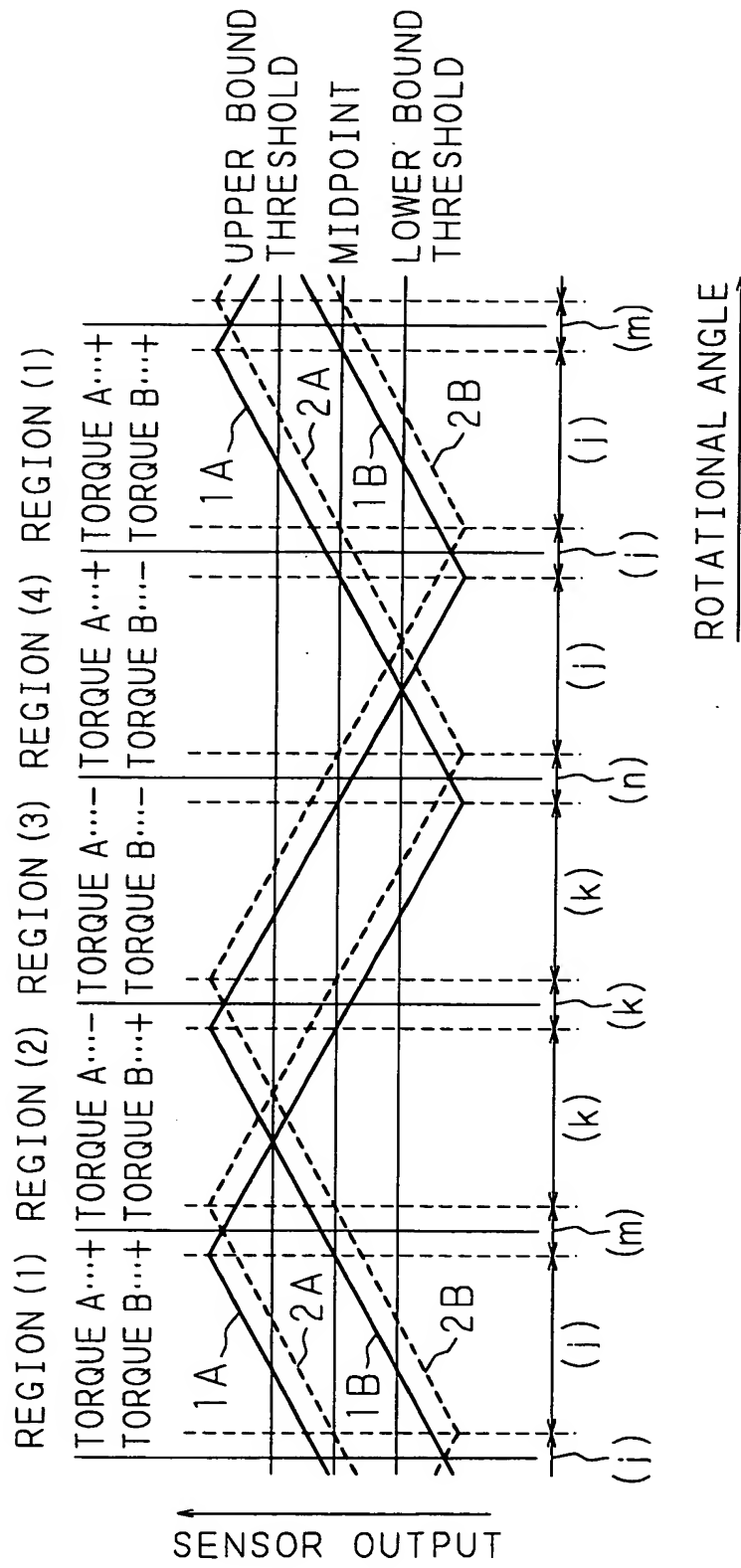


FIG. 137

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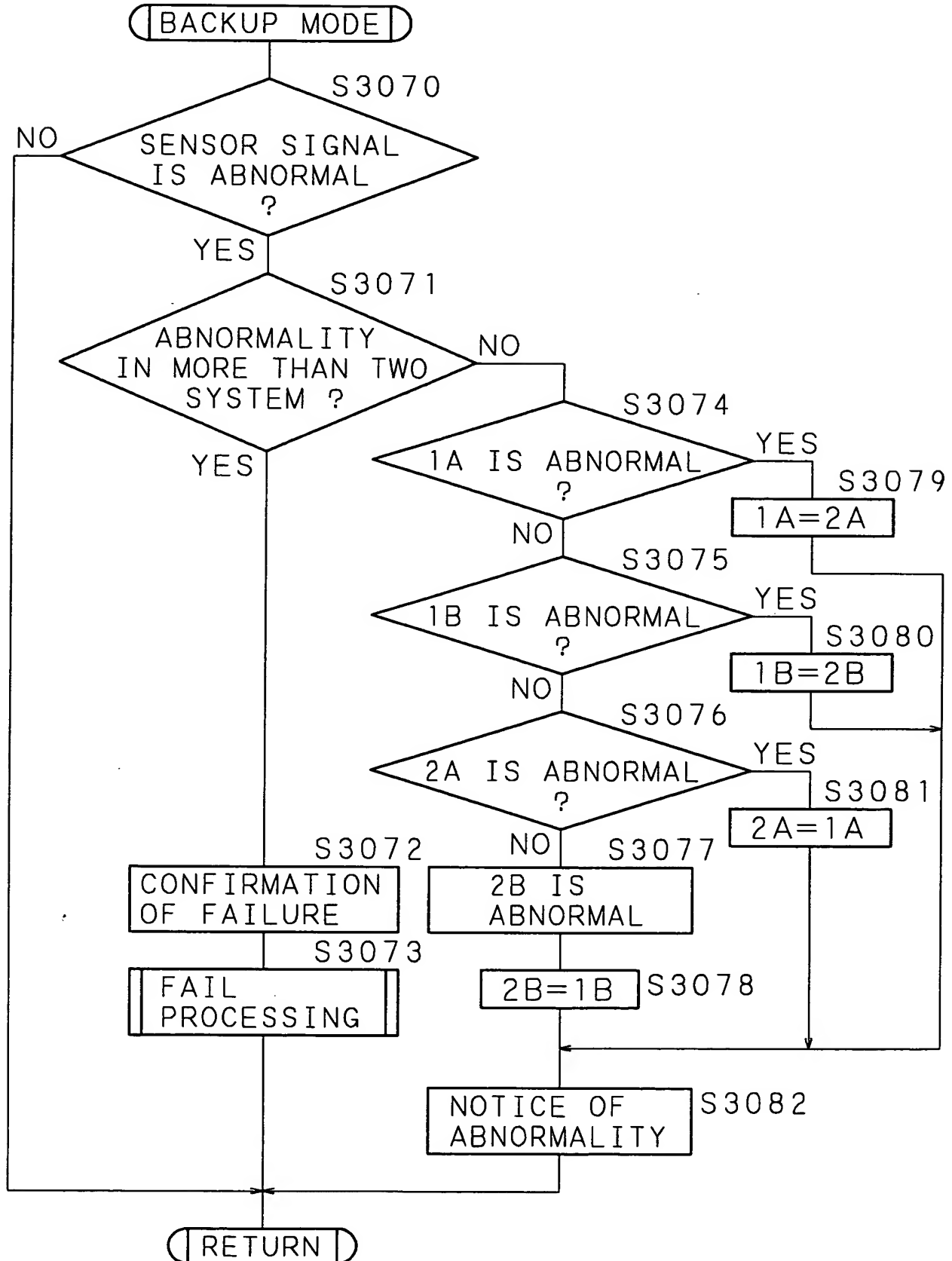




FIG. 138

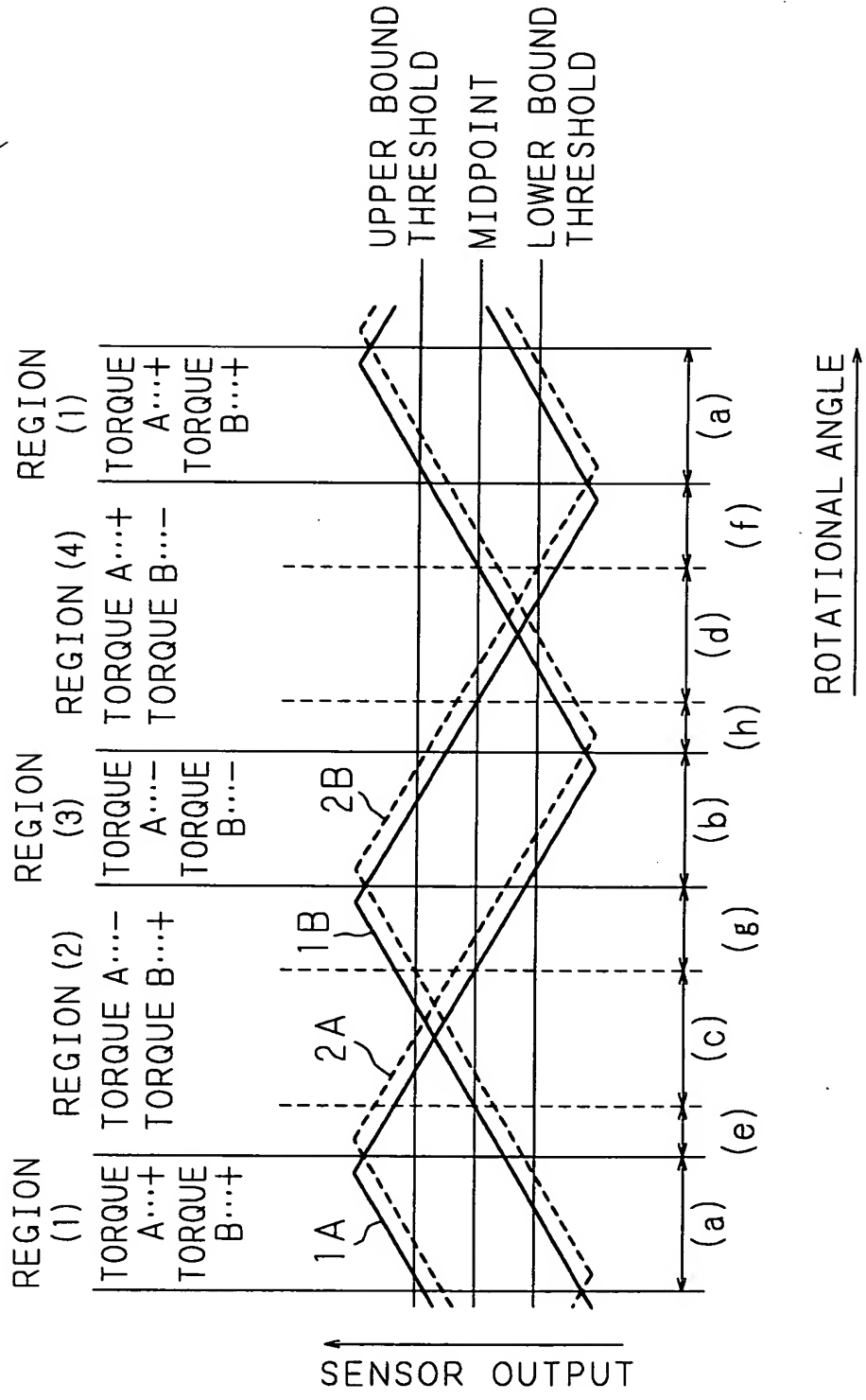
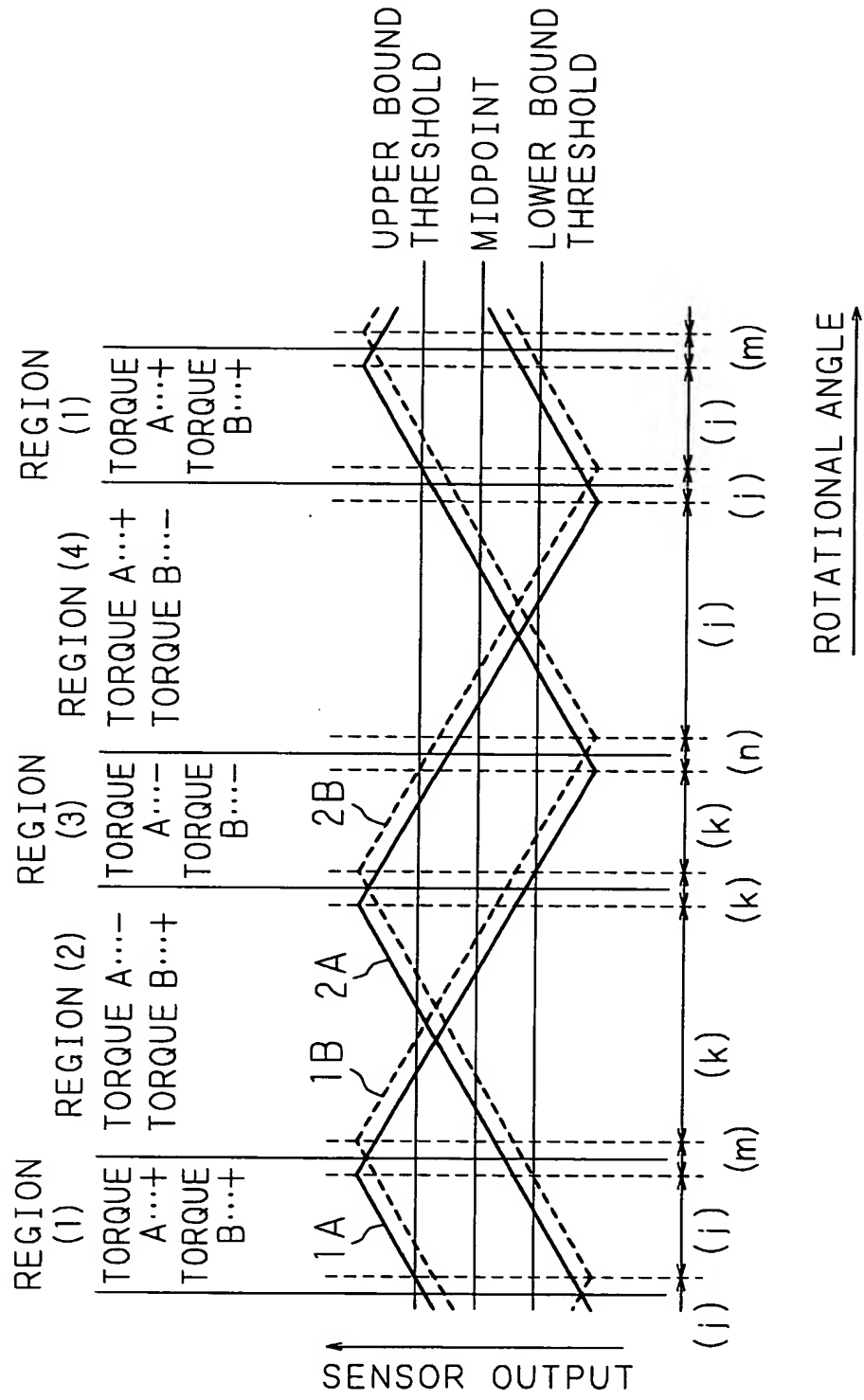
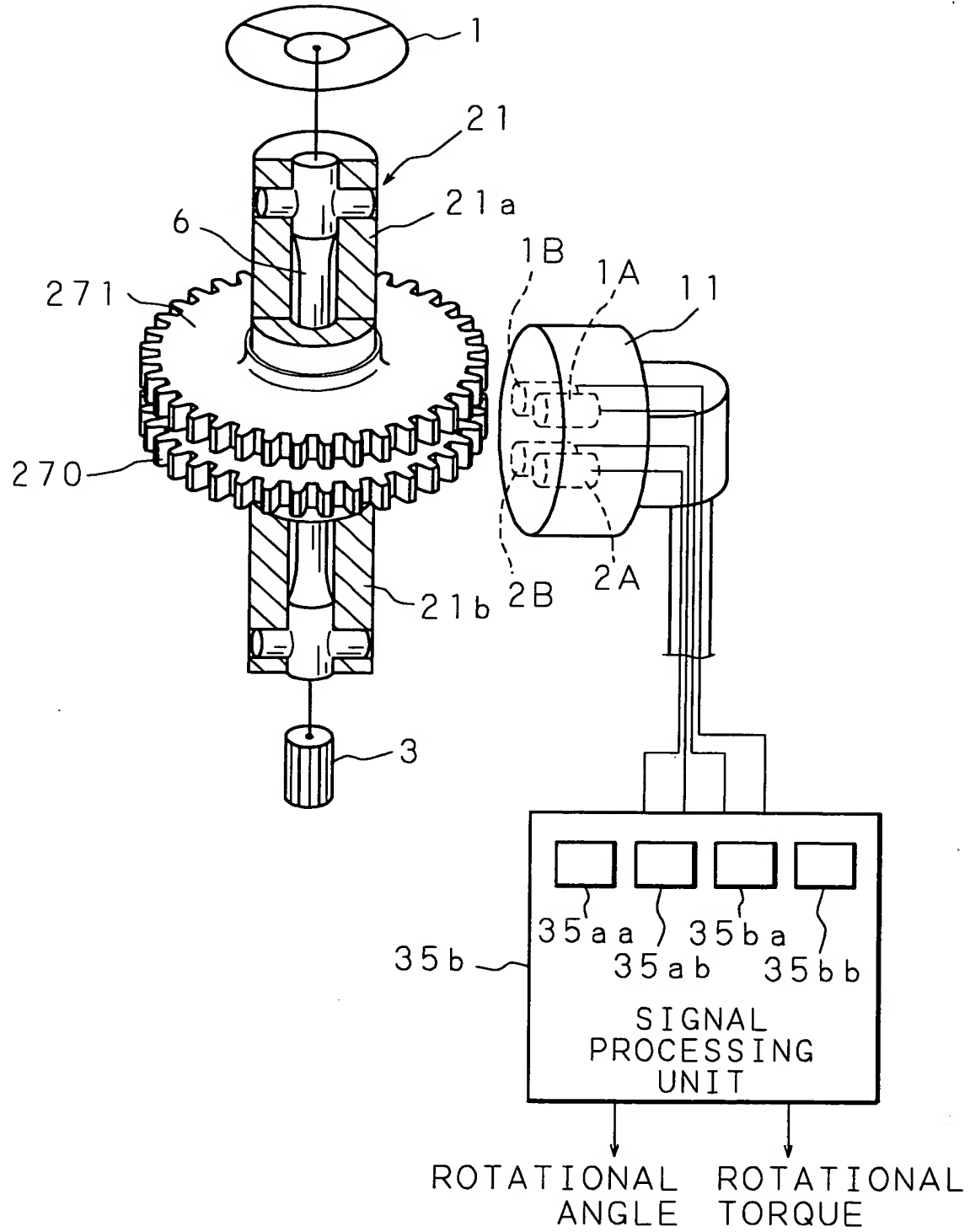


FIG. 139



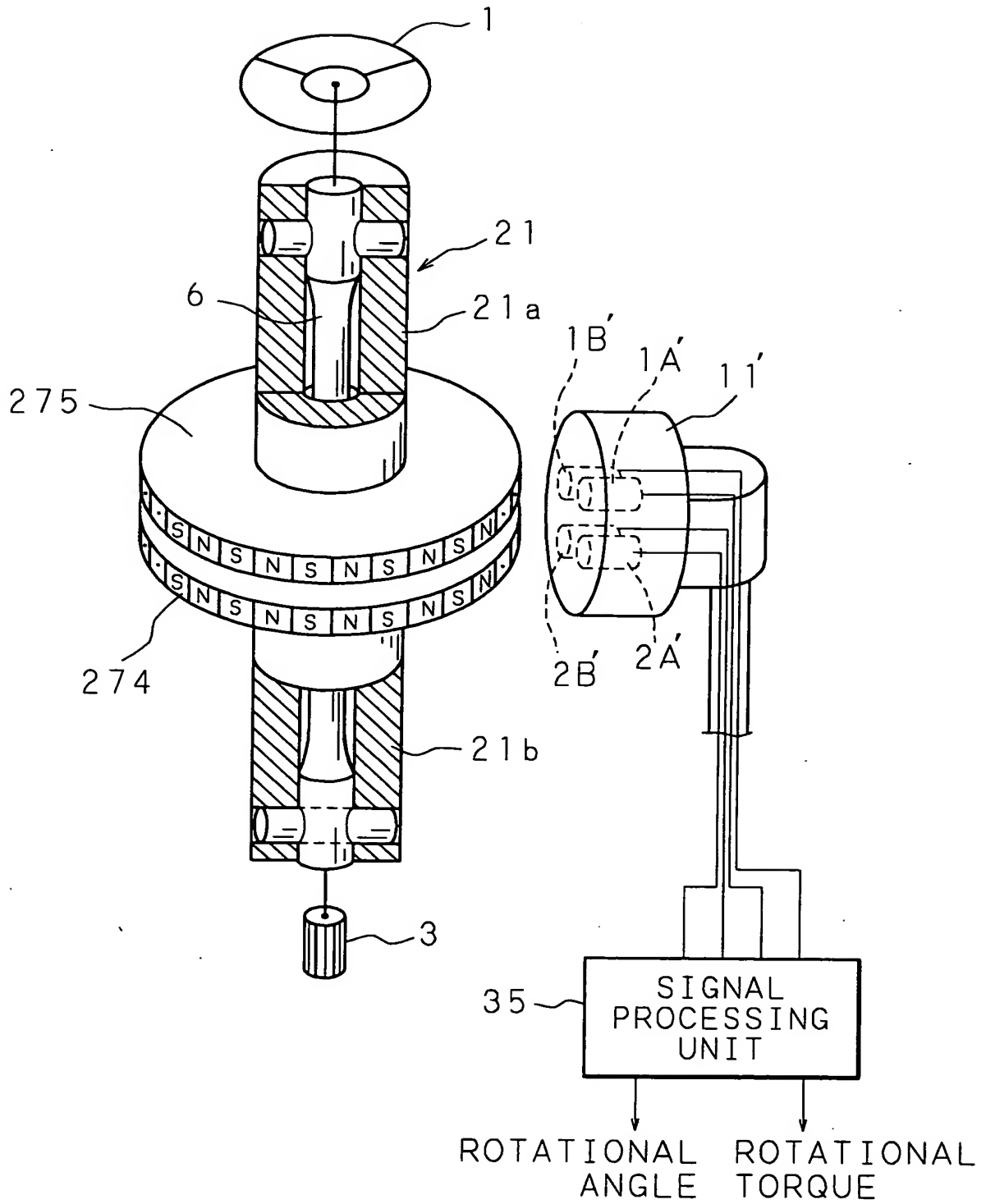
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FIG. 140



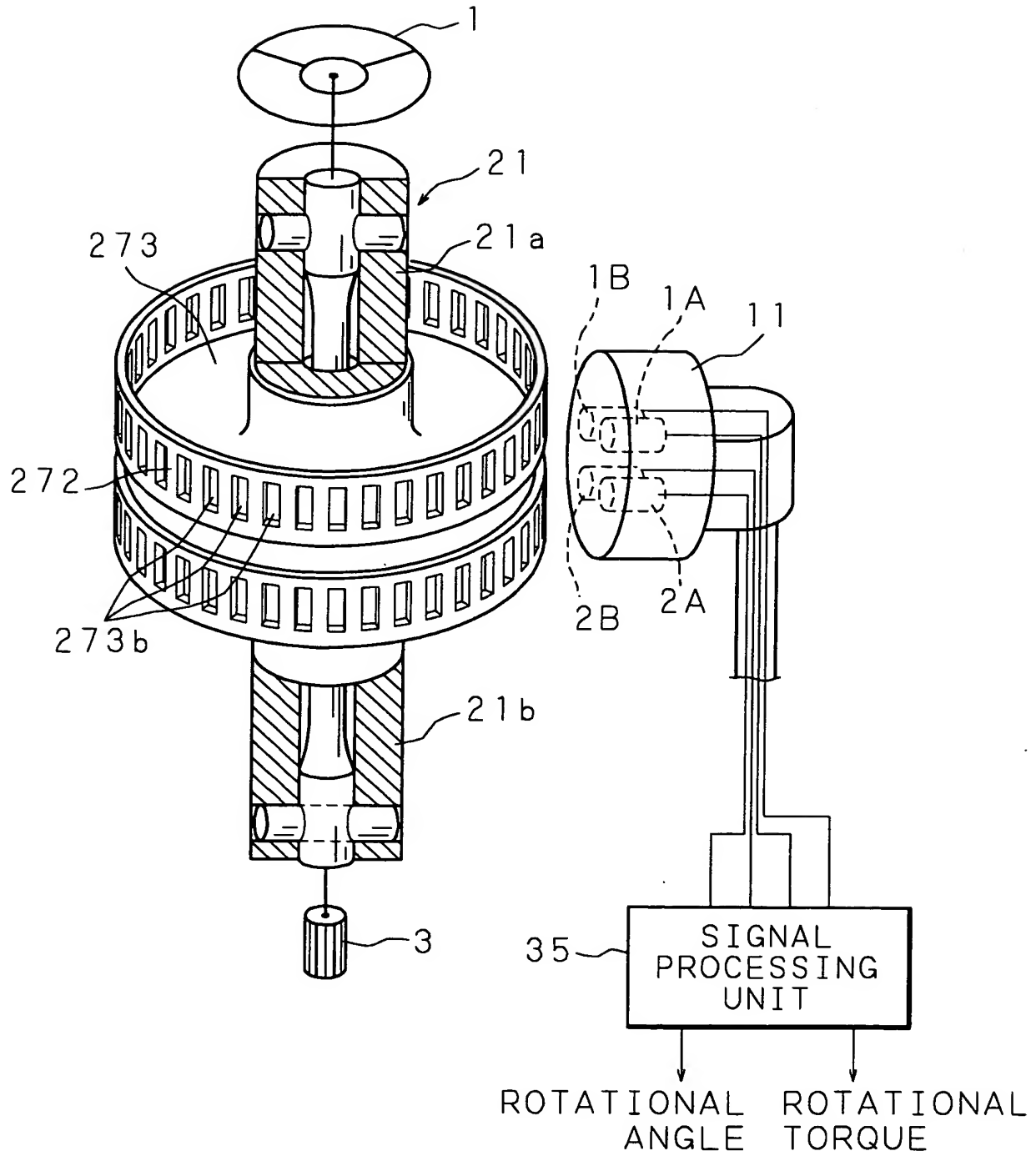
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FIG. 141



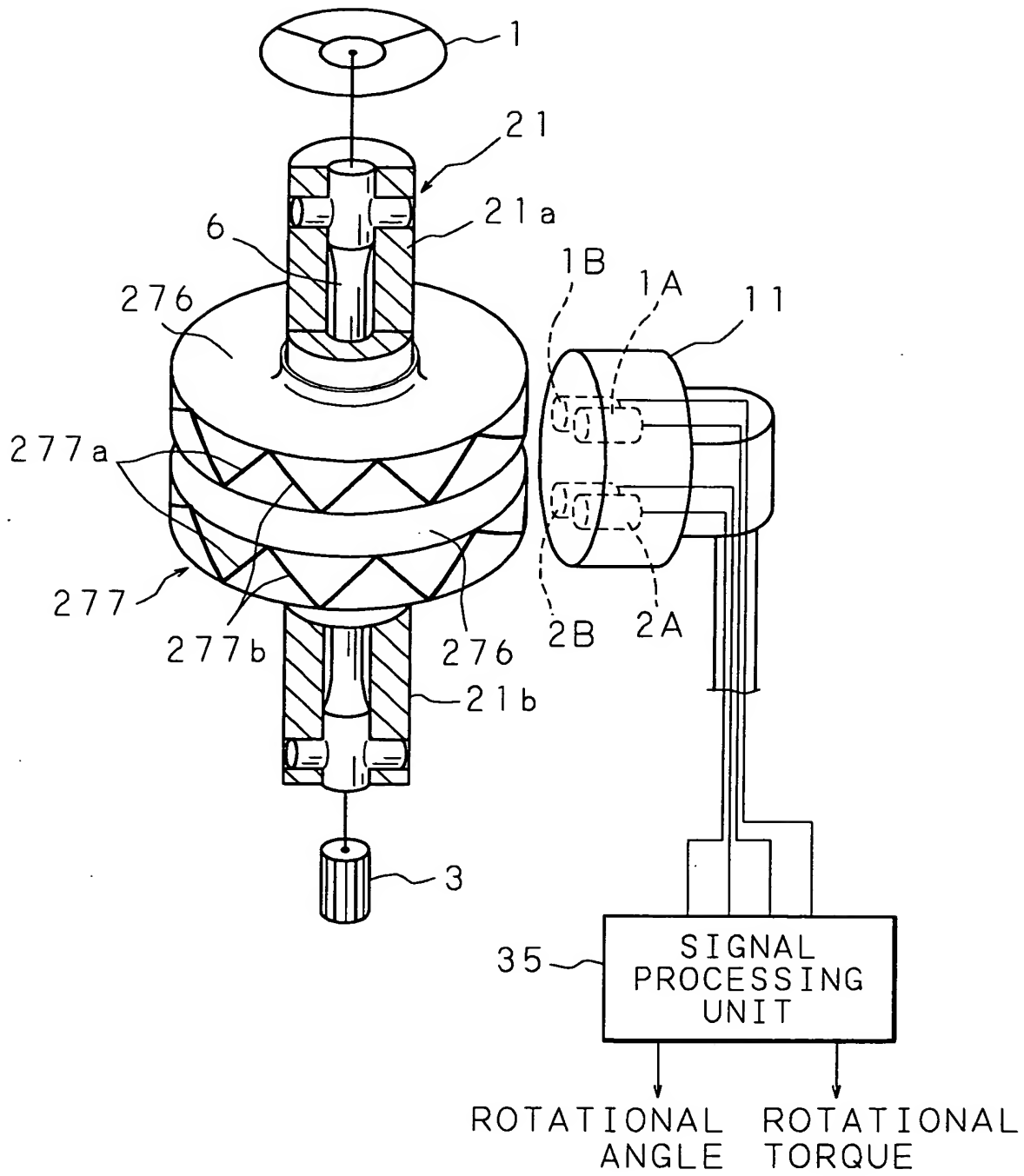
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FIG. 142



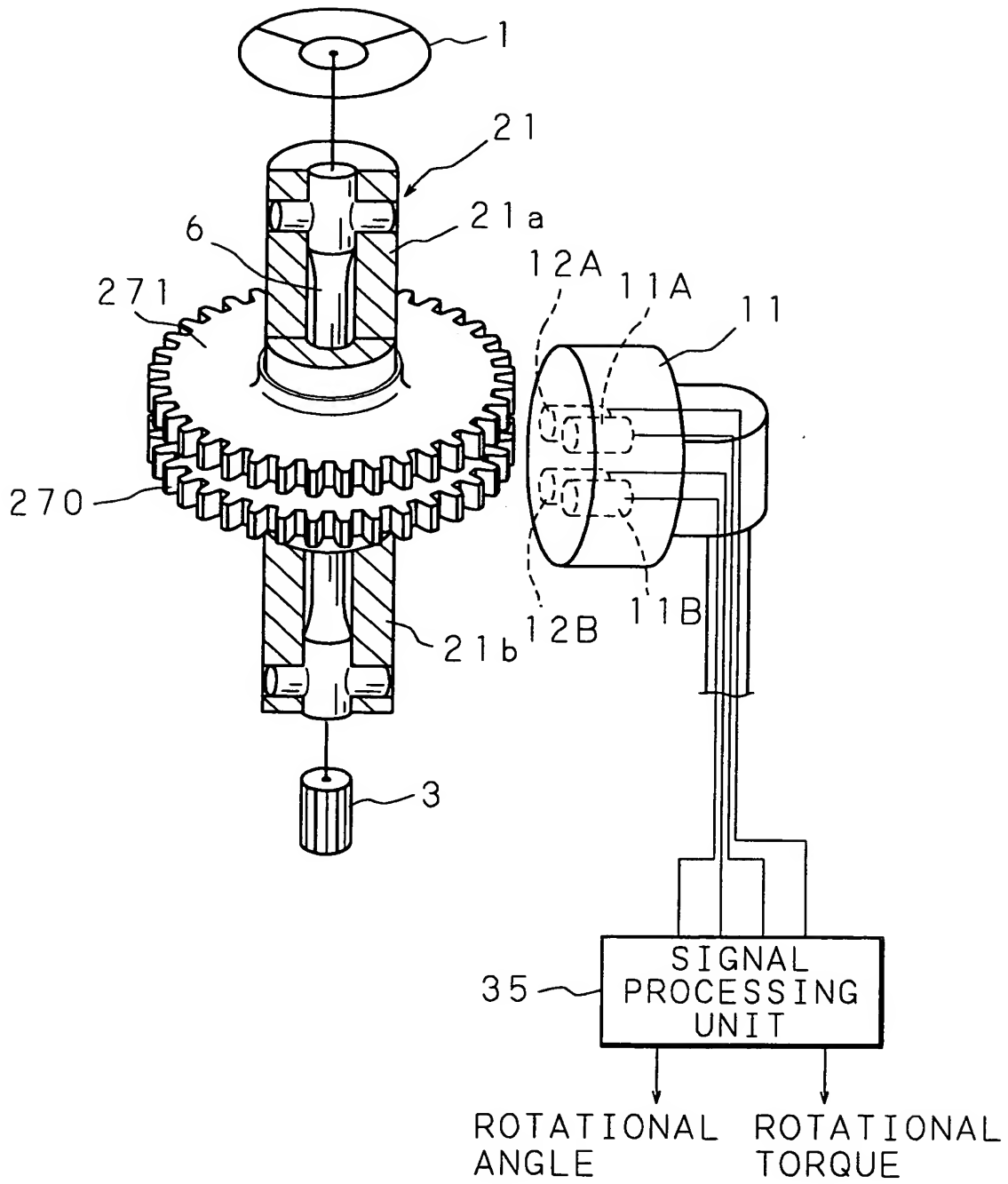
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FIG. 143



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FIG. 144



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FIG. 145

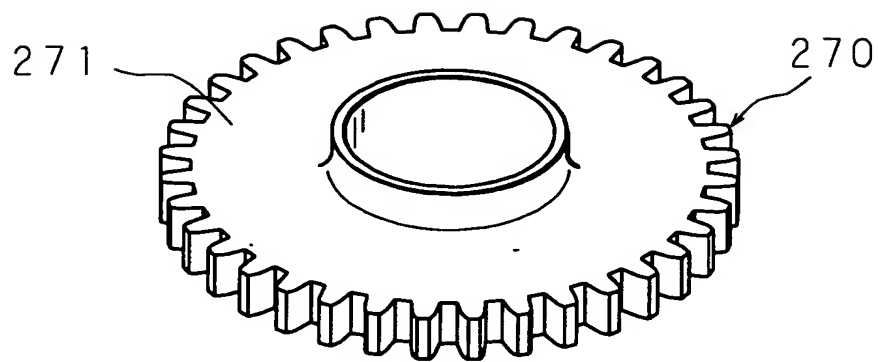
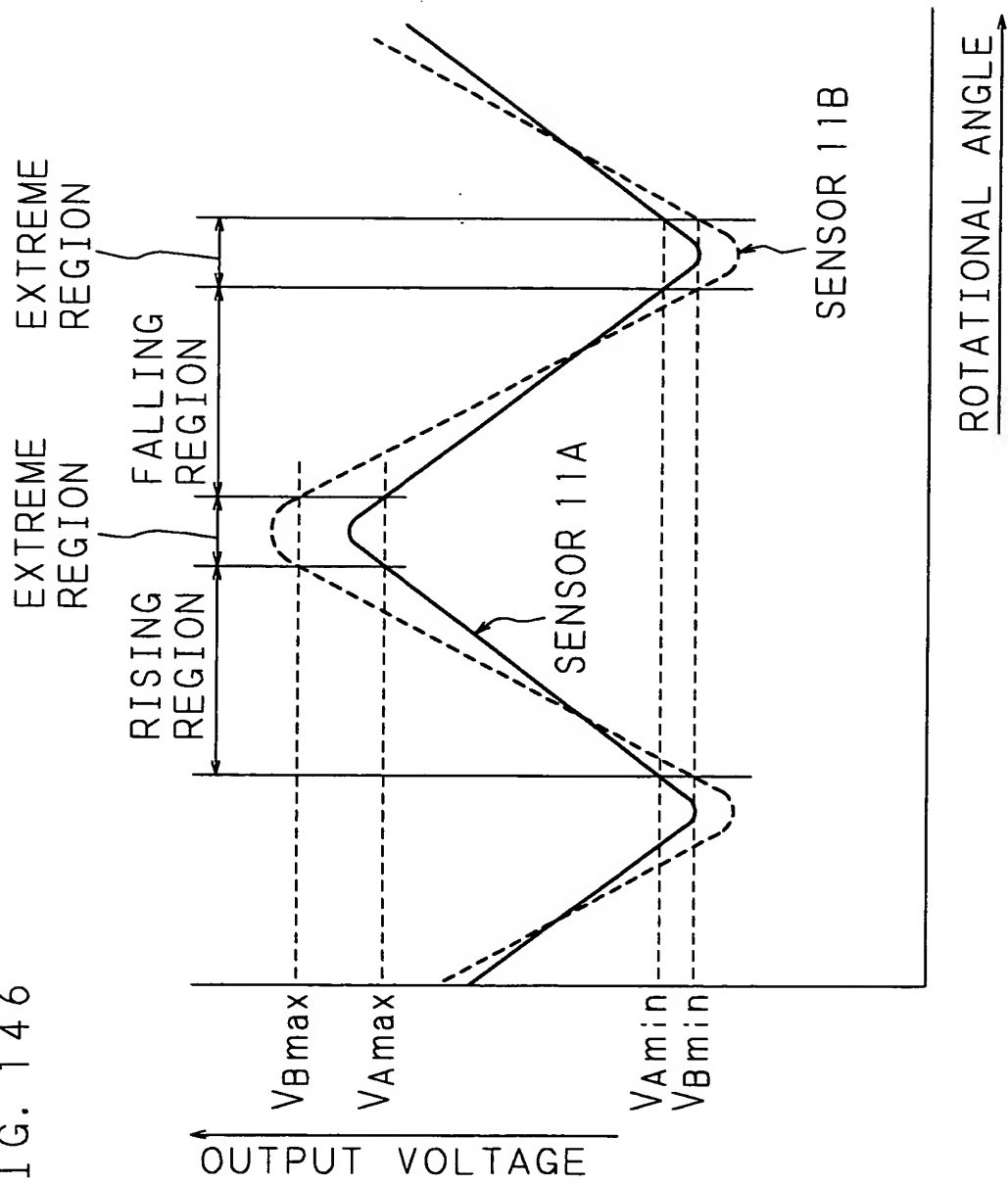


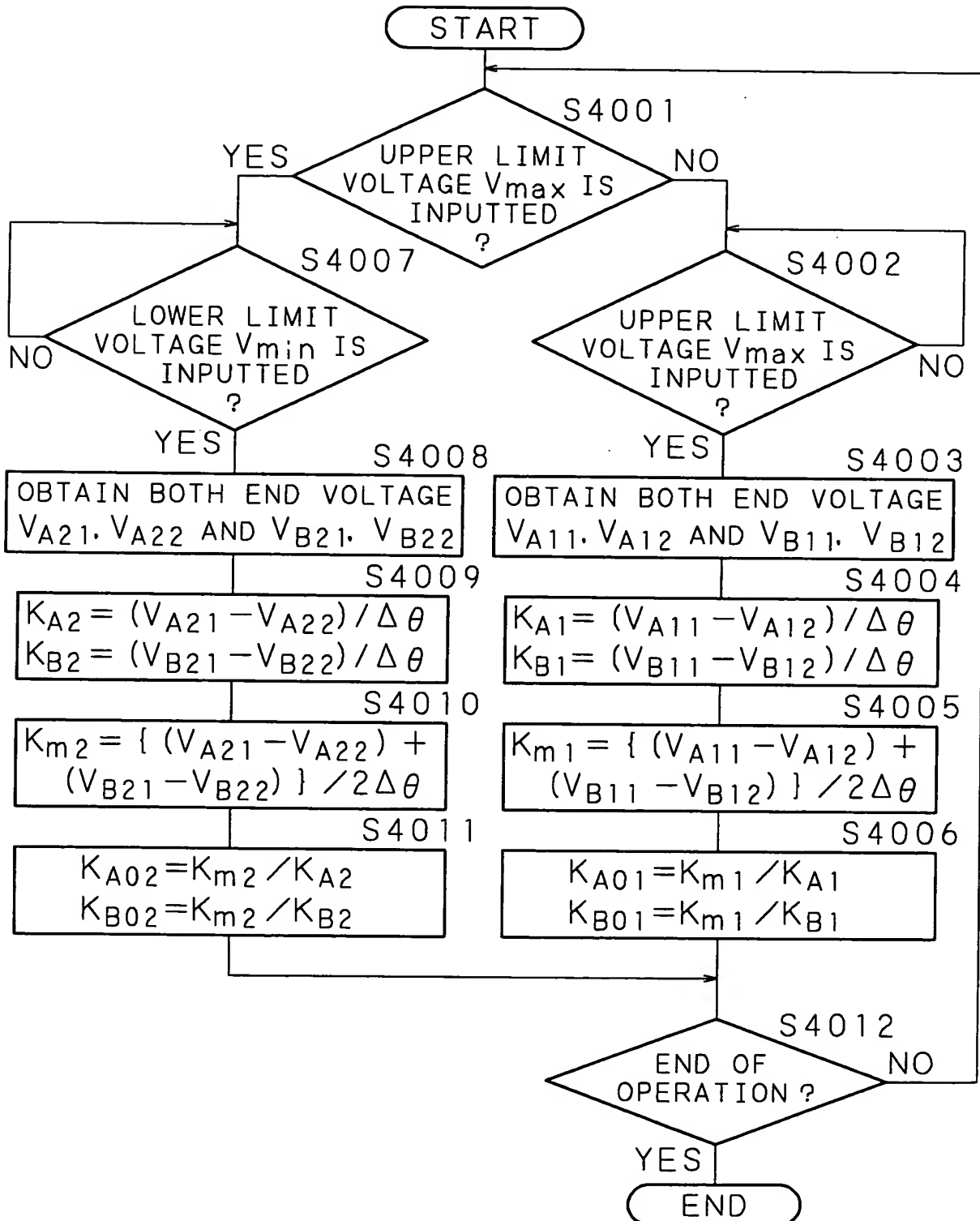


FIG. 146



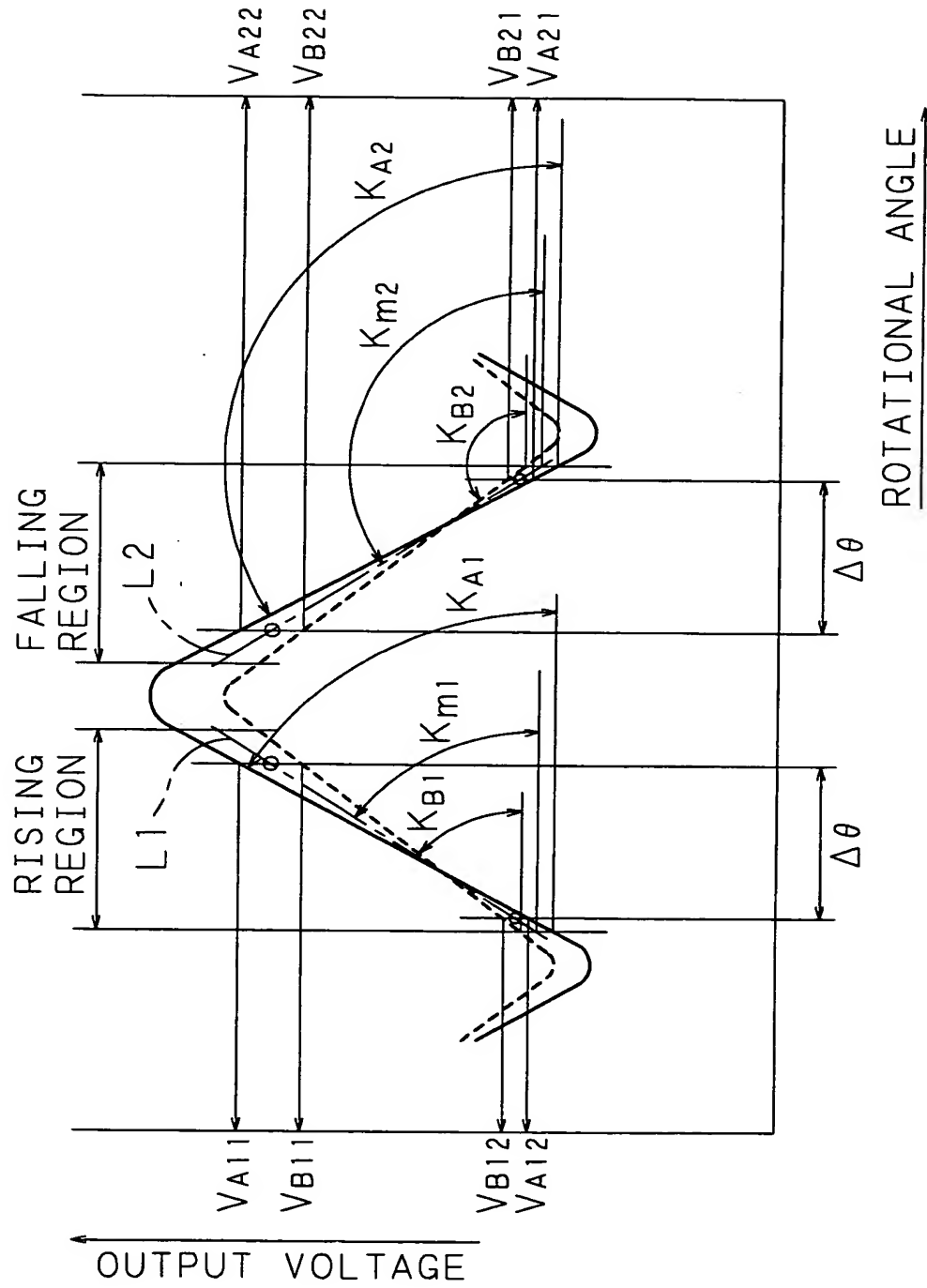
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FIG. 147



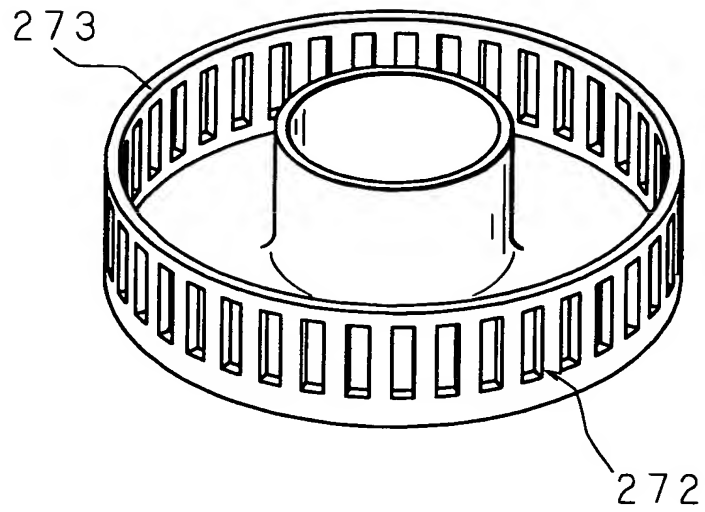
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FIG. 148



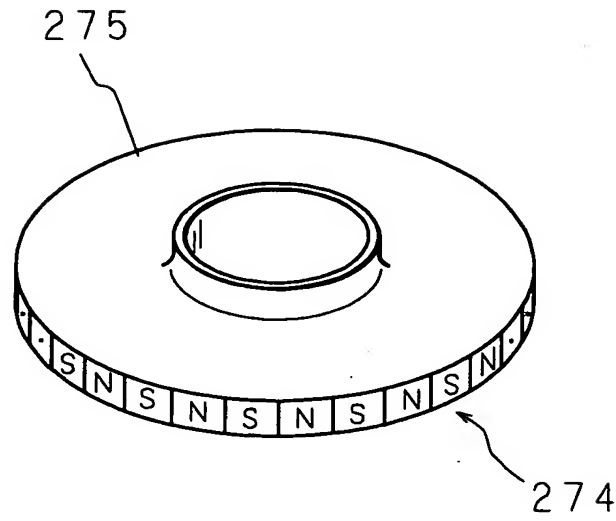
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FIG. 149



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FIG. 150



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FIG. 151

